



**Texas A&M University
Department of Electrical Engineering**

ELEN 654

VLSI System Design

**Prepared by
Dr. Gwan Choi
Yoon Yang**

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Section Wise Lab Class Details

Section	Instructor	T.A.	Place	Day/Time
654-600	Dr. G.Choi	Yoon Yang	Zach 213A	M 9:00AM-11:50AM
654-601	Dr. G.Choi	Yoon Yang	Zach 213A	F 03:00PM-5:50PM

Lab Teaching Assistant Details

Name	Offc Hrs	Email	Phone	Office Add
Yoon Yang	M 2:00PM-3:00PM or by Appointment	yoonseoky@gmail.com	949-241-2452	332F WERC

Tentative Schedule of labs

Lab#	Lab Content	Due In	Deliverables
1	Intro. to 654 Lab	1 Week	No
2	Intro. to RTL Design	1 Week	No
3	Intro. to H.264 Video Decoder Design	1 Week	No
4	H.264 Decoder Design (Details will be shown in the lab website)	~10 Weeks	Project plan, design discussion, mid/final reports, RTL codes, etc.

USEFUL REFERENCES:

Verilog Design Reference:

<http://www.asic-world.com/verilog/index.html>

http://www.sutherland-hdl.com/online_verilog_ref_guide/vlog_ref_top.html

ASIC Design Flow Tutorial Using Synopsys Tools:

<http://userwww.sfsu.edu/~necrc/files/synopsys%20tutorials/ASIC%20Design%20Flow%20Tutorial.pdf>

MISCELLANEOUS INFO & GRADING POLICIES

1. Sign the Laboratory Safety Agreement. If you haven't signed it in class, go to ZACH 111A to sign it.
2. EE Novell Account: You may need to use this account during periods when you don't have access to lab. If you haven't got one, apply for it in ZACH 100M.
3. Check your NEO Email Account and Lab website regularly! Lab notifications and additional materials (if necessary) will be sent to you through the Neo emails and the lab website (I will let you know when I build the website).
4. The student is responsible to submit his reports on time. Any absence in the lab must be compensated in the same week itself. TAs help can be sought if there is any problem in completing the lab. Get an appointment with the TA to meet in the lab class to discuss any implementation related queries. For design related issues, use the TAs office hours.
5. Grading Policies: All labs are due exactly before the corresponding lab in the week it is due. Any delays in lab submission with result in the following penalties:
 - One day late submission will incur a penalty of **25%**
 - Two day late submission will incur a penalty of **50%**
 - Three day late submission will incur a penalty of **80%**
 - Four days or more will lead to **100%** penalty. You may still turn in your lab report to see where you stand in that particular lab, it is a learning experience nonetheless.

IMPORTANT LAB NOTICE:

Please Keep Checking the Lab Website for Lab Notices.

The Americans with Disabilities Act (ADA) is a federal anti-discrimination statute that provides comprehensive civil rights protection for persons with disabilities. Among other things, this legislation requires that all students with disabilities be guaranteed a learning environment that provides for

reasonable accommodation of their disabilities. If you believe you have a disability requiring an accommodation, please contact the Department of Student Life/Services for Students with Disabilities, in Cain Hall. The phone number is 845-1637.