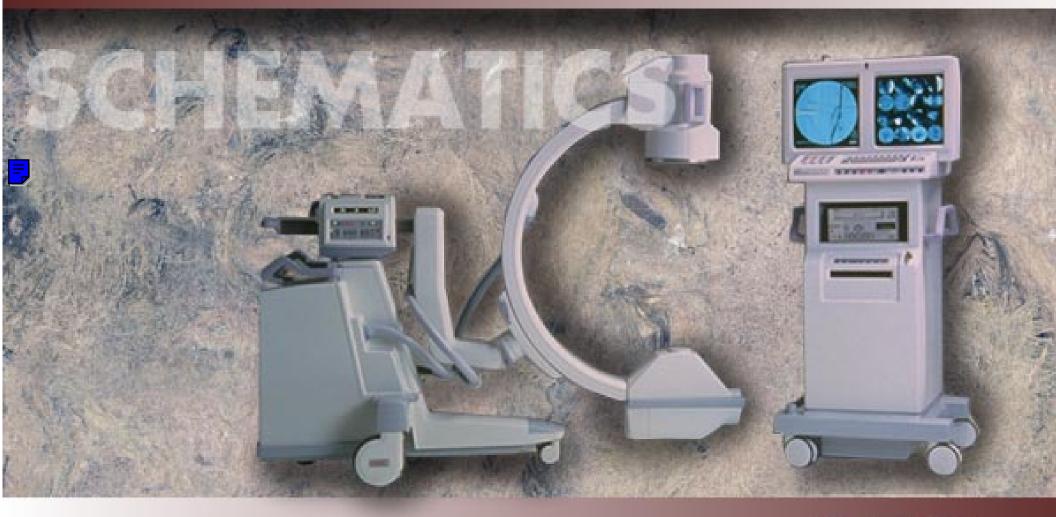
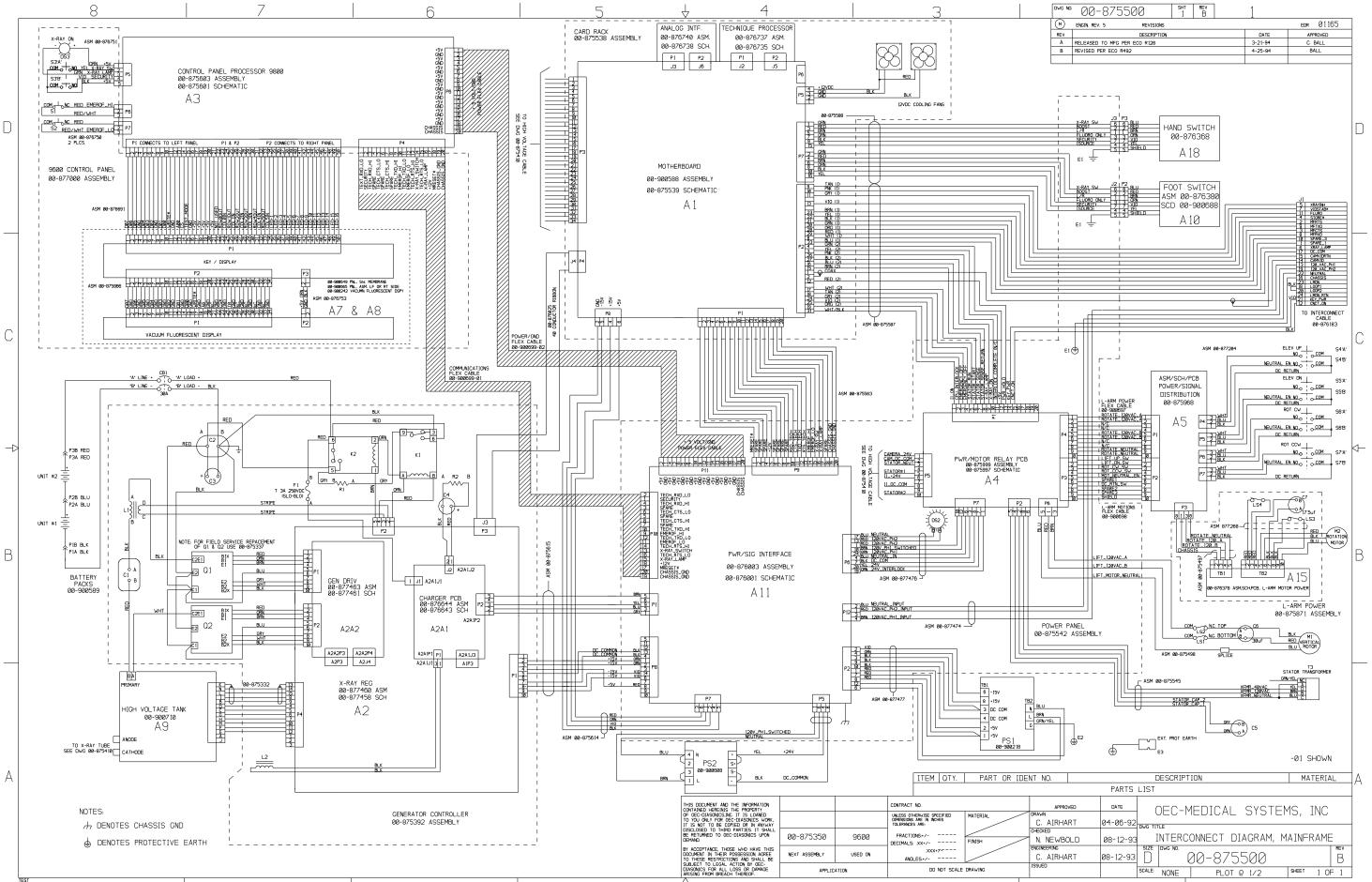
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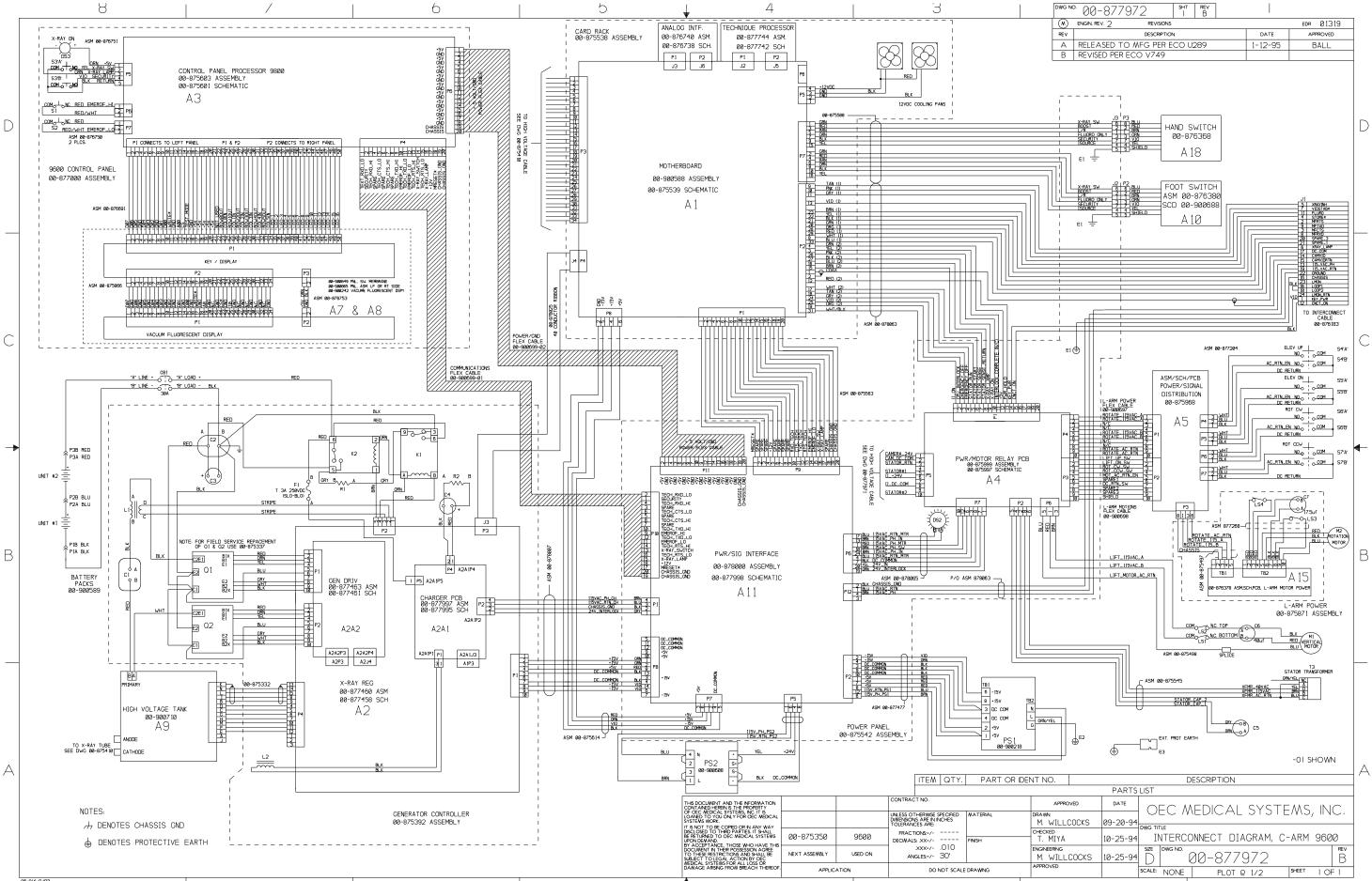


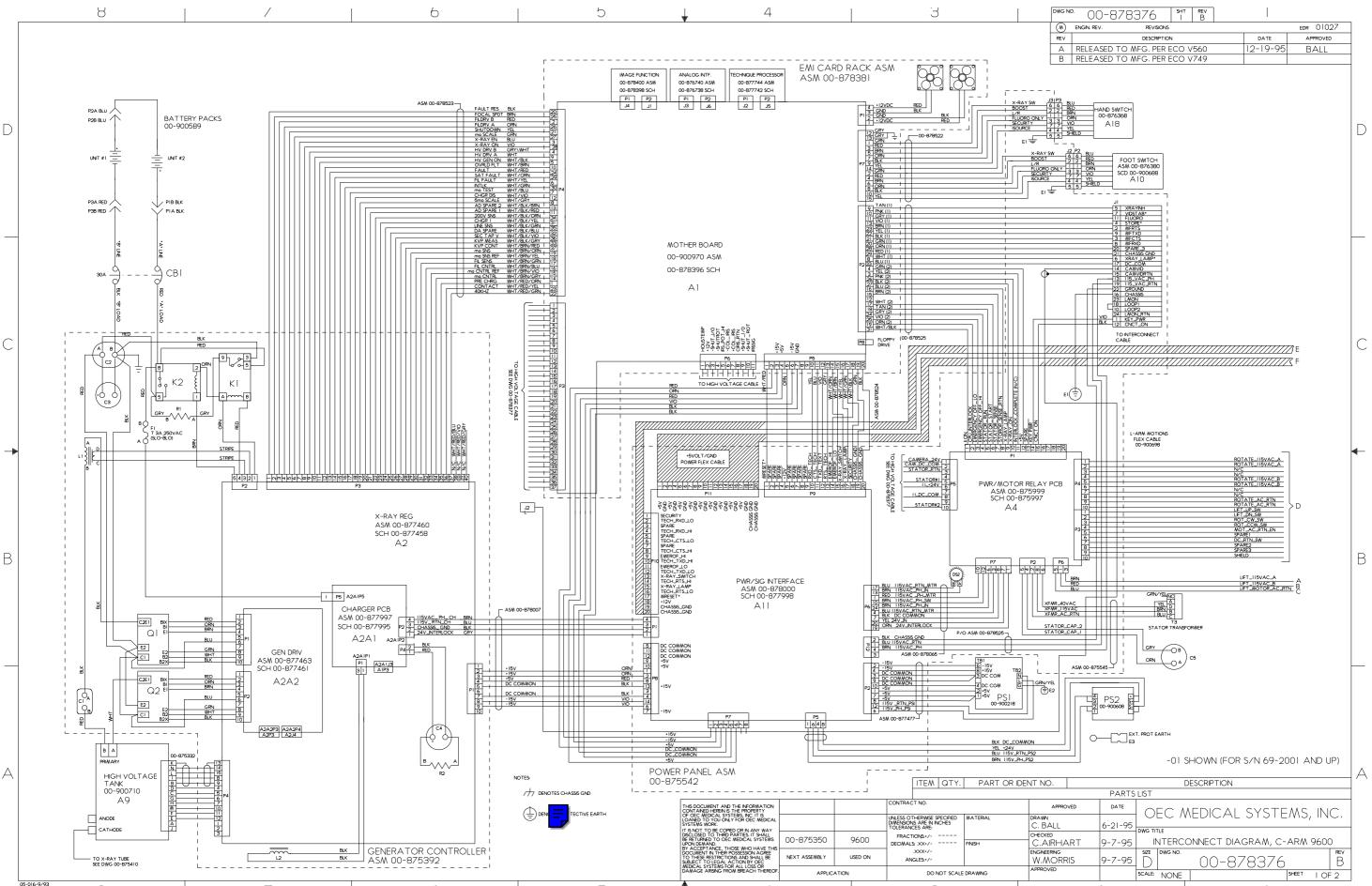


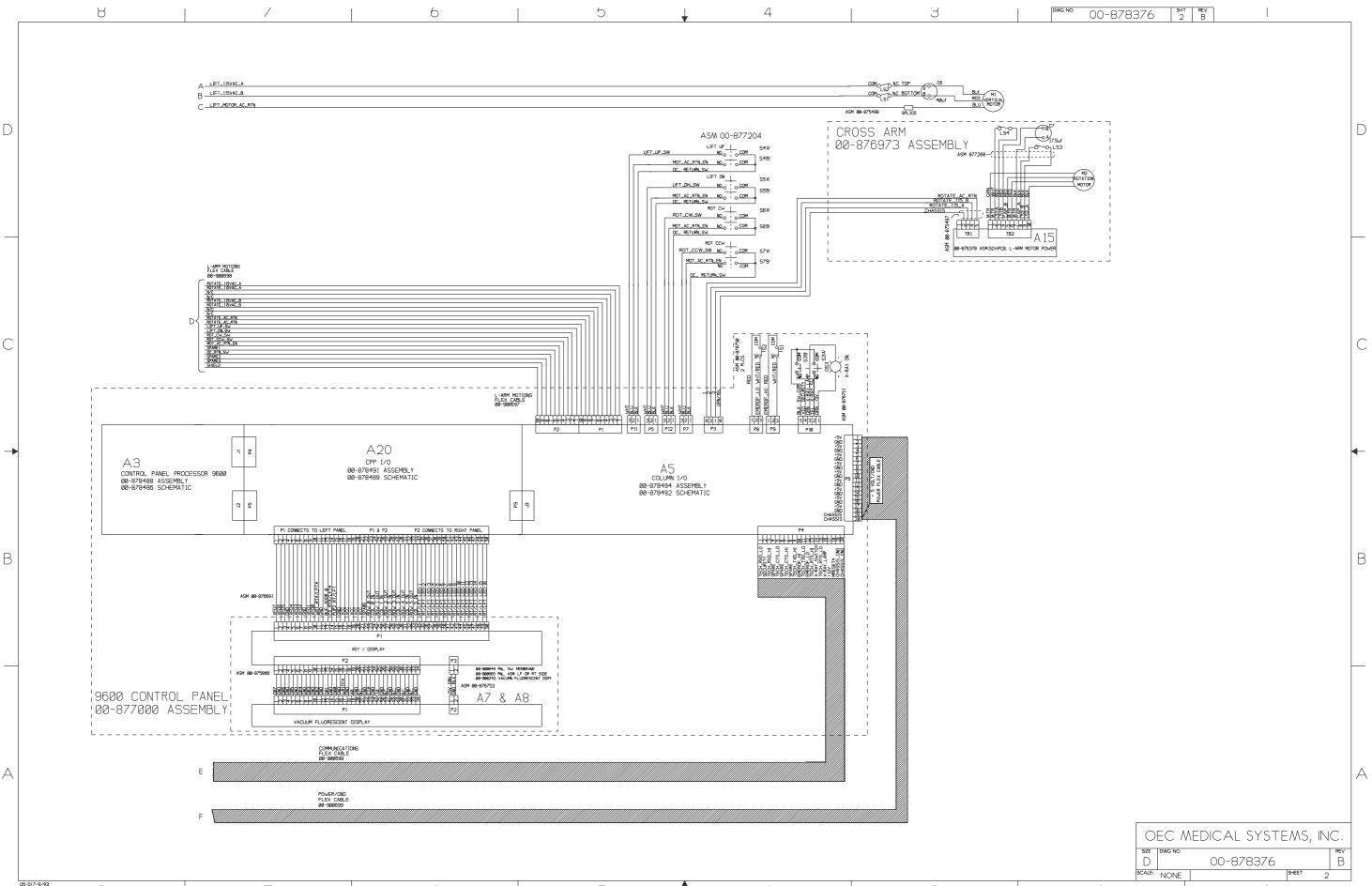


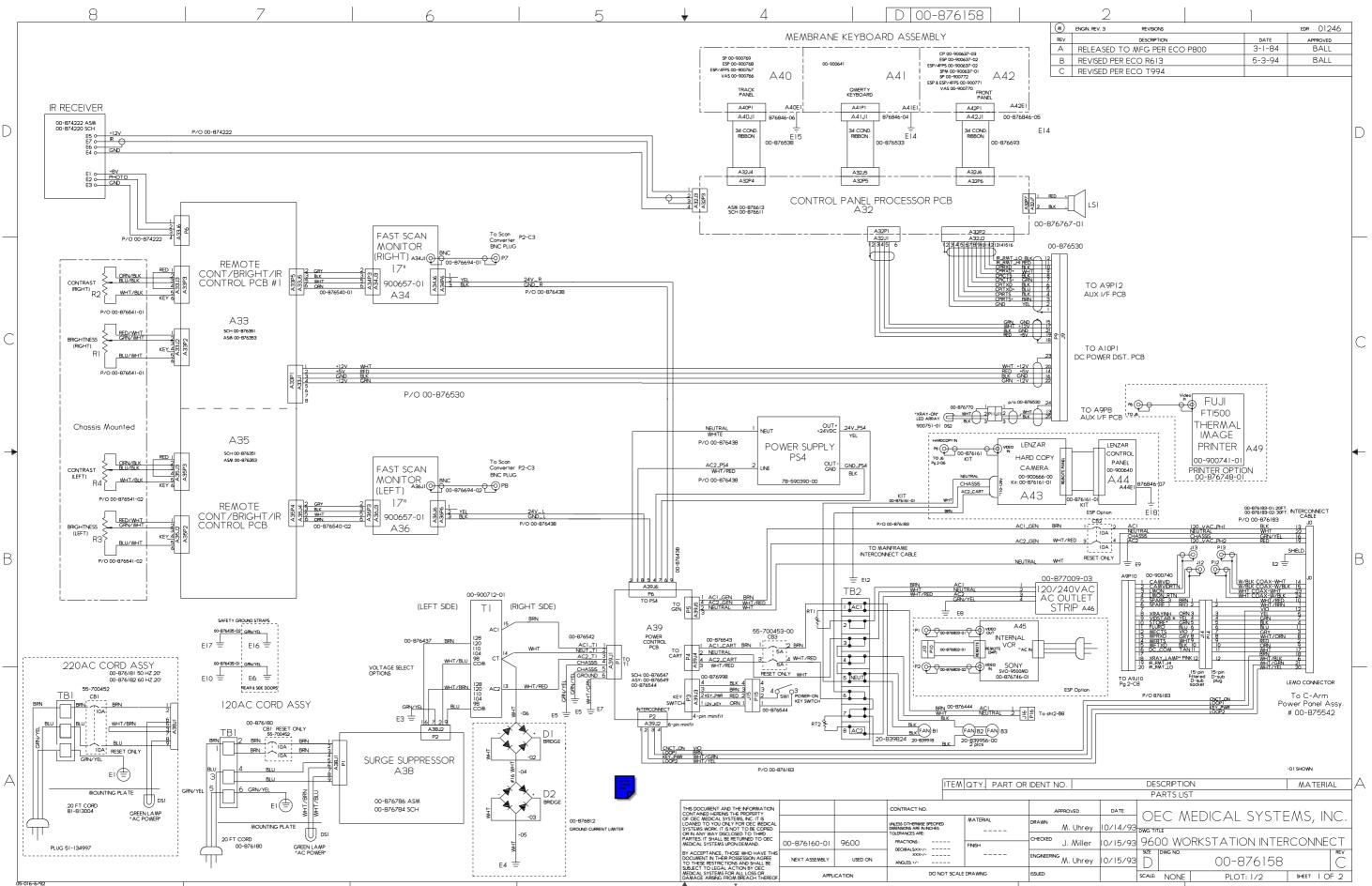
OEC MEDICAL Systems, inc

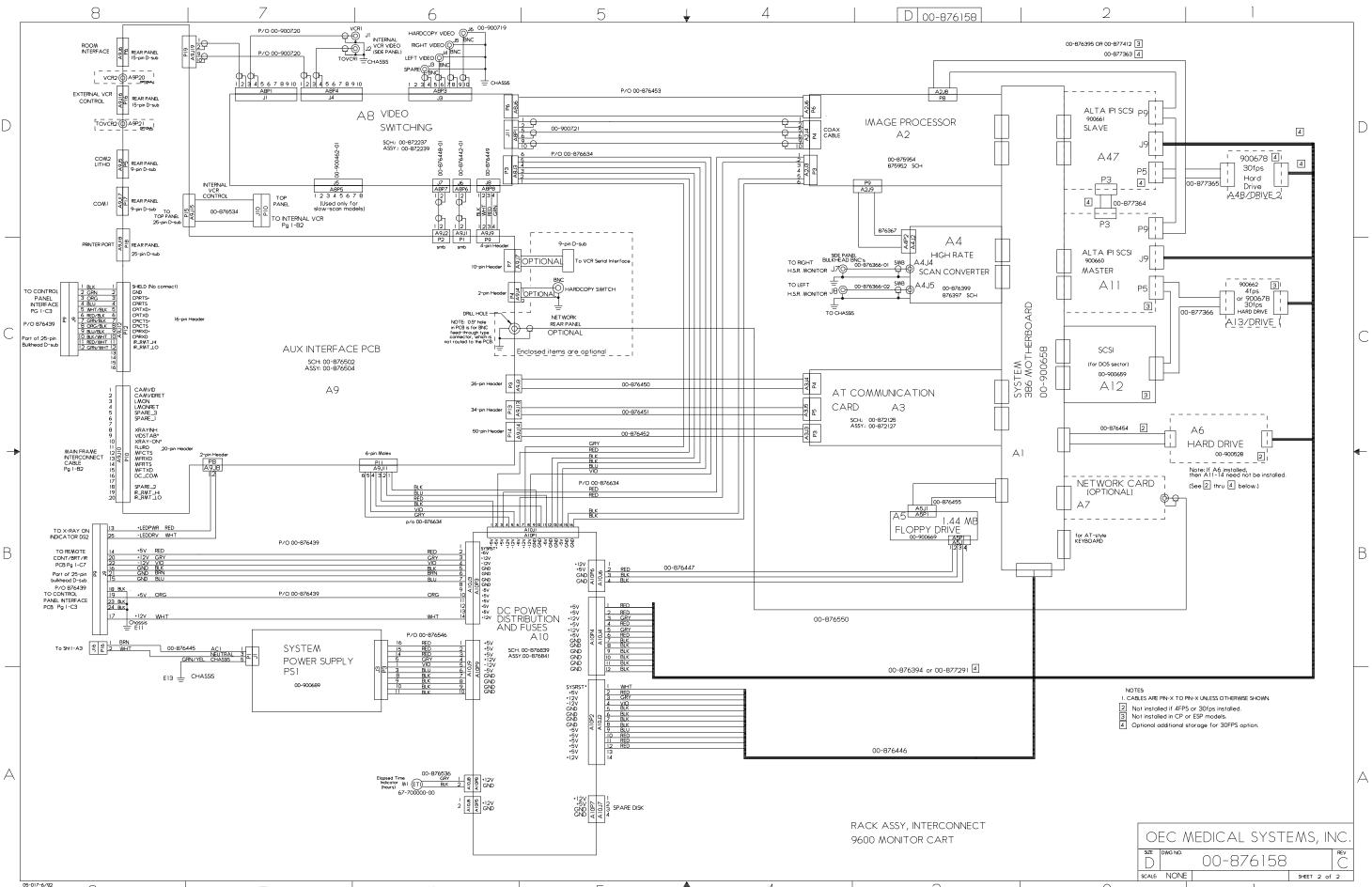


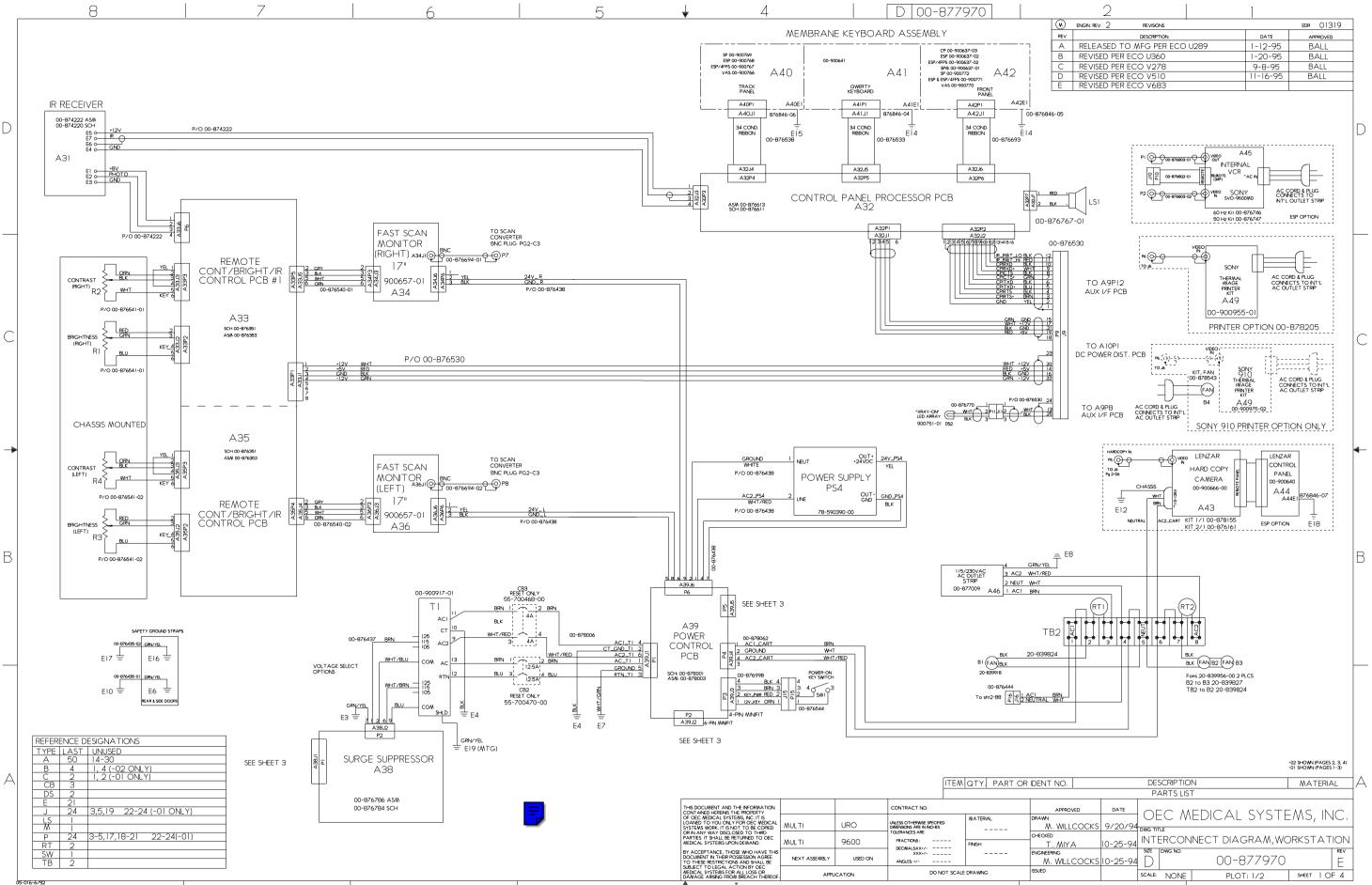


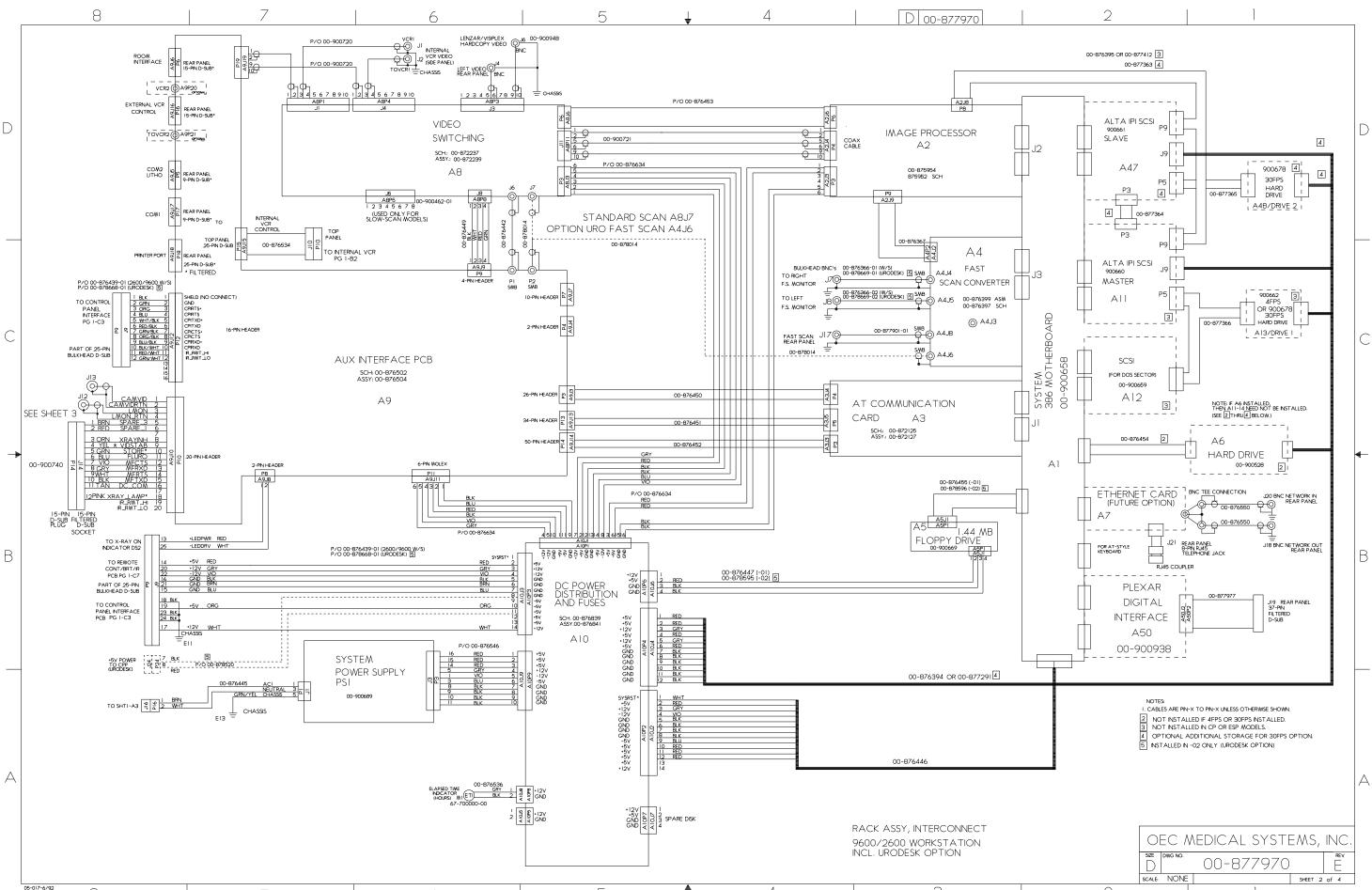


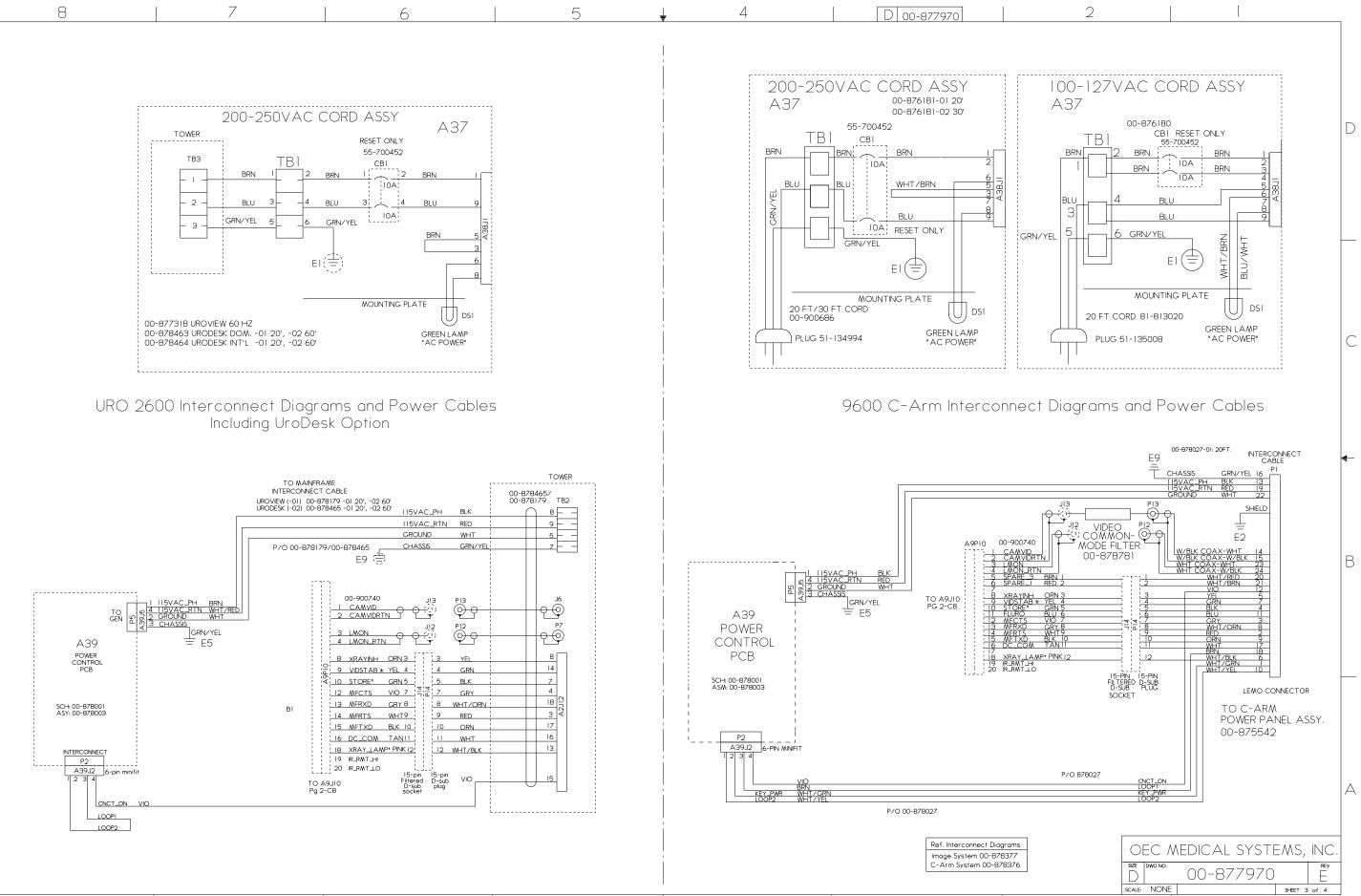








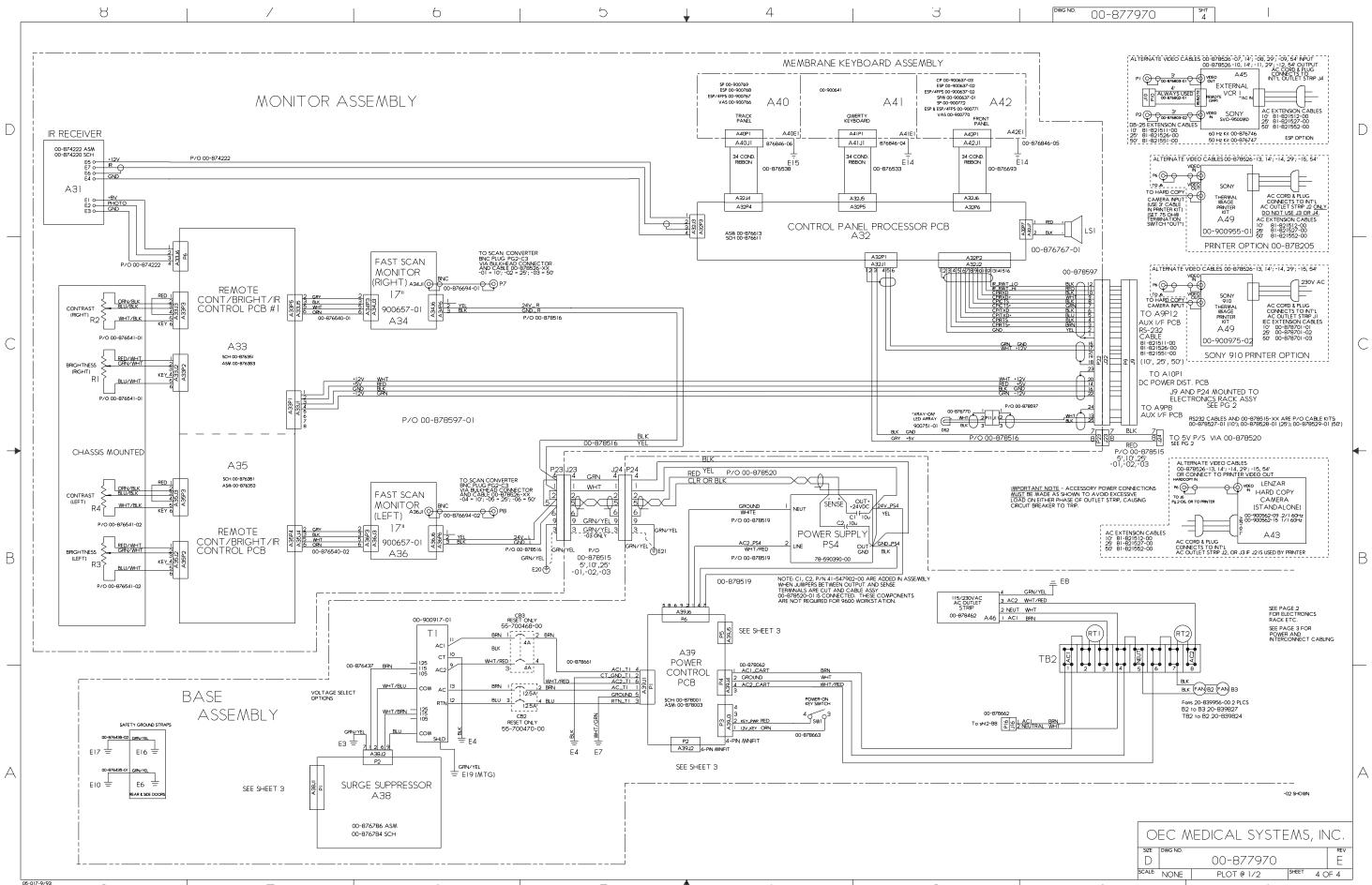


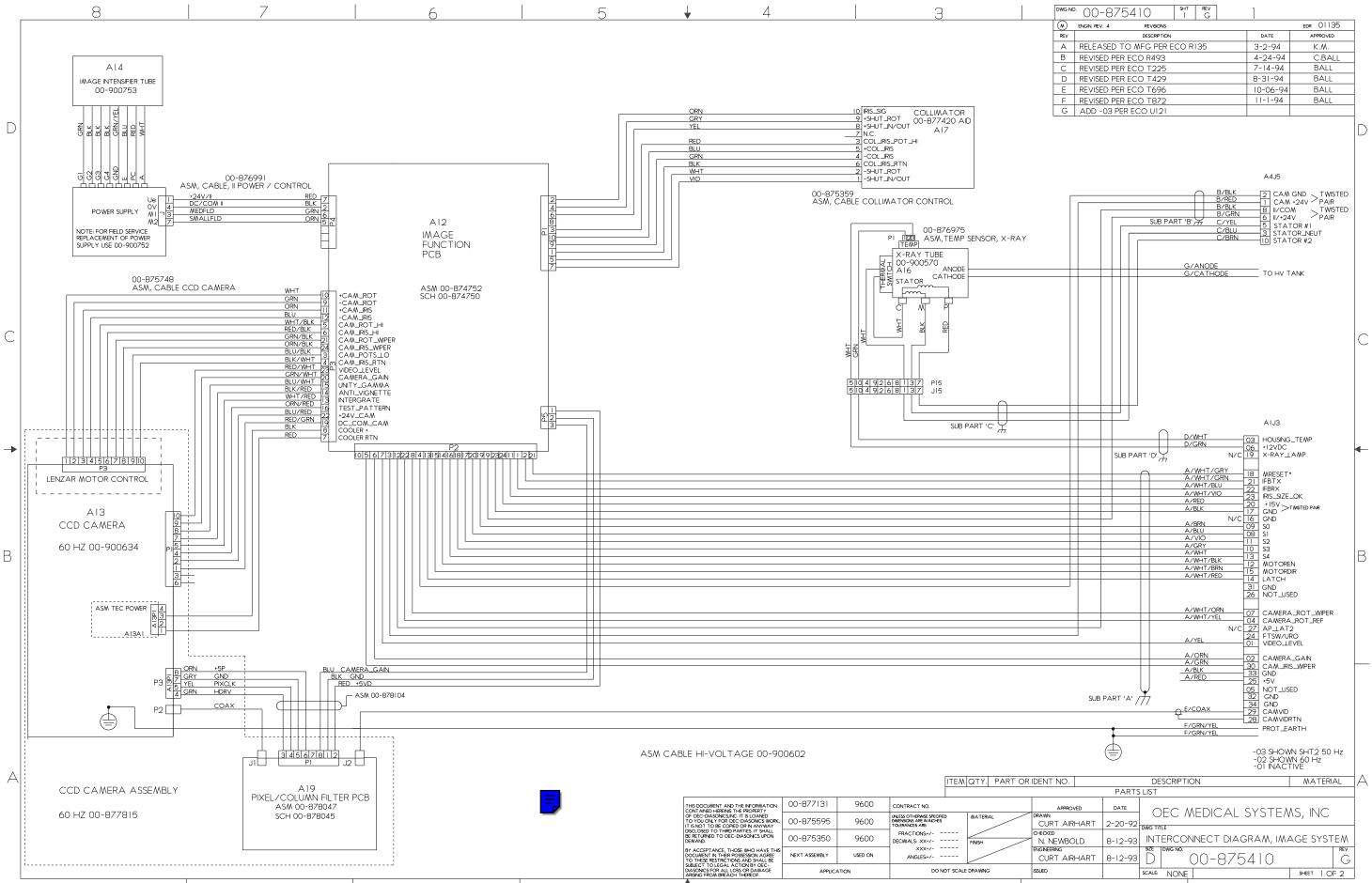


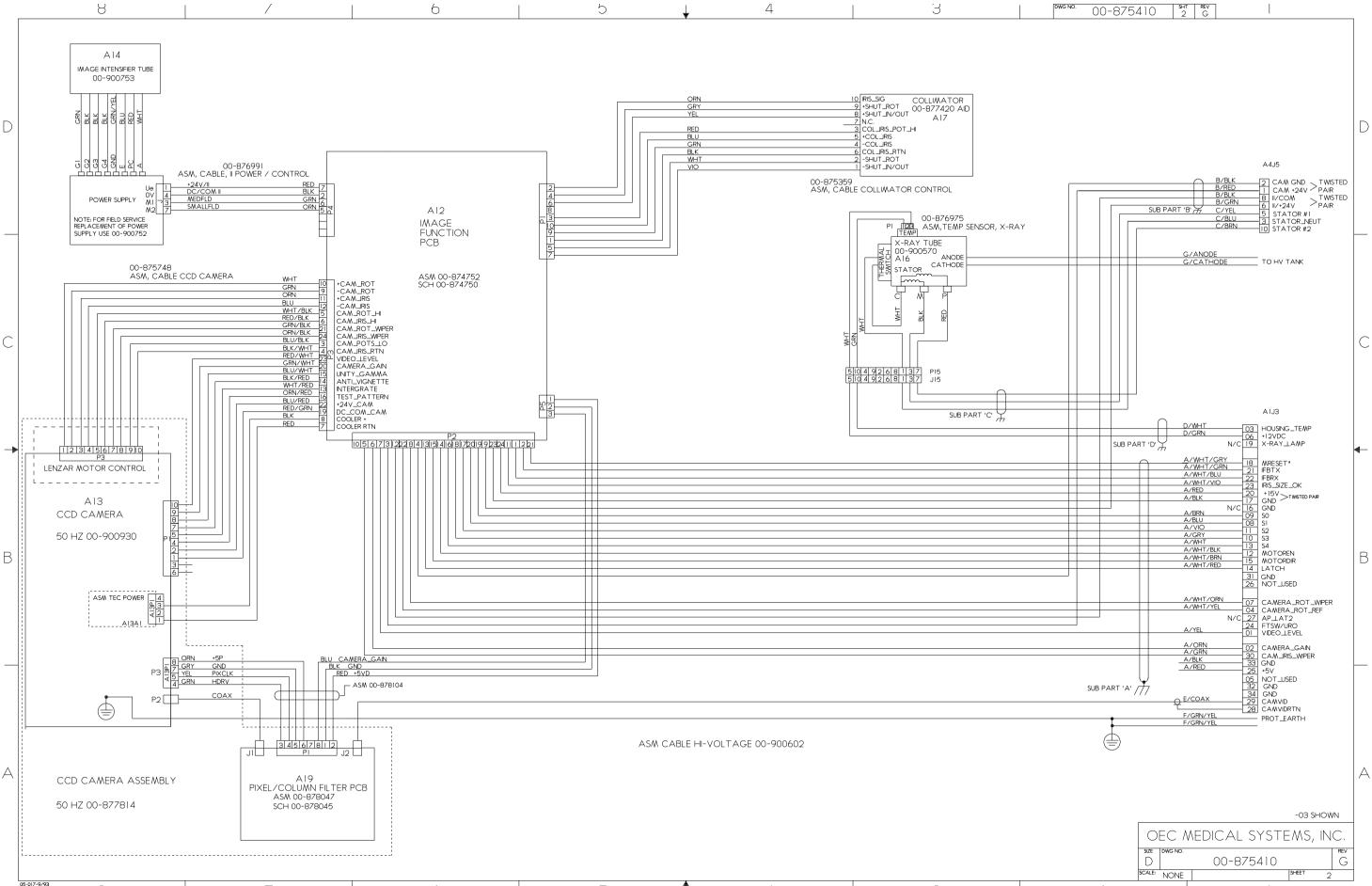
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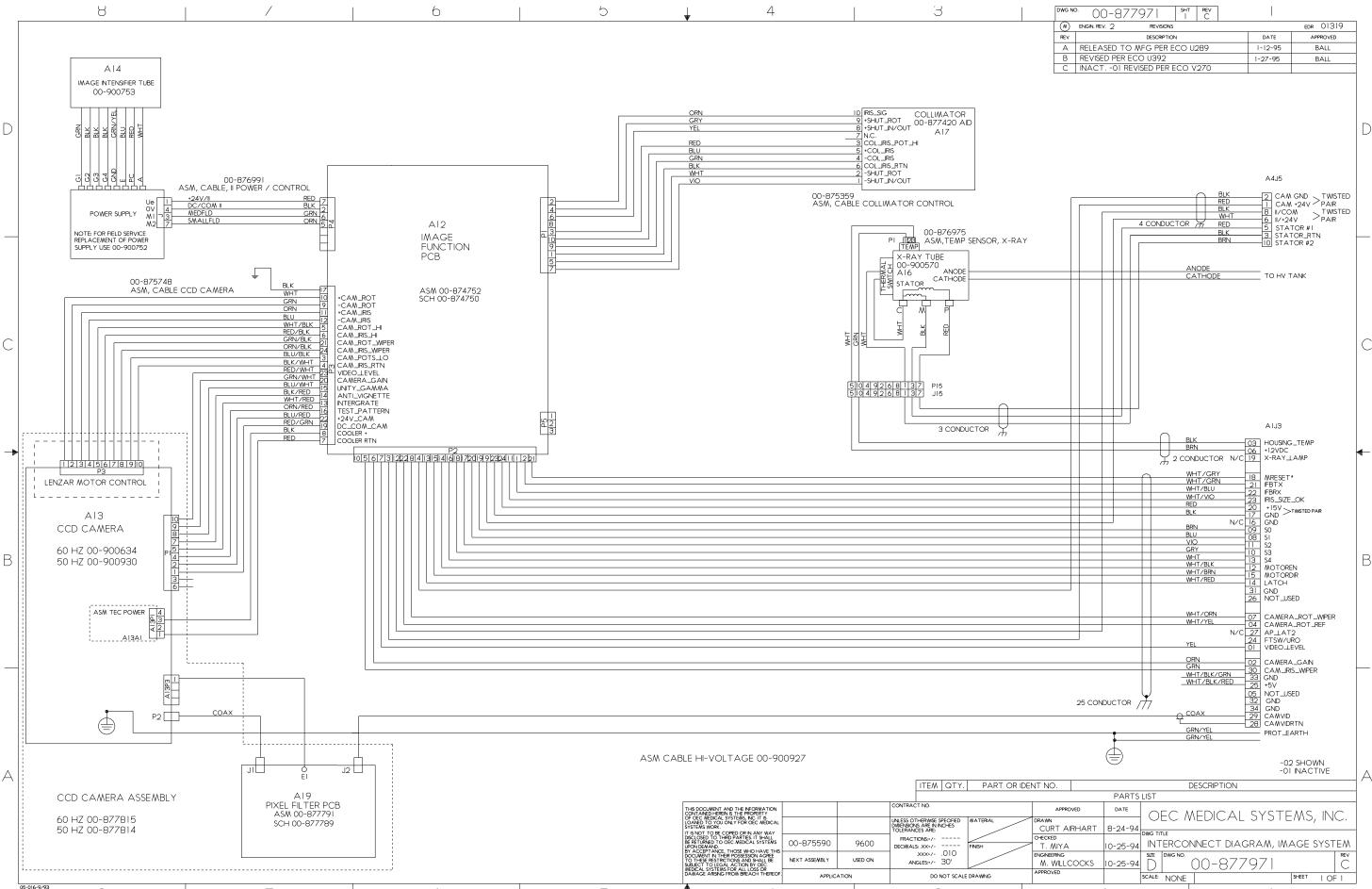
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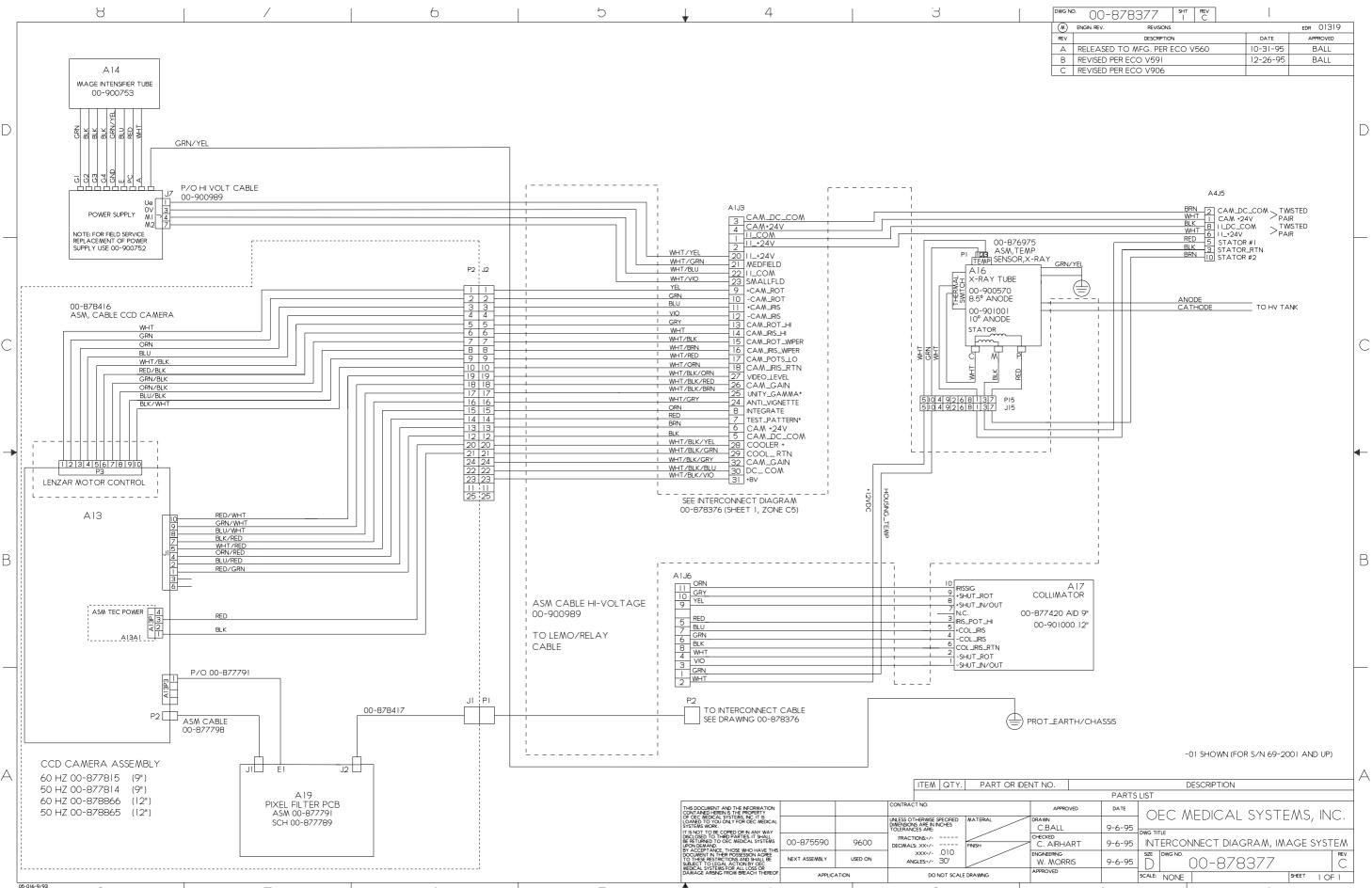
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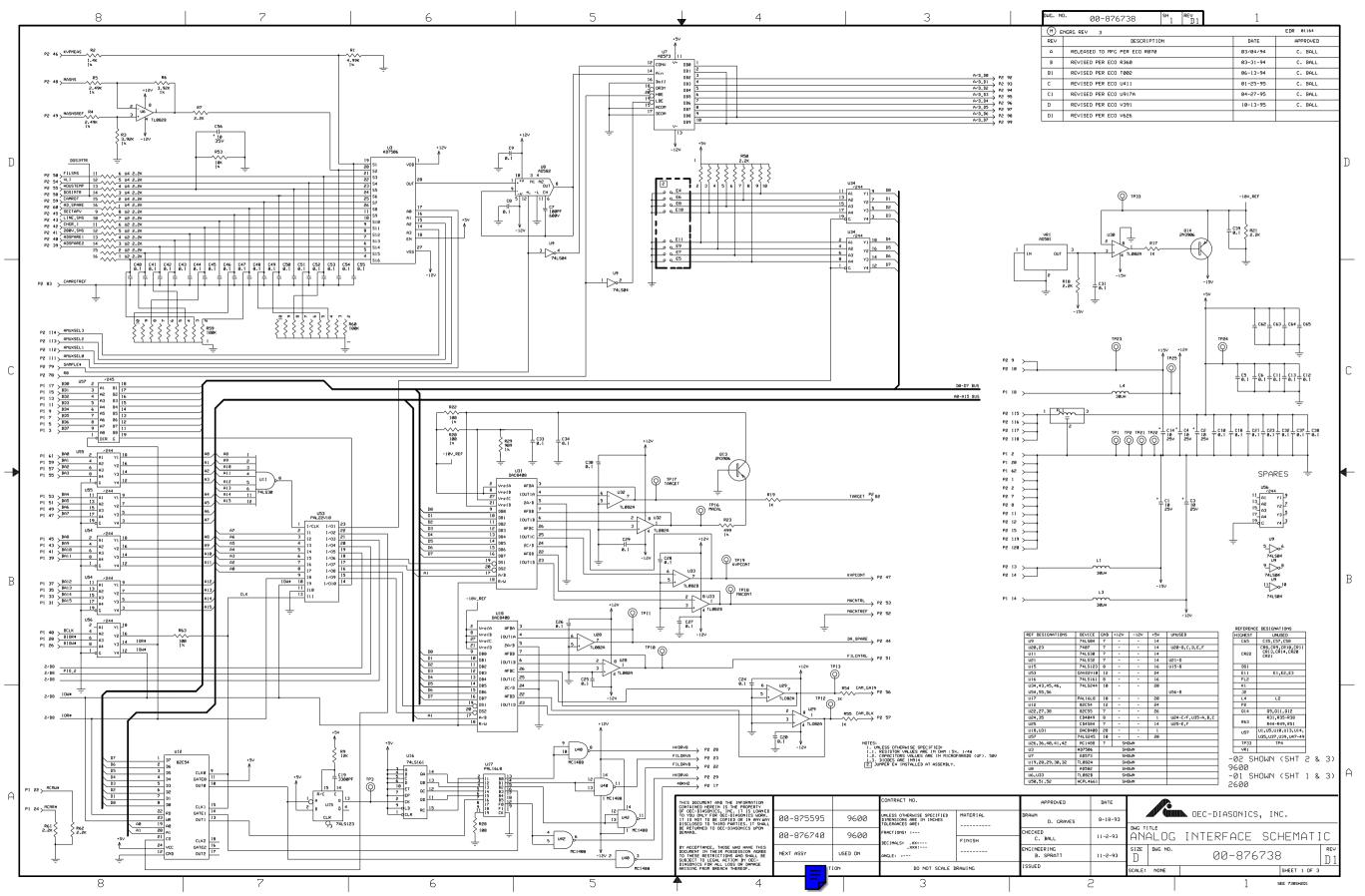


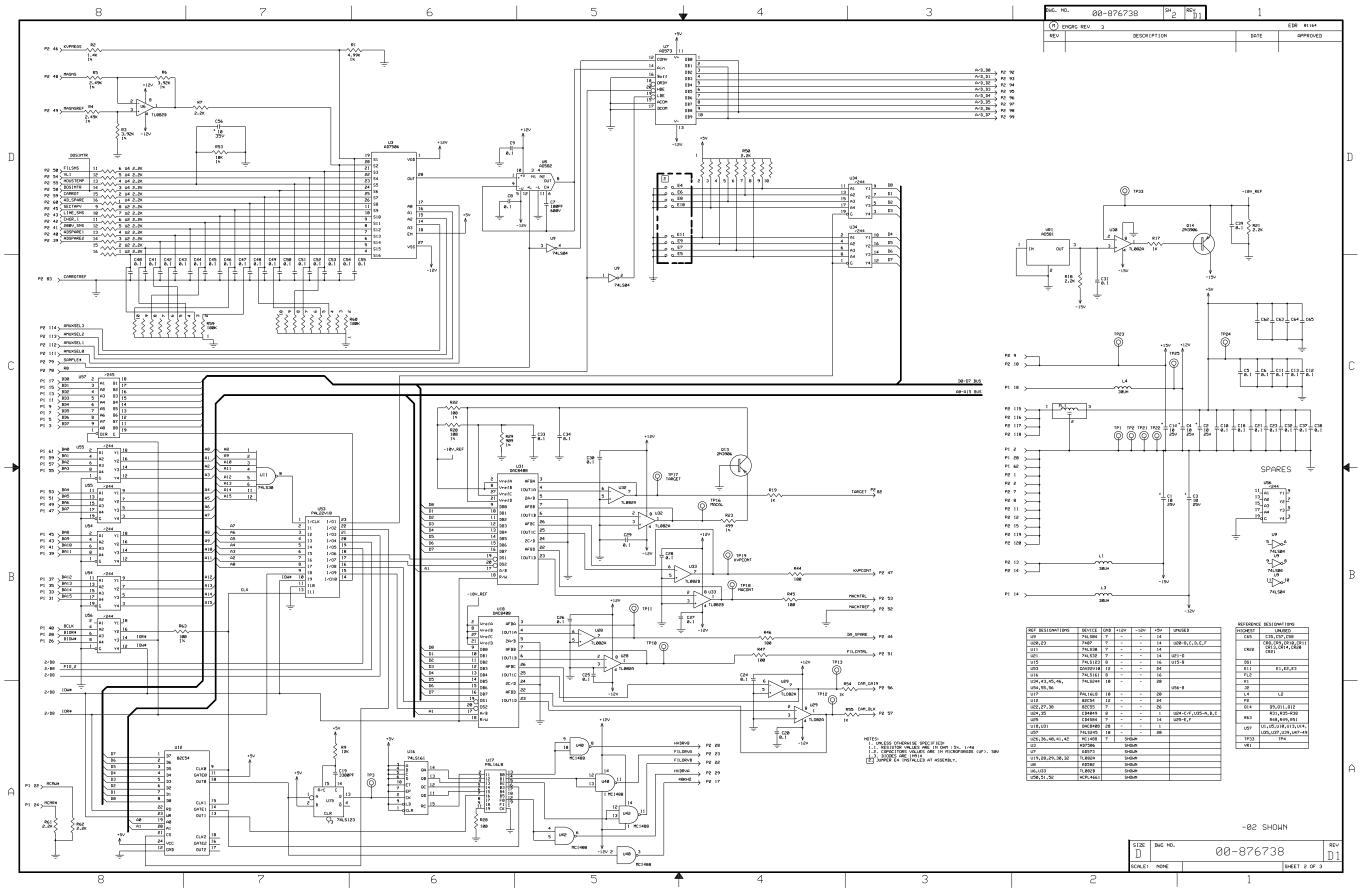


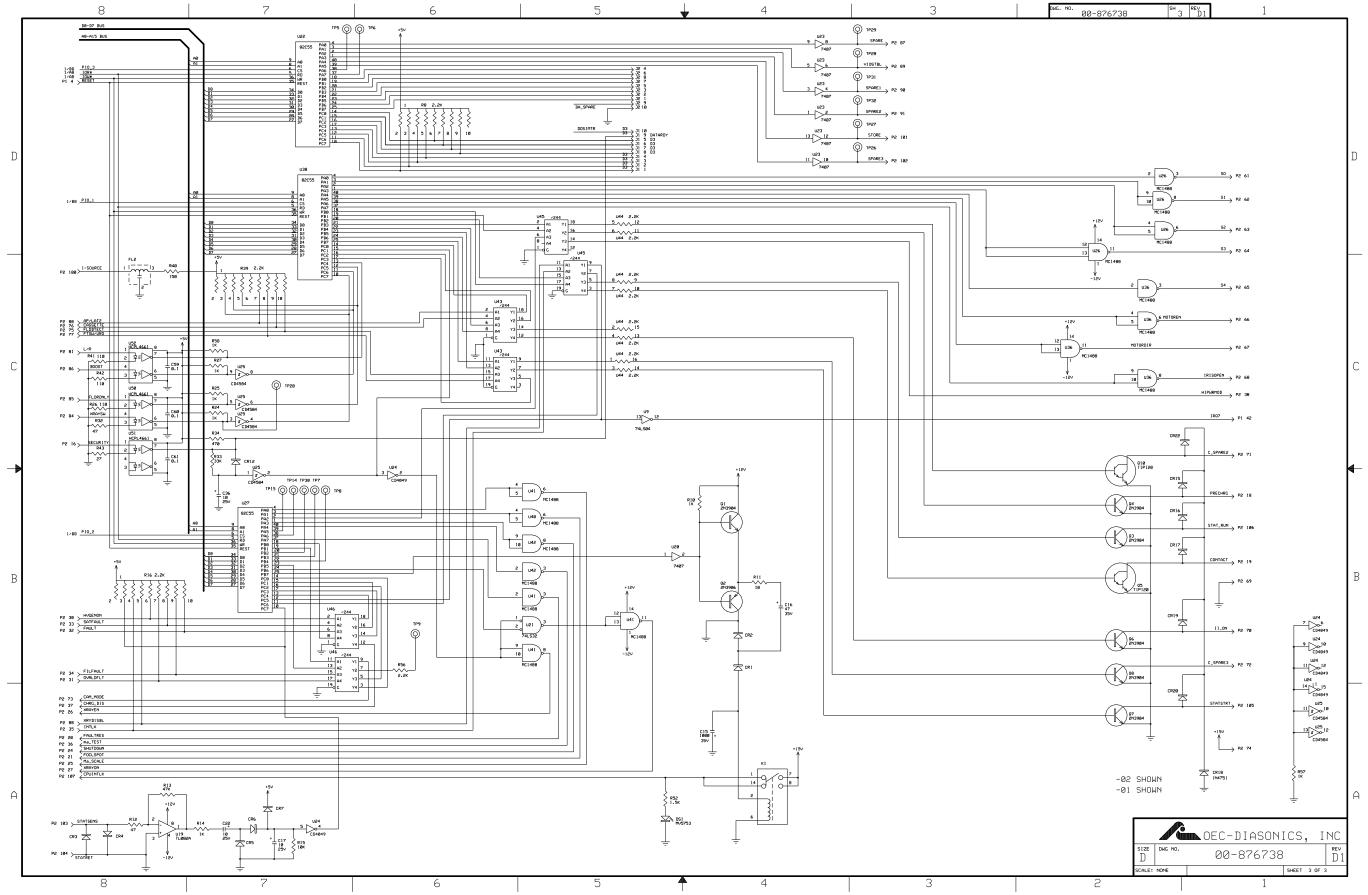


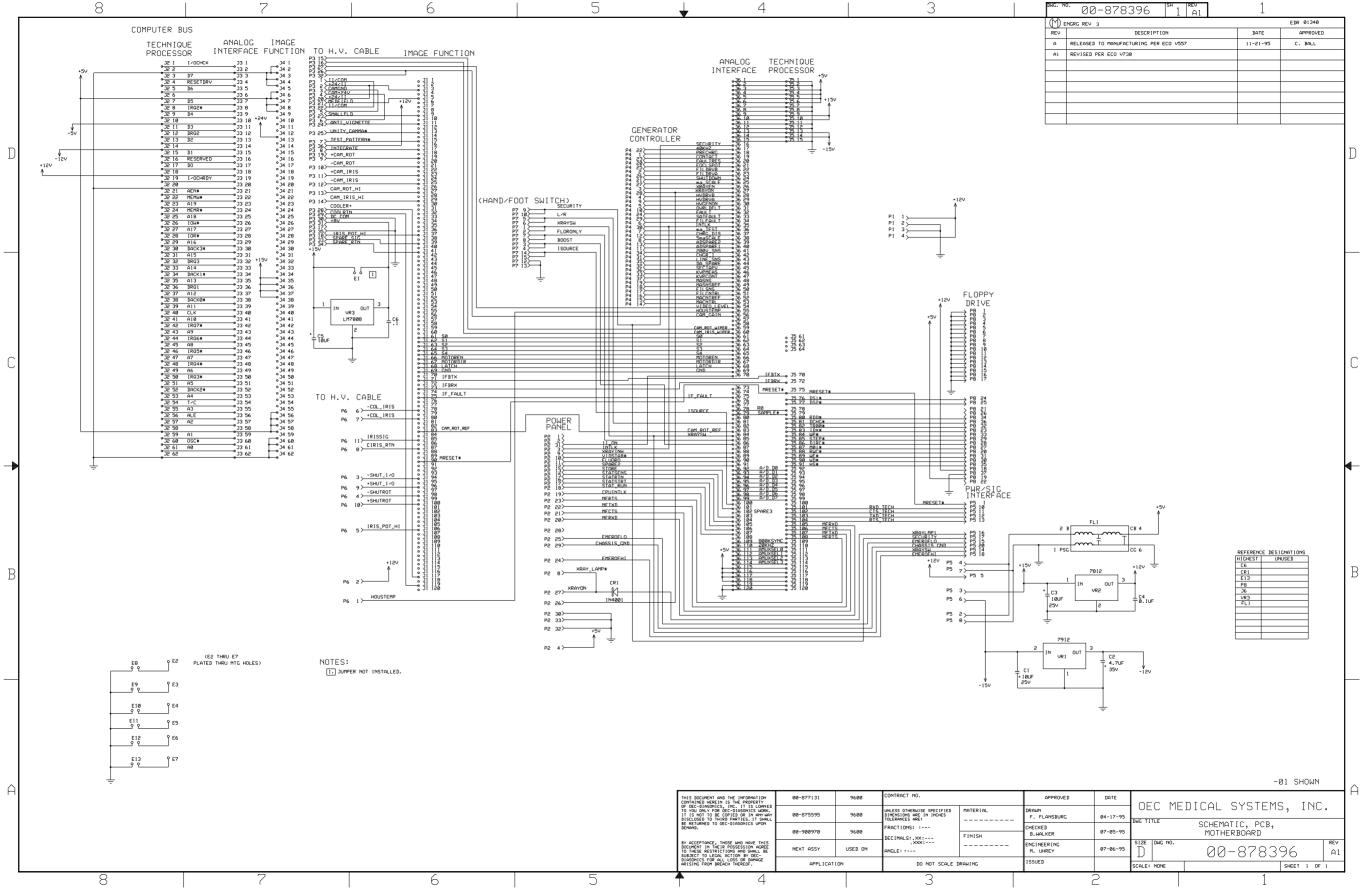


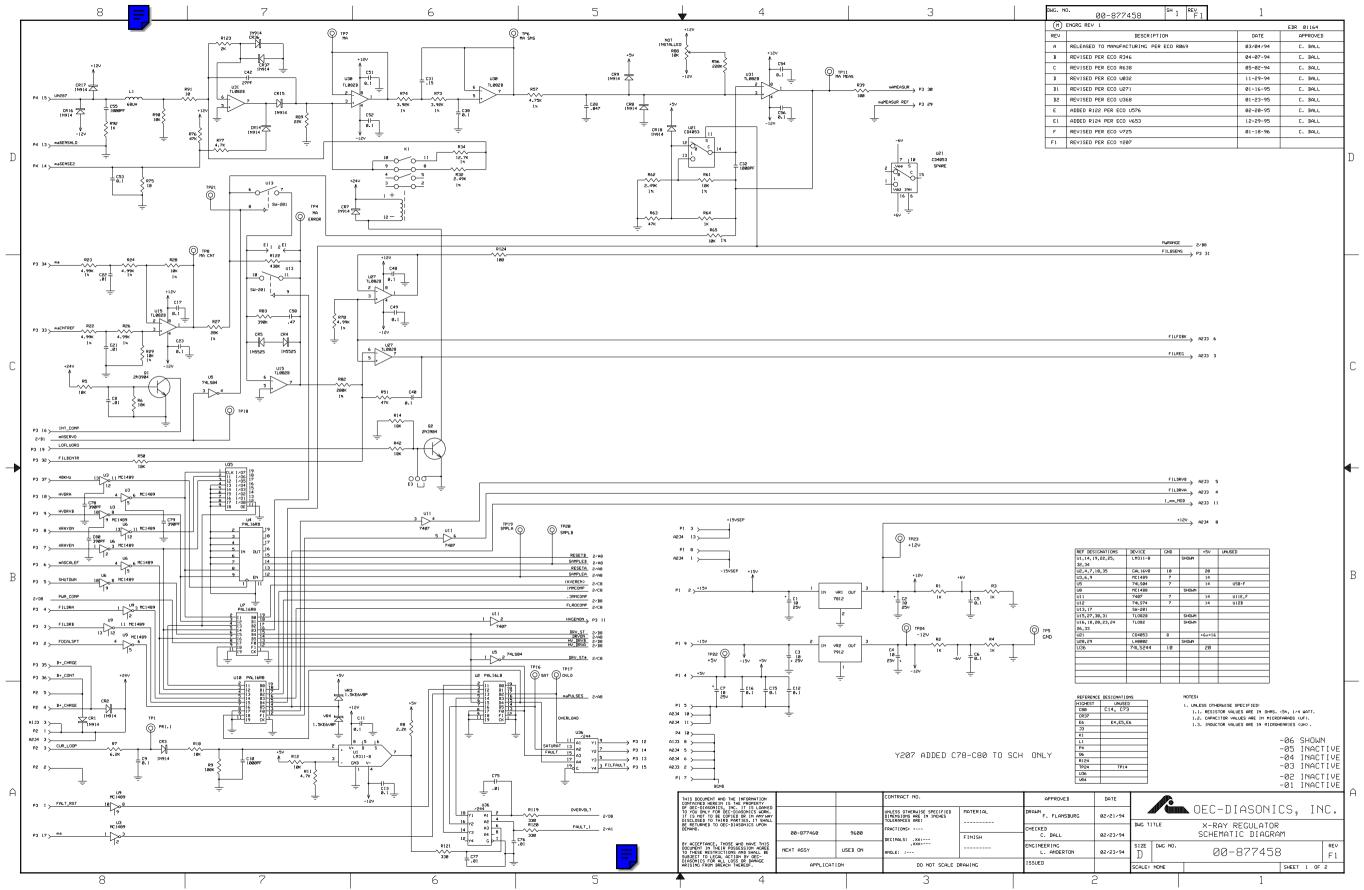


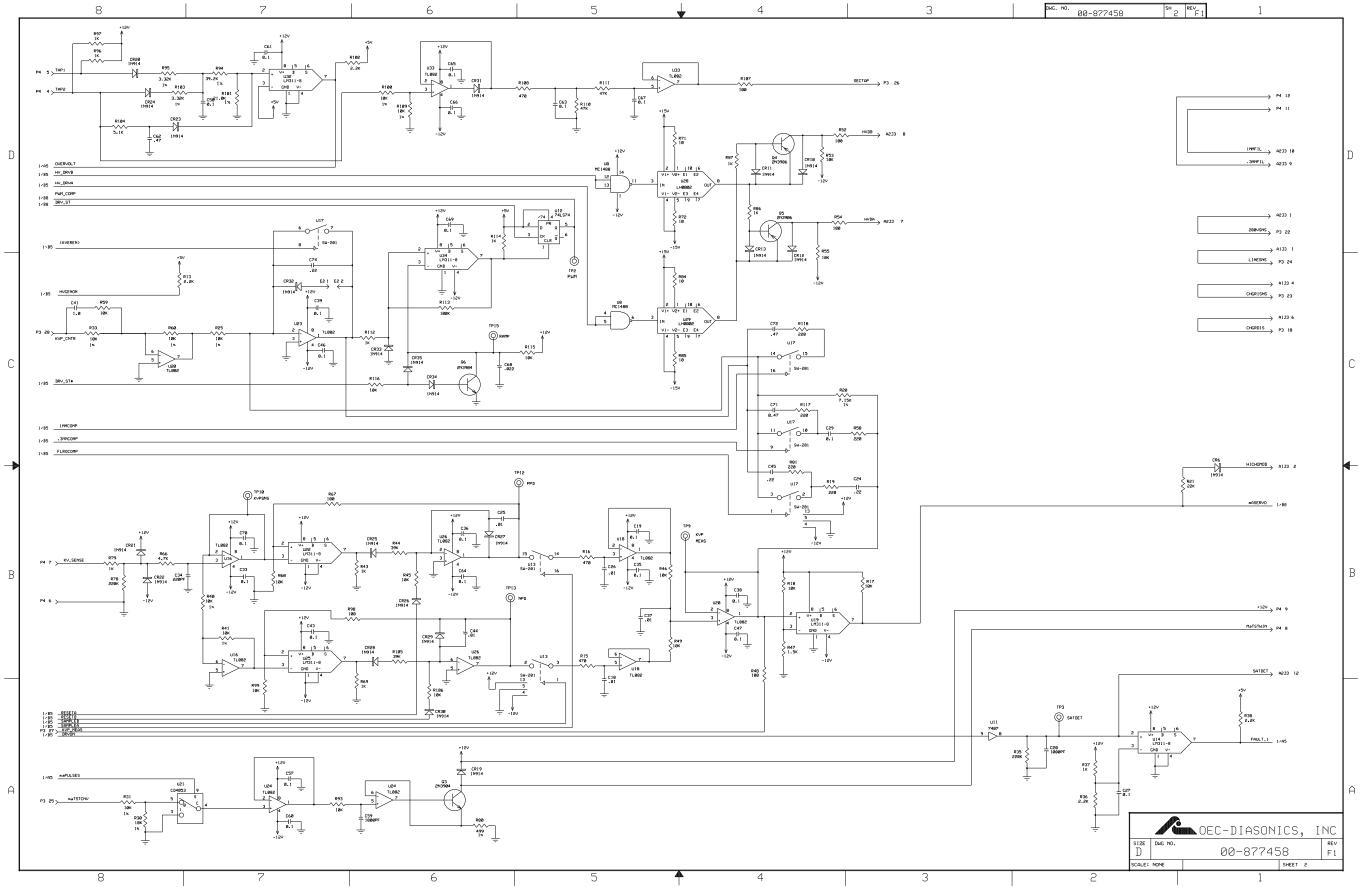


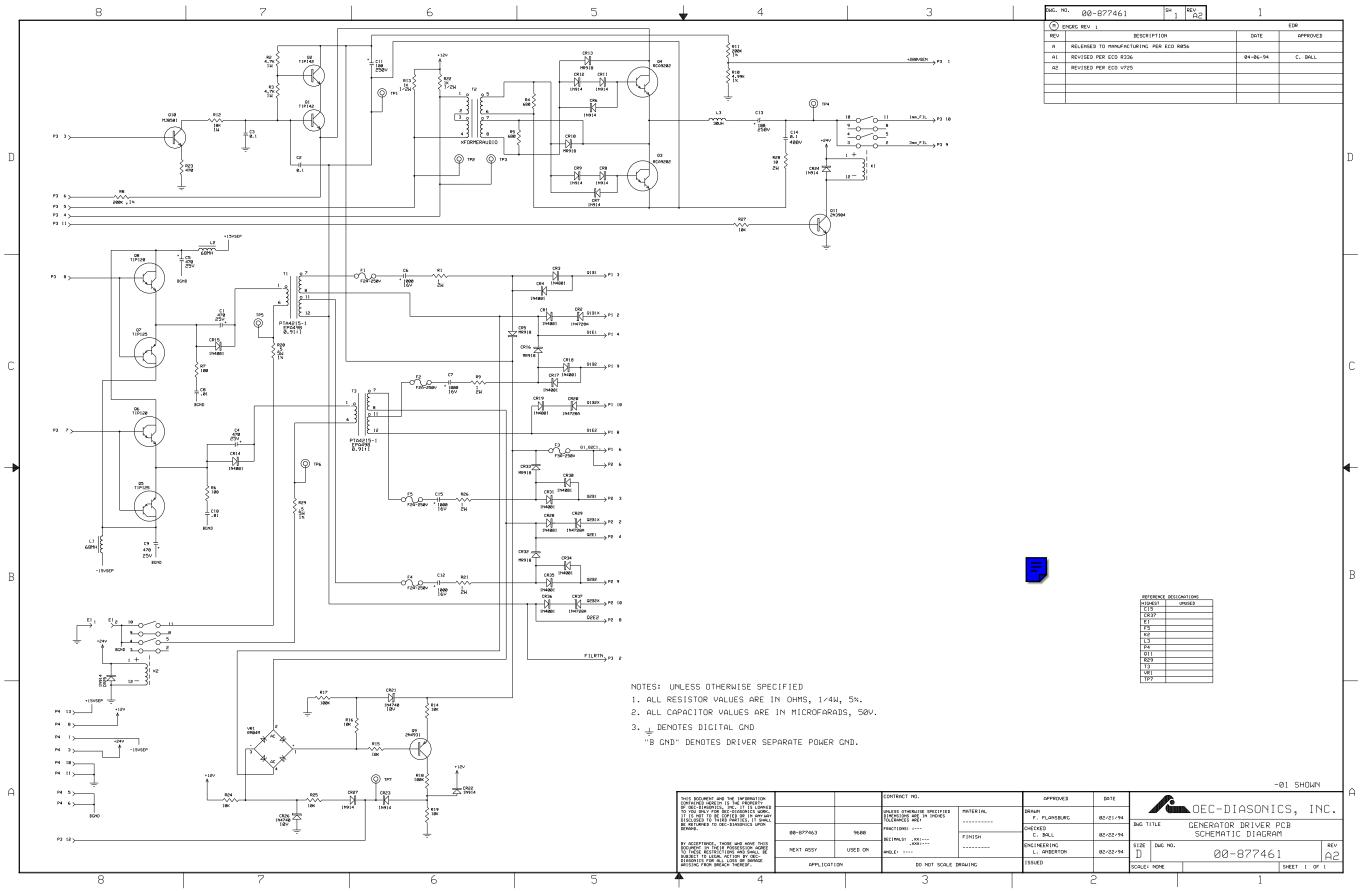


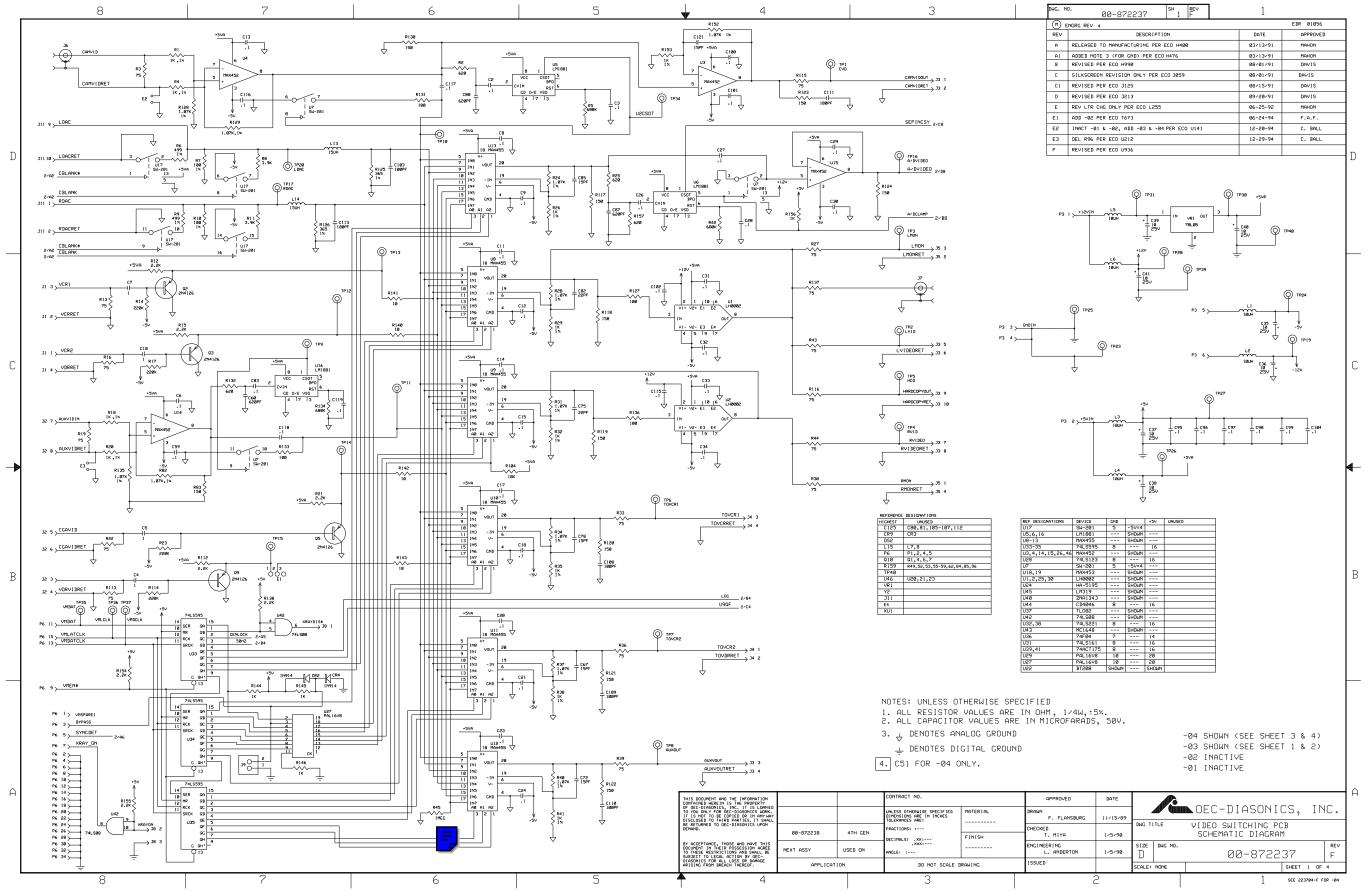


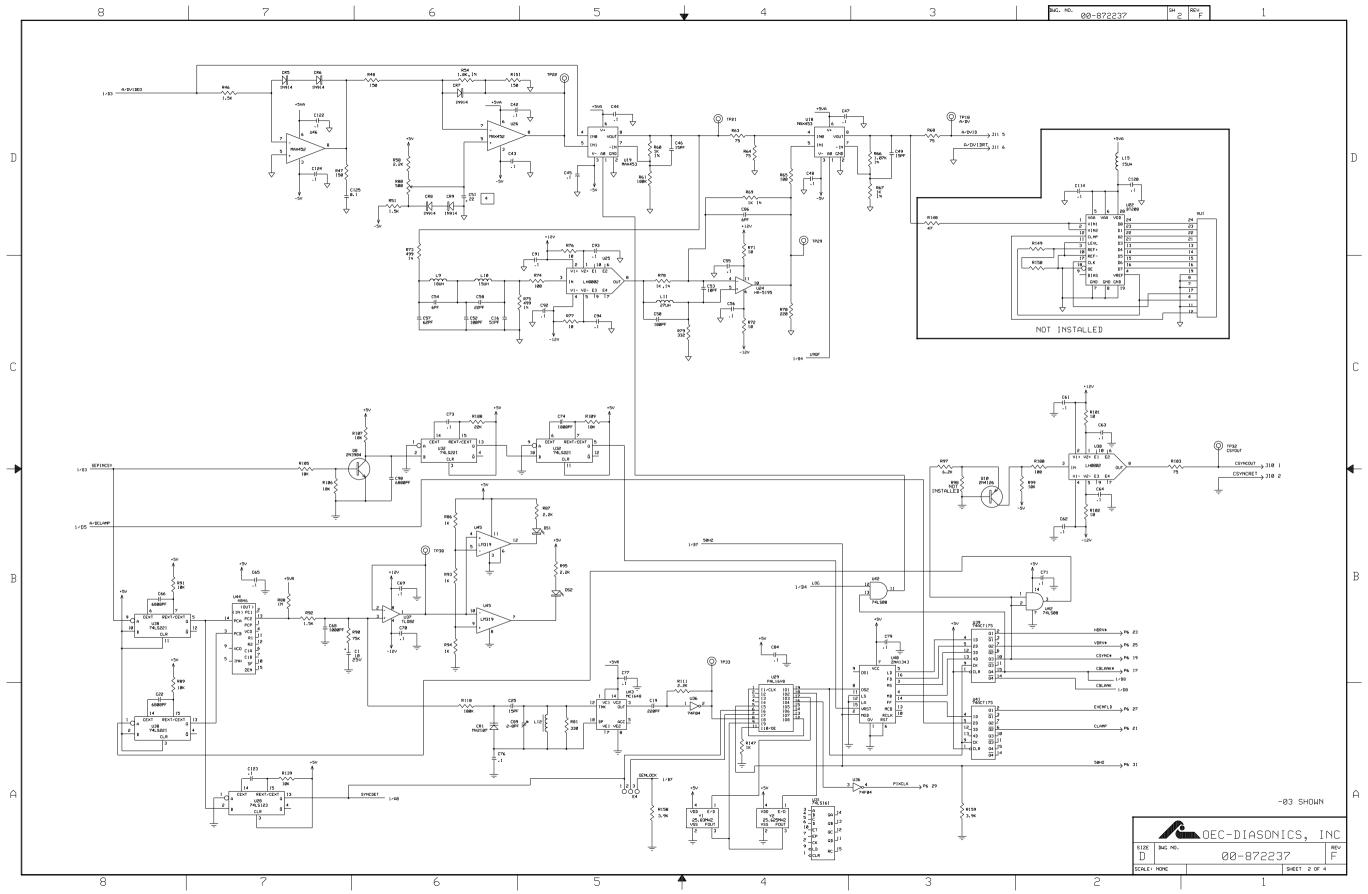


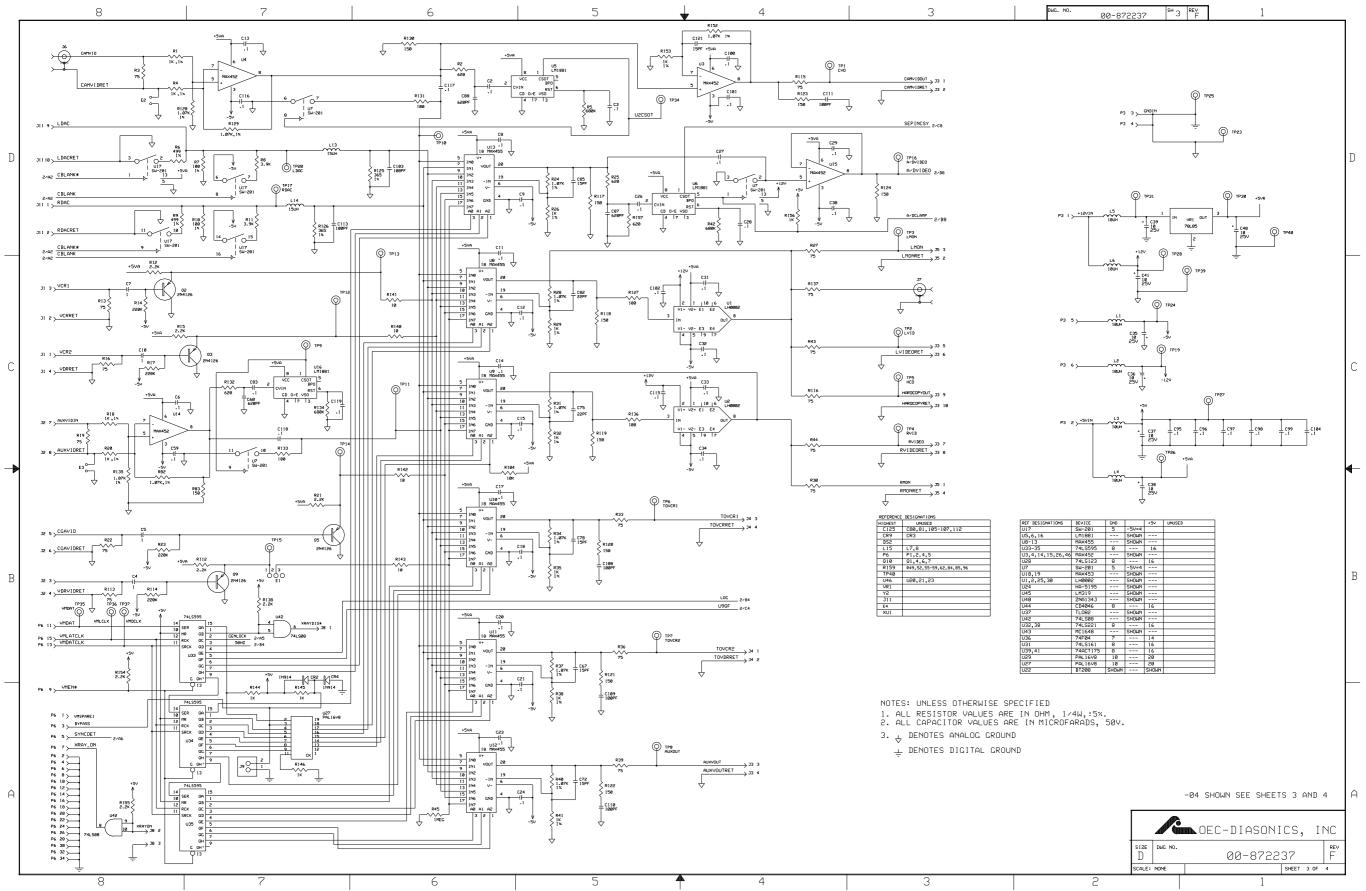


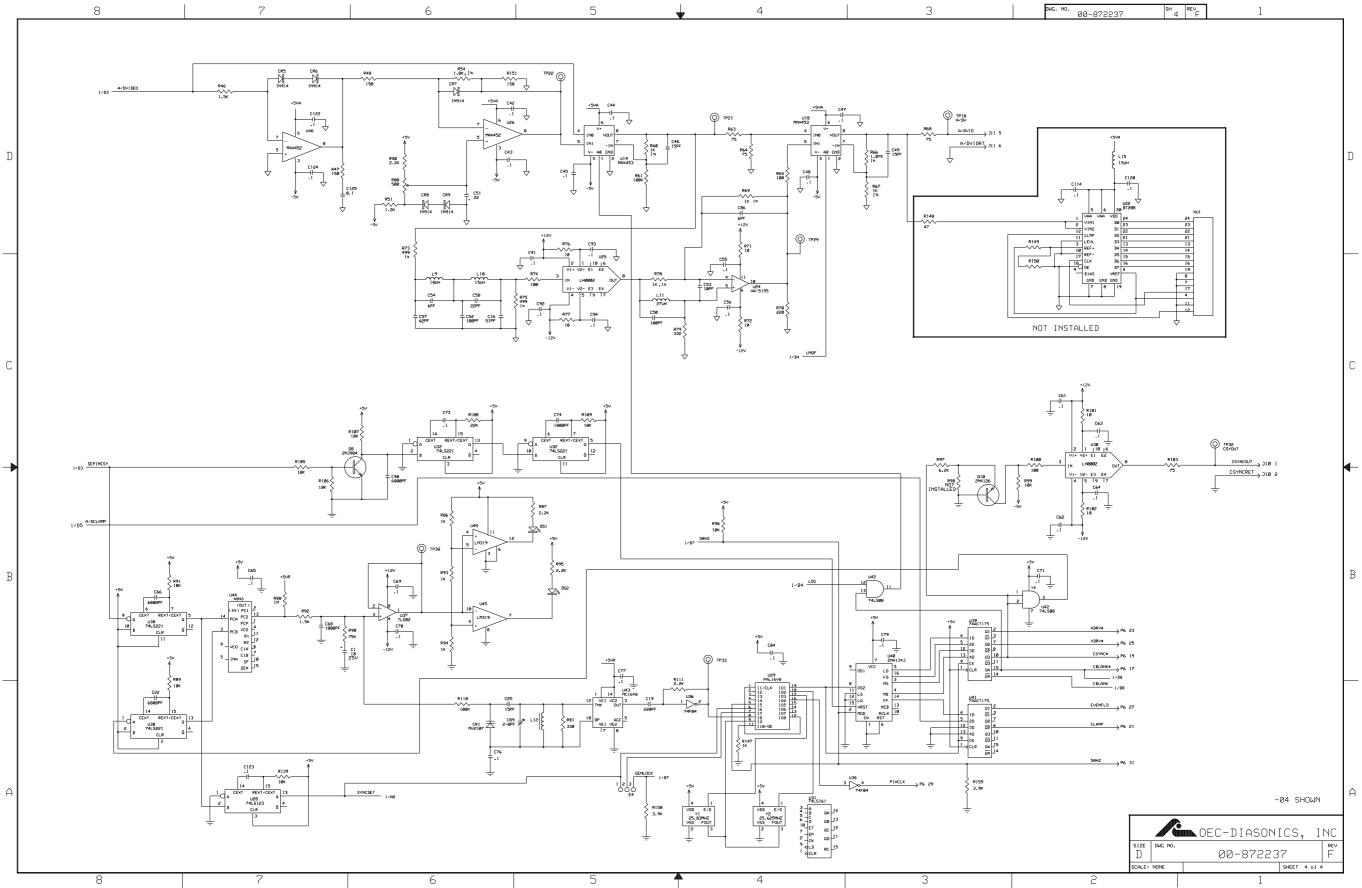


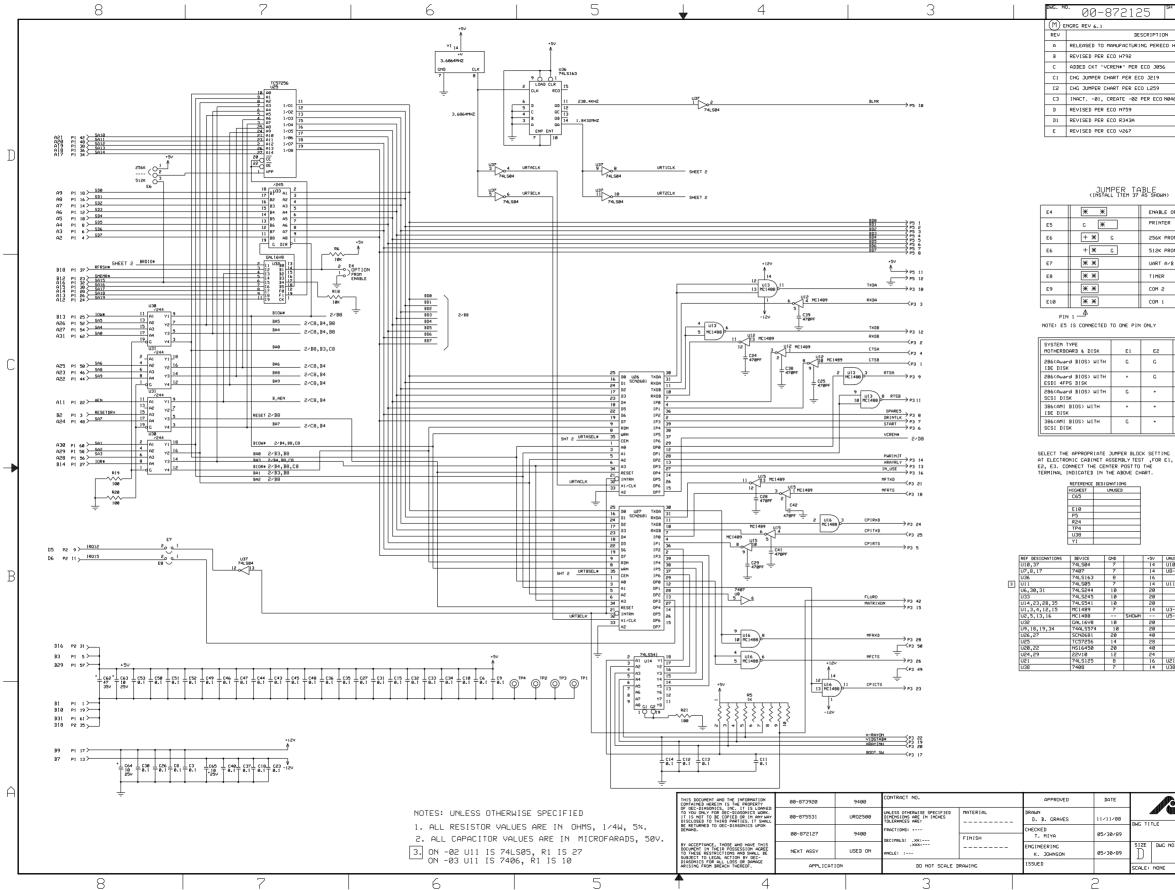












° 00-872125 [№] 1 E	1		
NGRG REV 6.1		EDR 01021	
DESCRIPTION	DATE	APPROVED	
RELEASED TO MANUFACTURING PERECO H486	03/25/91	DAVIS	
REVISED PER ECO H792	06/11/91	DAVIS	
ADDED CKT "VCREN*" PER ECO J056	08/05/91	DAVIS	
CHG JUMPER CHART PER ECO J219	09/20/91	DAVIS	
CHG JUMPER CHART PER ECO L259	06/25/92	MAHON	
INACT01, CREATE -02 PER ECO N046	08/05/93	C. BALL	
REVISED PER ECO N759	11/12/93	C. BALL	
REVISED PER ECO R343A	04/07/94	C. BALL	
REVISED PER ECO V267			
	UU-872125 1 E NCRG REV 6.1 DESCRIPTION RELEASED TO MANUFACTURING PERECO M486 REVISED PER ECO H792 ADDED CKT "UCRENN" PER ECO 3056 CH0 3000 CHG 3UMPER CHART PER ECO 3219 CH0 3000 CHG 3UMPER CHART PER ECO L259 IIINCT01, CREATE -02 PER ECO N046 REVISED PER ECO N759 REVISED PER ECO R343A	UU-872125 1 I I NCRG REV 6.1 DESCRIPTION DATE RELEASED TO MANUFACTURING PERECO H486 33225/91 REVISED PER ECO H792 06/11/91 ADDED CKT "VCREN*" PER ECO 3856 88/85/91 CHG JUMPER CHART PER ECO 3219 09/28/91 CHG JUMPER CHART PER ECO 1259 06/25/92 INACT01, CREATE -02 PER ECO N046 88/85/93 REVISED PER ECO N759 11/12/93 REVISED PER ECO R343A 04/87/94	

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ENABLE OPTION PROM PRINTER 256K PROM SELECT -02) (-03) 512K PROM SELECT UART A/R TIMER COM 2 COM 1

SYSTEM TYPE MOTHERBOARD & DISK	El	E2	E3
286(Award BIOS) WITH IDE DISK	c	C	C
286(Award BIOS) WITH ESDI 4FPS DISK	•	G	G
286(Award BIOS) WITH SCSI DISK	G	+	+
386(AMI BIOS) WITH IDE DISK	•	+	G
386(AMI BIDS) WITH SCSI DISK	C	+	G

	REF DESIGNATIONS	DEVICE	GND		+5V	UNUSED
	U10,37	74LS04	7		14	U10-E,F
	U7,8,17	7407	7		14	U8-B, D
	U36	74LS163	8		16	
3	U11	74LS05	7		14	U11-E,F
_	U6,30,31	74LS244	10		50	
	U33	74LS245	10		20	
	U14,23,28,35	74LS541	10		20	
	U1,3,4,12,15	MC1489	7		14	U3-A, B
	U2,5,13,16	MC1488		SHOWN		U5-C,D
	U35	GAL16V8	10		50	
	U9,18,19,34	74ALS574	10		20	
	U26,27	SCN2681	50		40	
	U25	TC57256	14		28	
	020,22	NS16450	50		40	
	U24,29	S5A10	15		24	
	US1	74LS125	8		16	U21-B,C
	U38	7408	7		14	U38-C,D

-03 SHOWN -02 SHOWN -01 INACTIVE

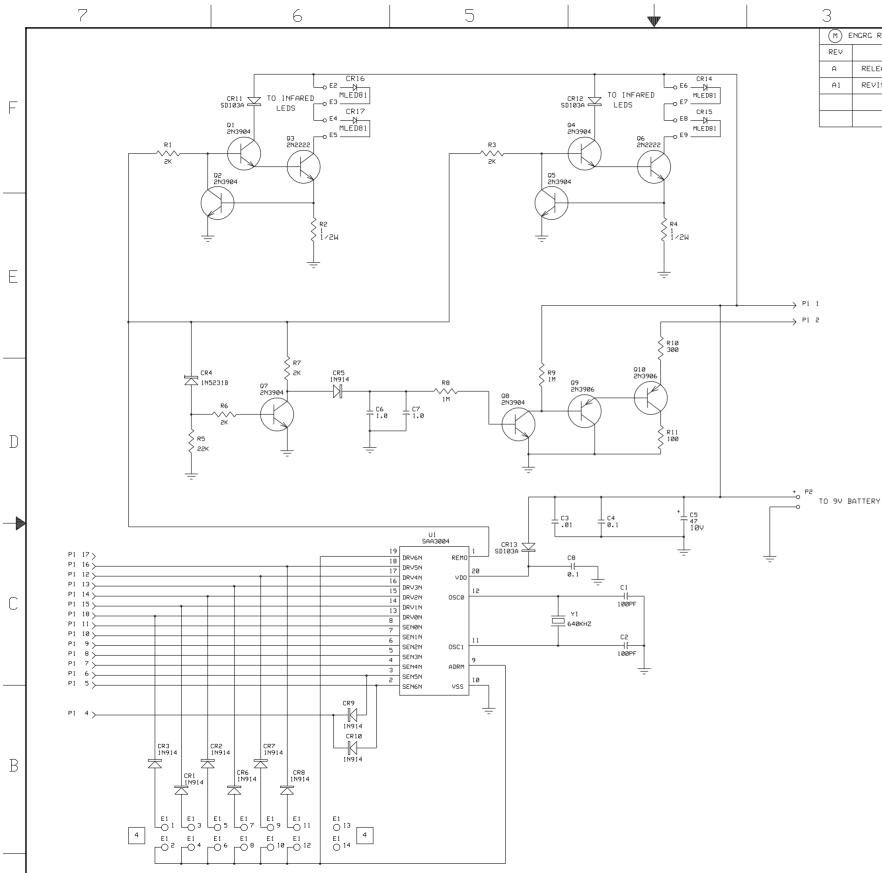
SHEET 1 OF 2

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CEC-DIASONICS, INC.

00-872125

COMMUNICATIONS PCB SCHEMATIC DIAGRAM



3		2			1
M	ENGRG REV 4	REVISIONS			EDR 01131
REV		DESCRIPTION		DATE	APPROVED
A	RELEASED TO MANUFACTURING PERECO H482			03/26/91	MAHON
A1	REVISED PER ECC	U060A			

REFERENCE	DESIGNATIONS
HIGHEST	UNUSED
C8	
CR17	
E9	
P2	
Q10	
R11	
U1	
Y1	

1. ALL RESISTOR VALUES ARE INOHMS, 1/4W, 5%. 2. ALL CAPACITOR VALUES ARE IN MICROFARADS, 50V.

4. JUMPER PINS 11-12 OF E1 (-03)

NOTES:

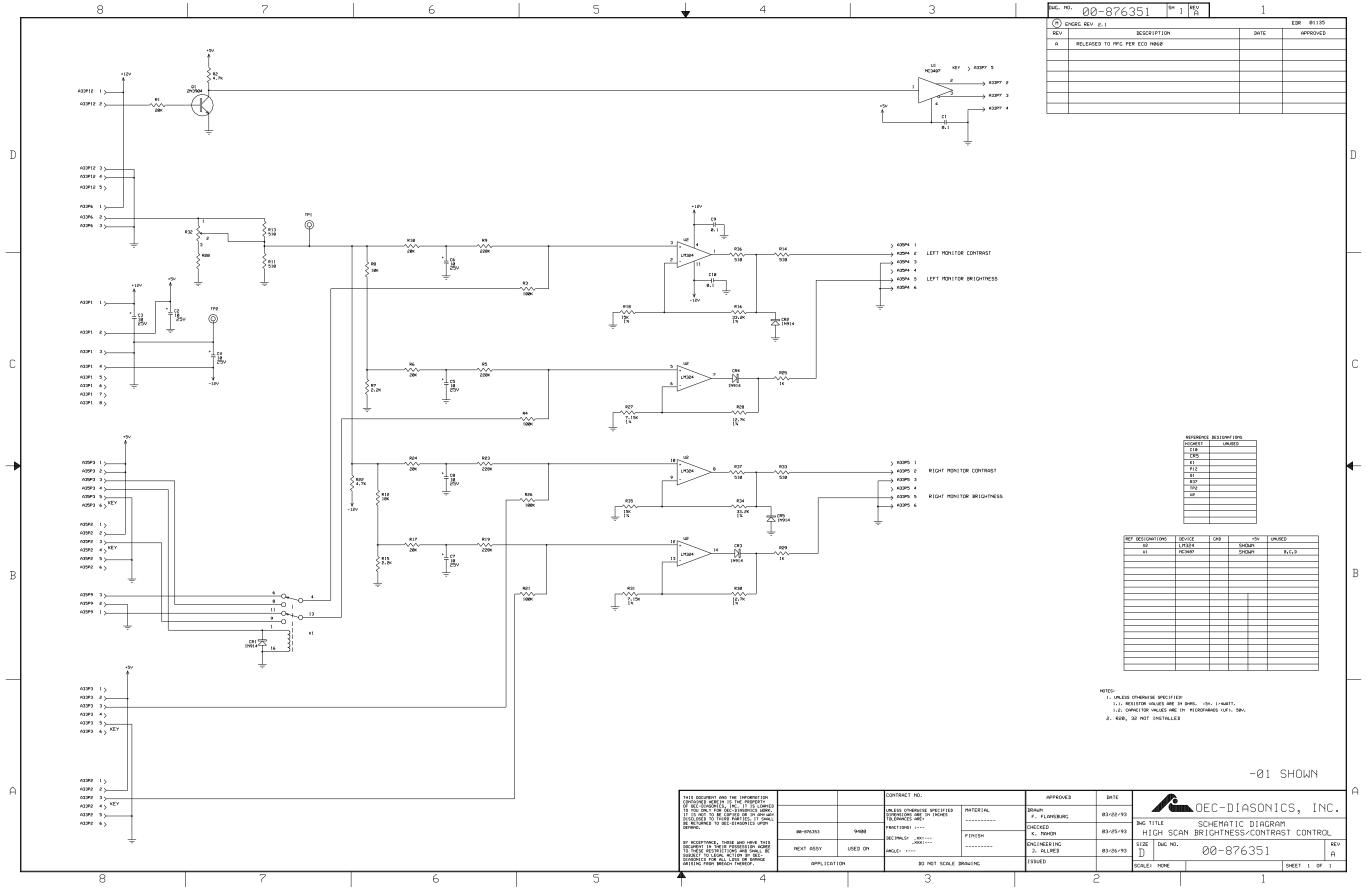
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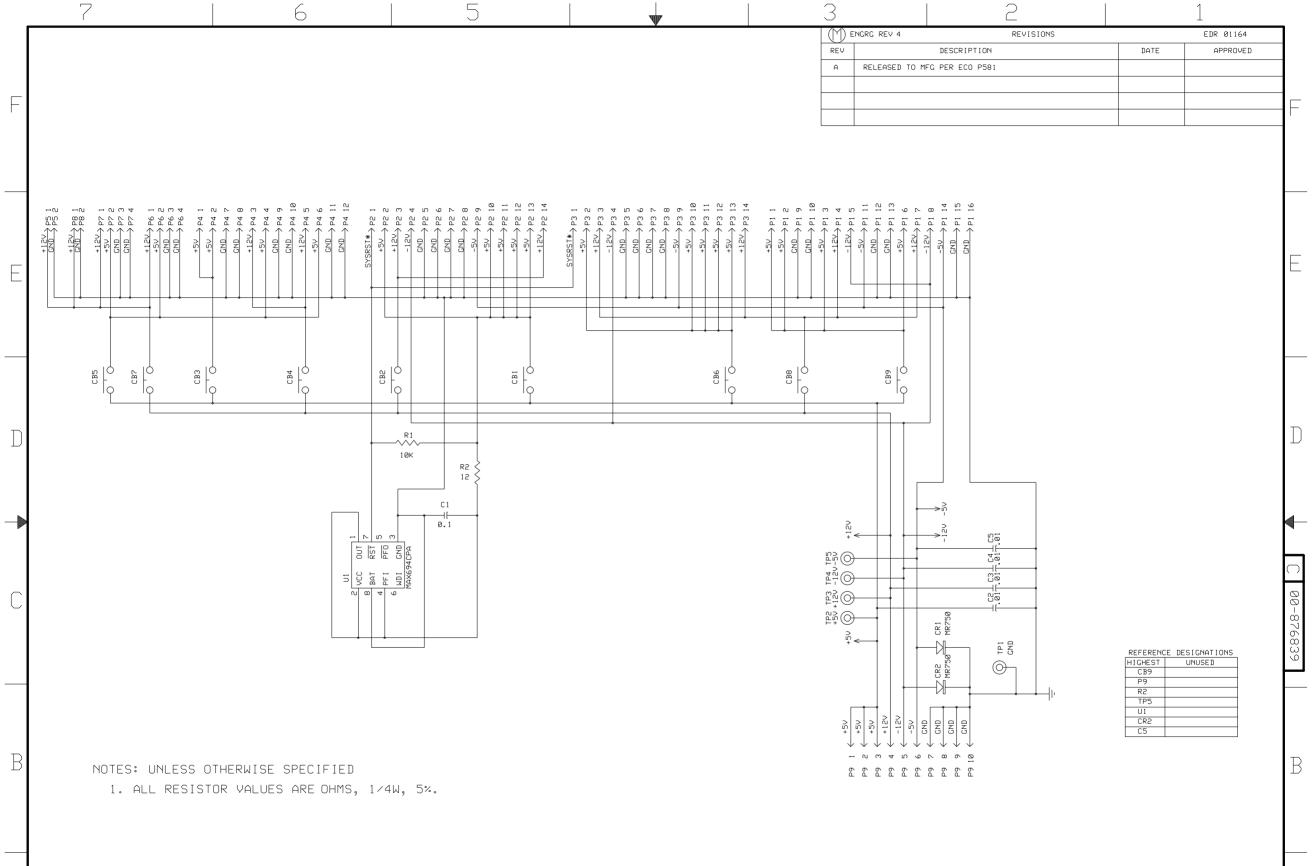
-03 SHOWN -02 INACTIVE -01 INACTIVE Ε

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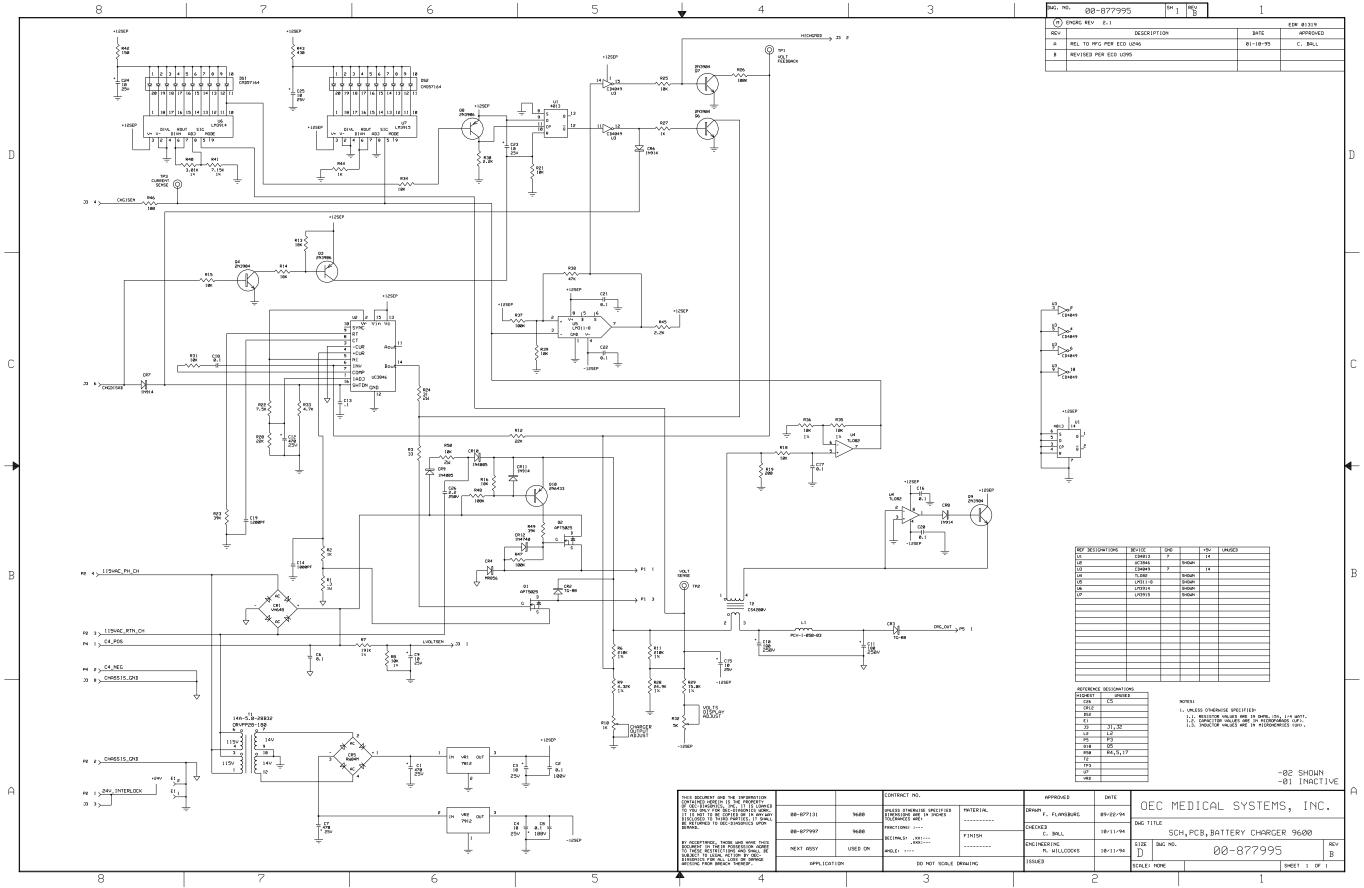
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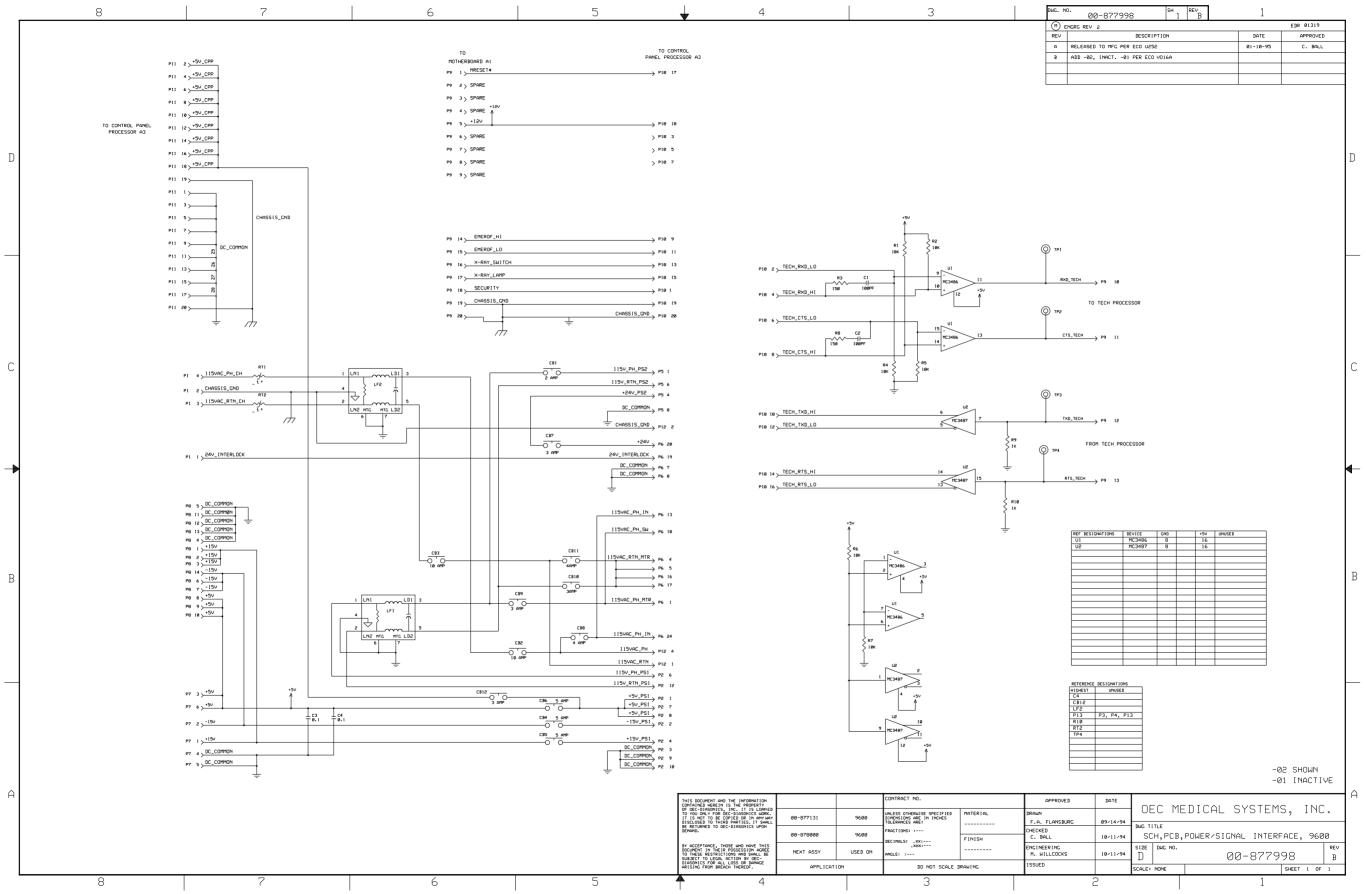
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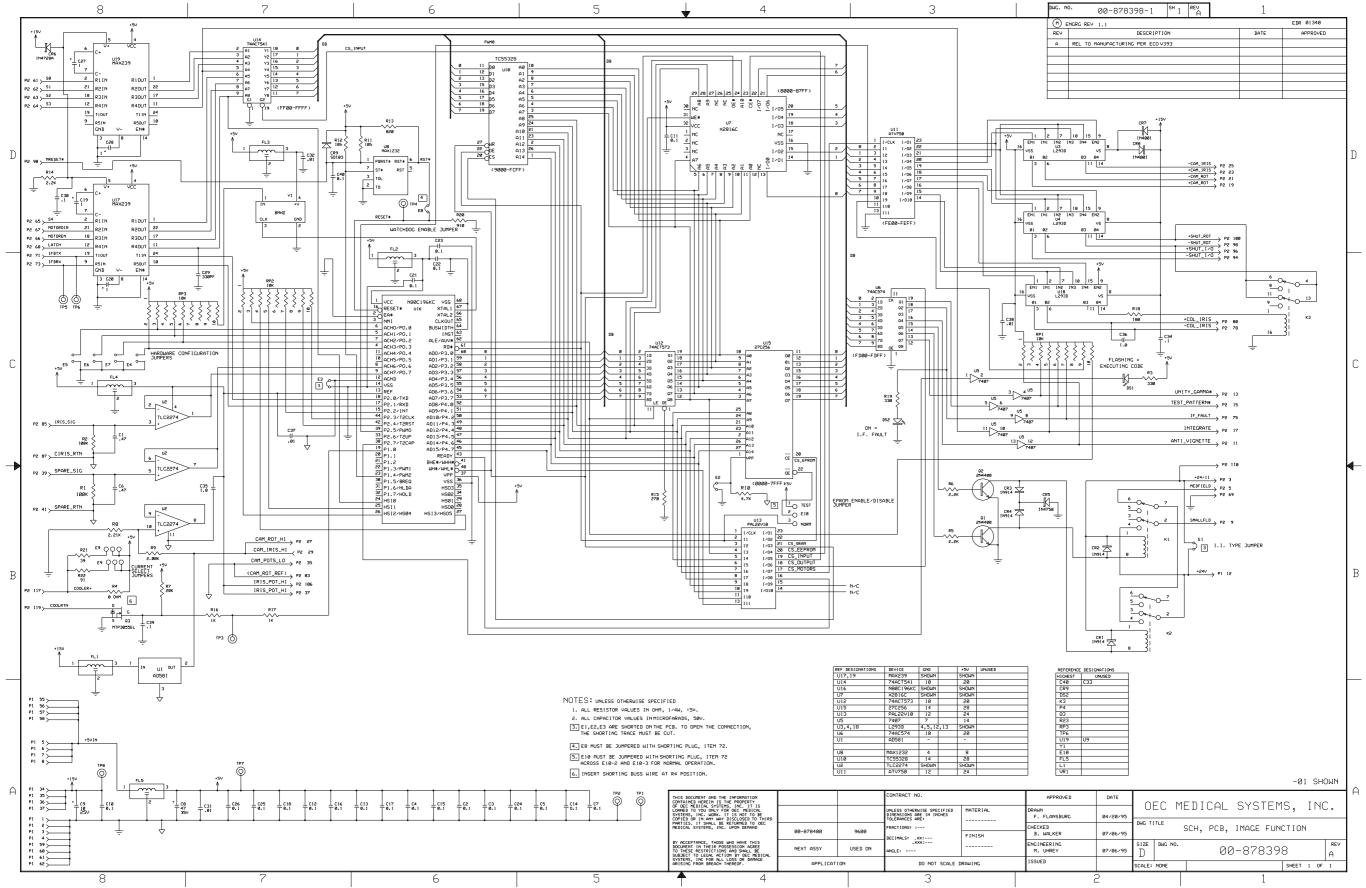


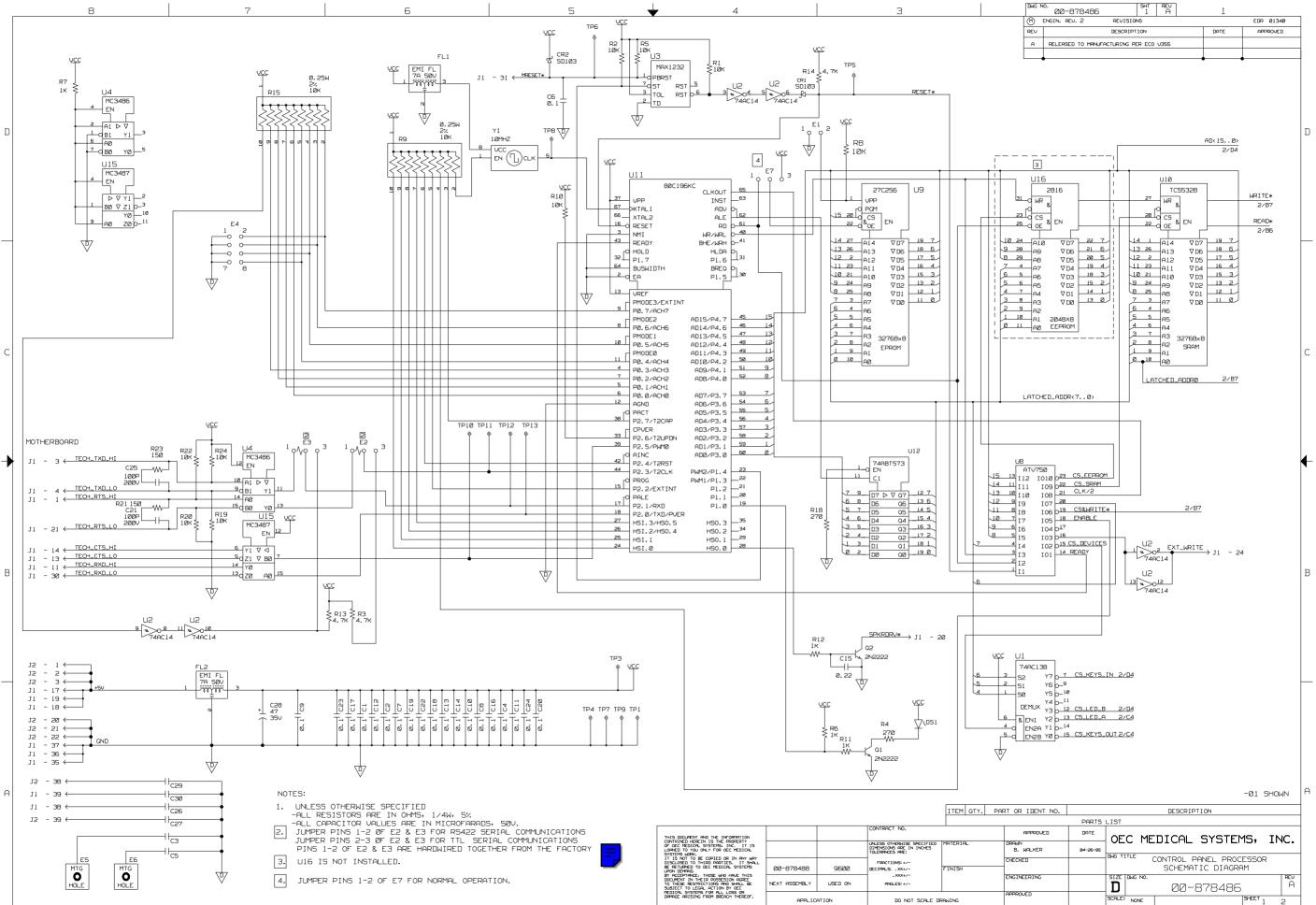


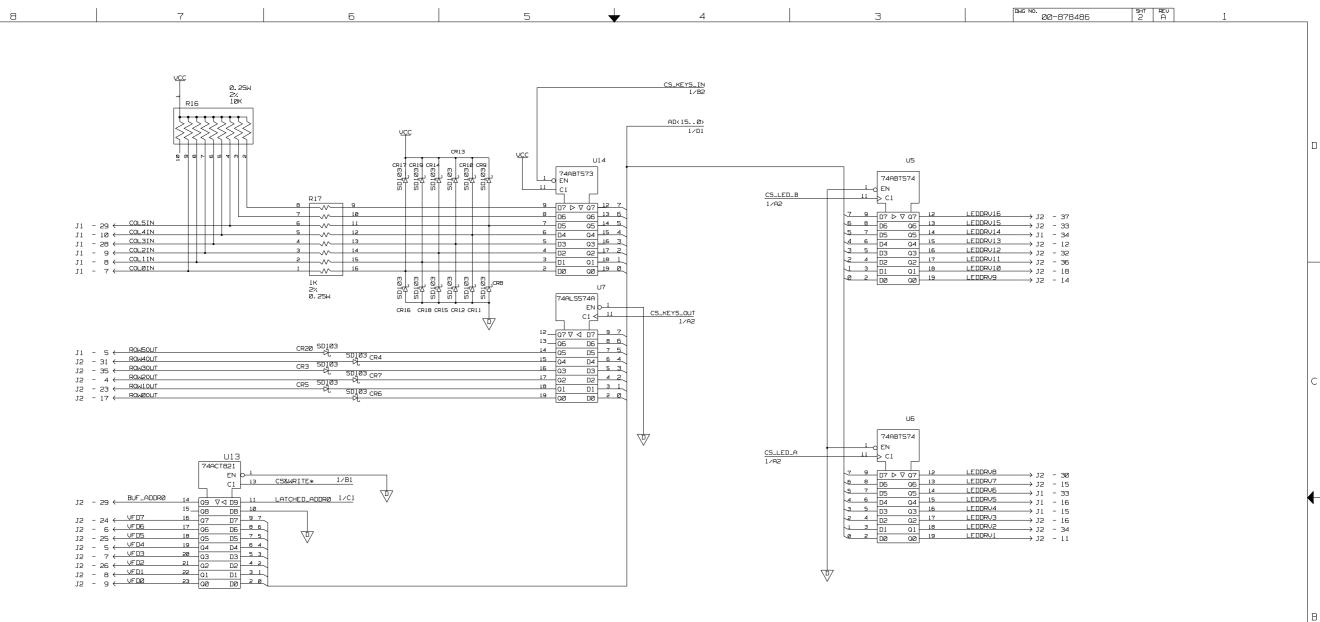
-01 SHOWN











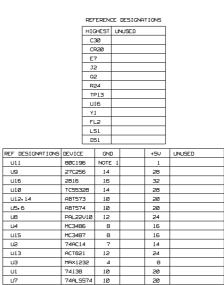
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		DICAL	SYSTEM	15,	IN	IC.
_	DWG NO.		PANEL PROC		D	REU
D		SCHEMAT	IC DIAGRA	M	'r	A
SCALE:	NONE	ØØ-878	3486	SHEET	2	

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NOTE 1: PINS 14, 36 & 68

U11

U9

U16

U10

U8

υ4

U15

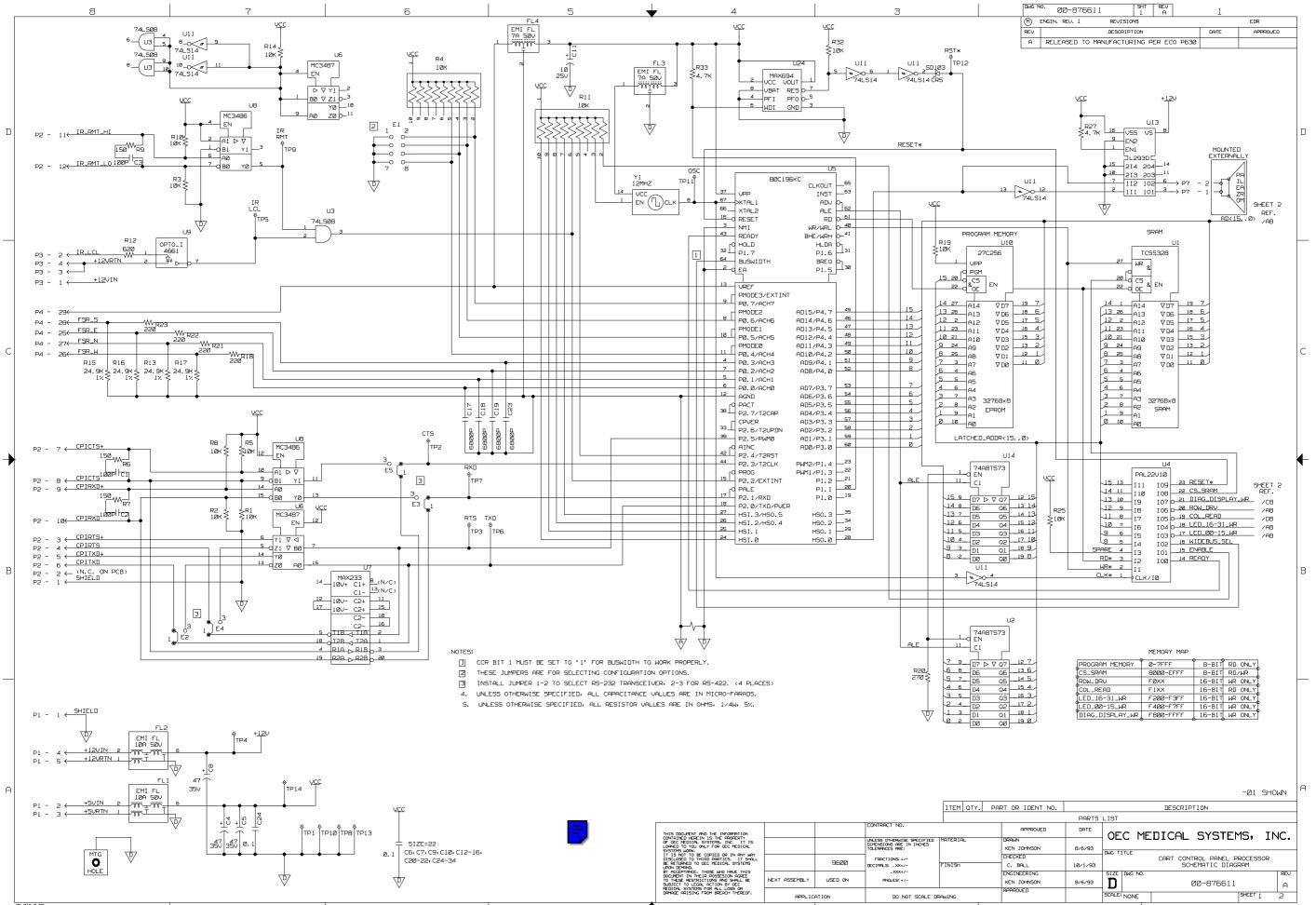
U2

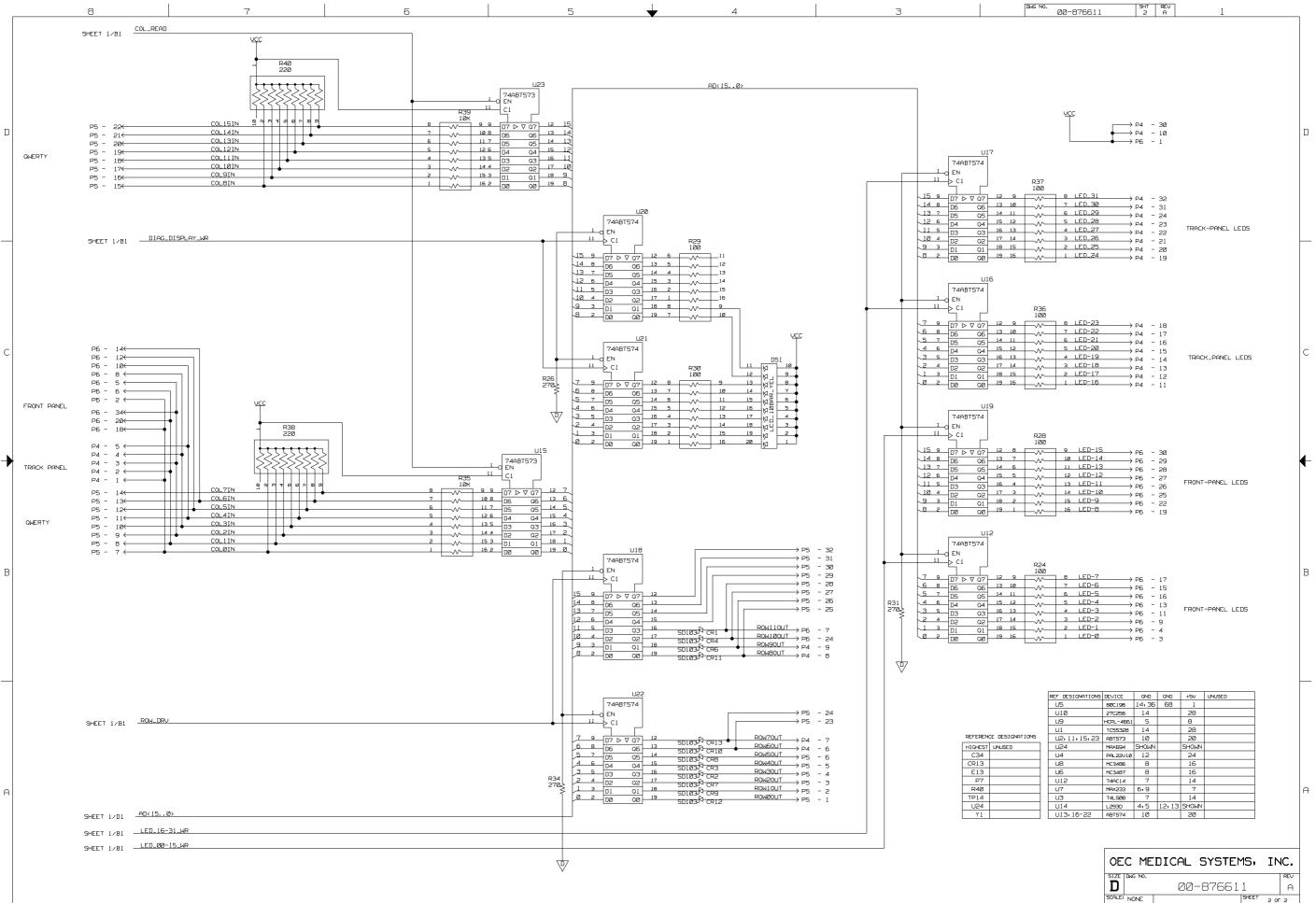
U13

U3

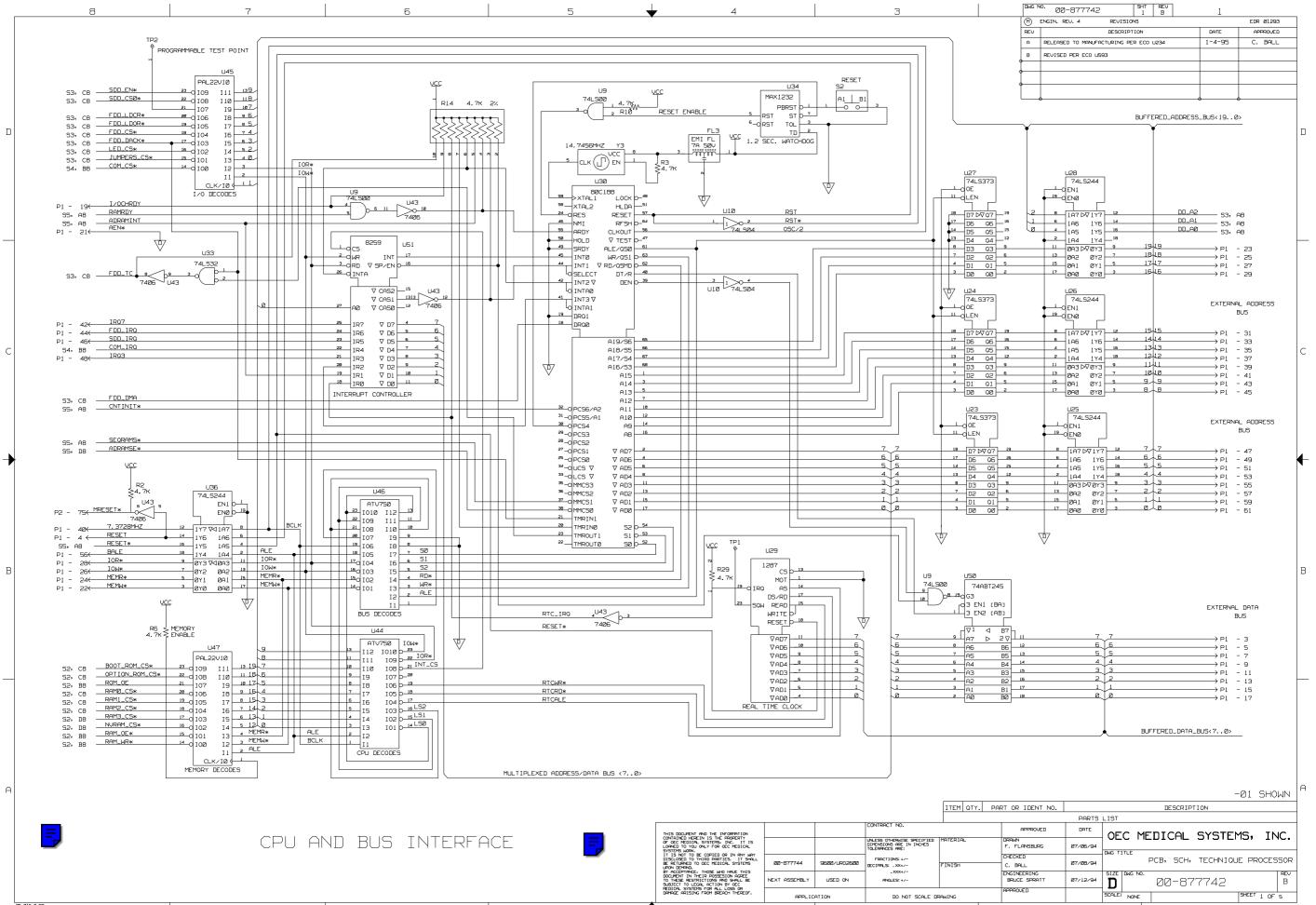
U1

U7



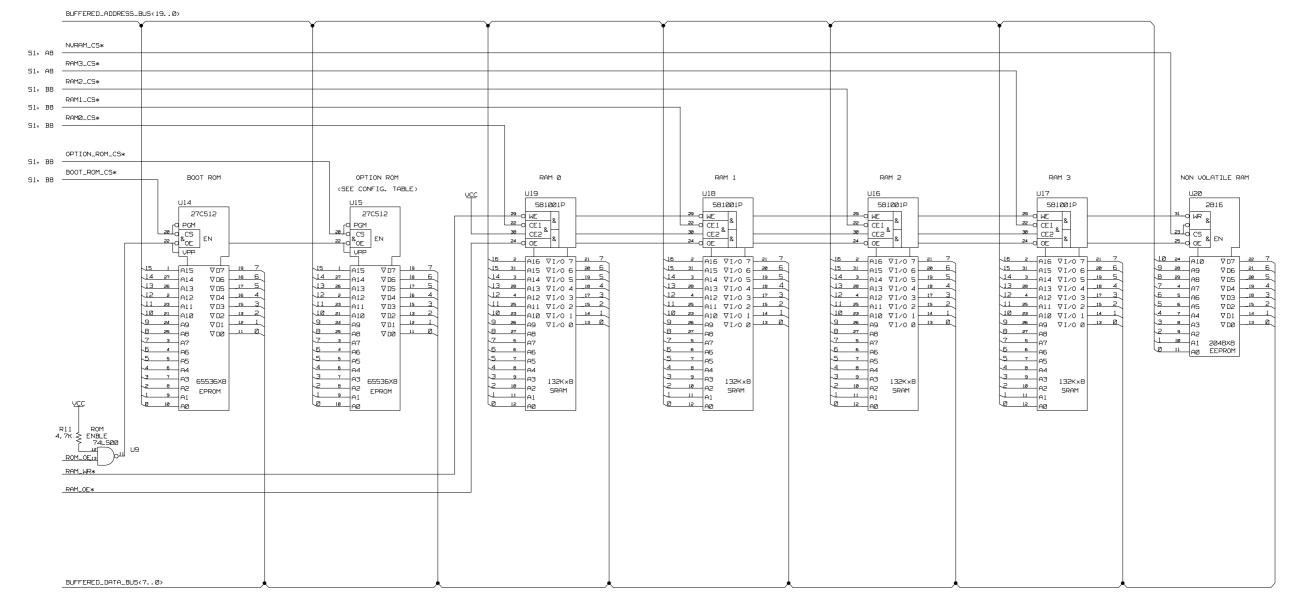


SHEET 2 OF 2



MEMORY

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D		N0.	00	-8	777.					B
SCALE:	NO	NE					SHEET	2	OF	5

DWG NO. 00-877742 SHT REV 2 B

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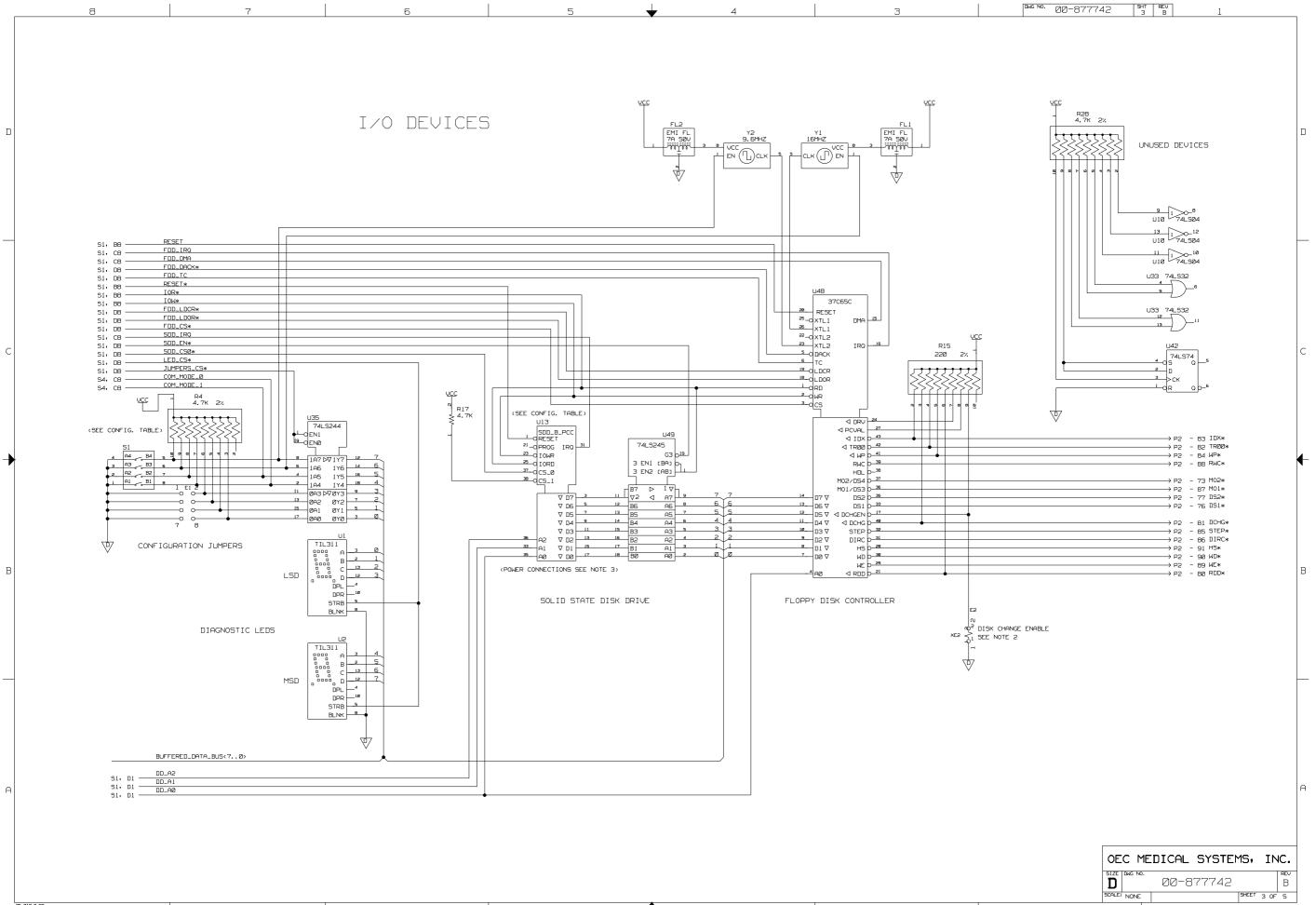
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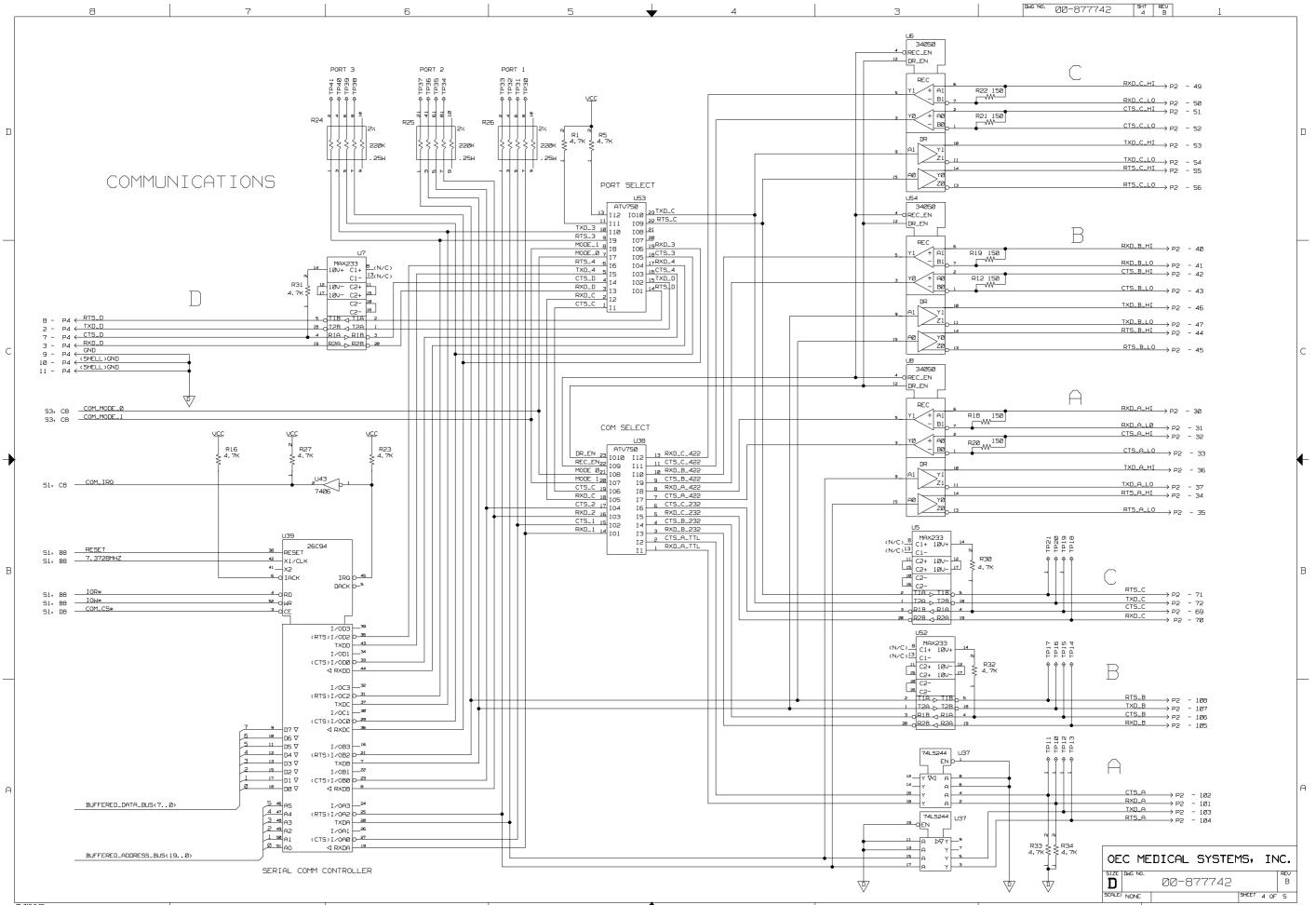
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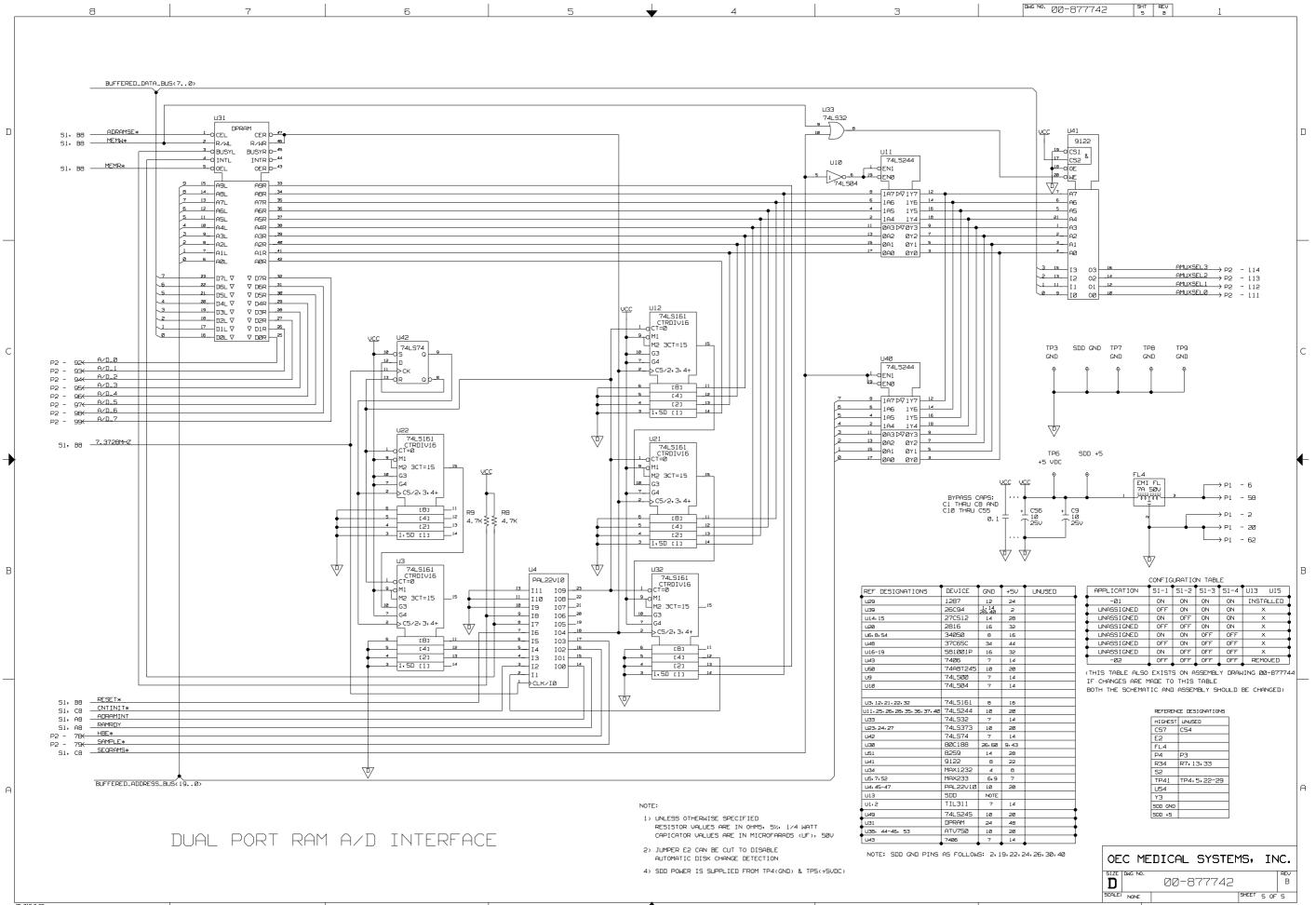
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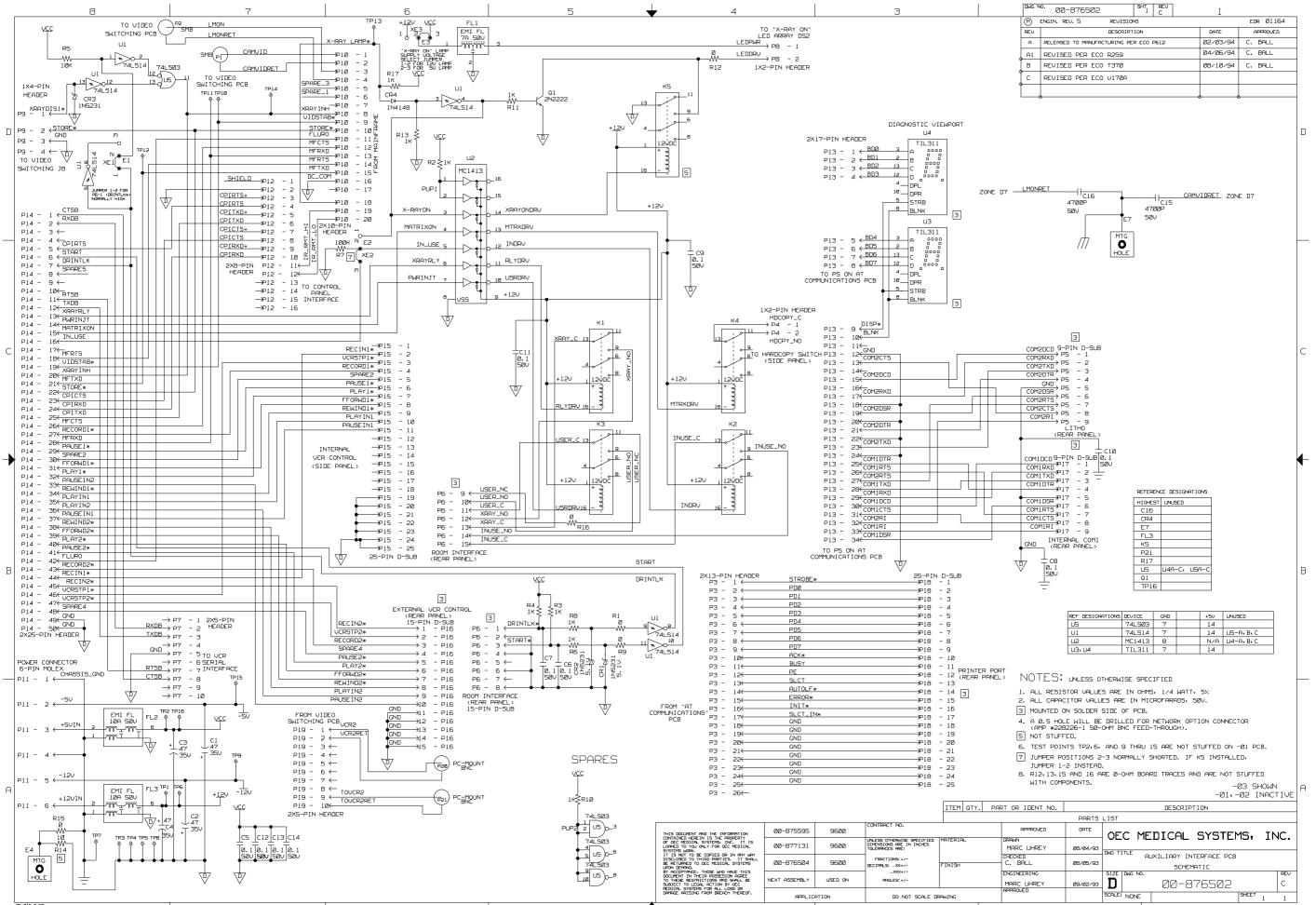
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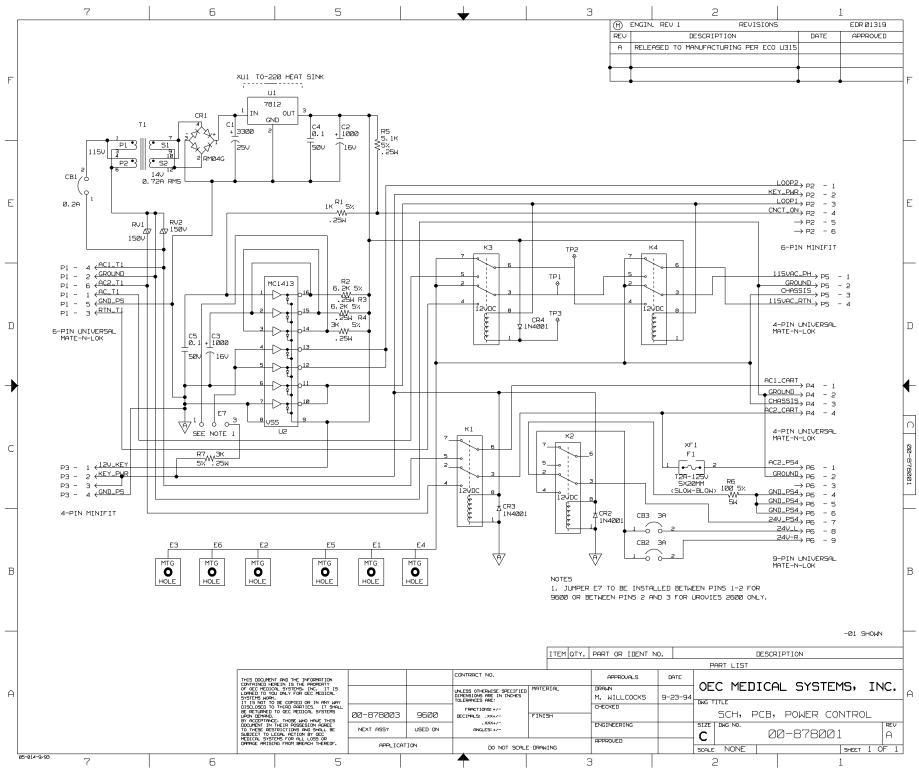
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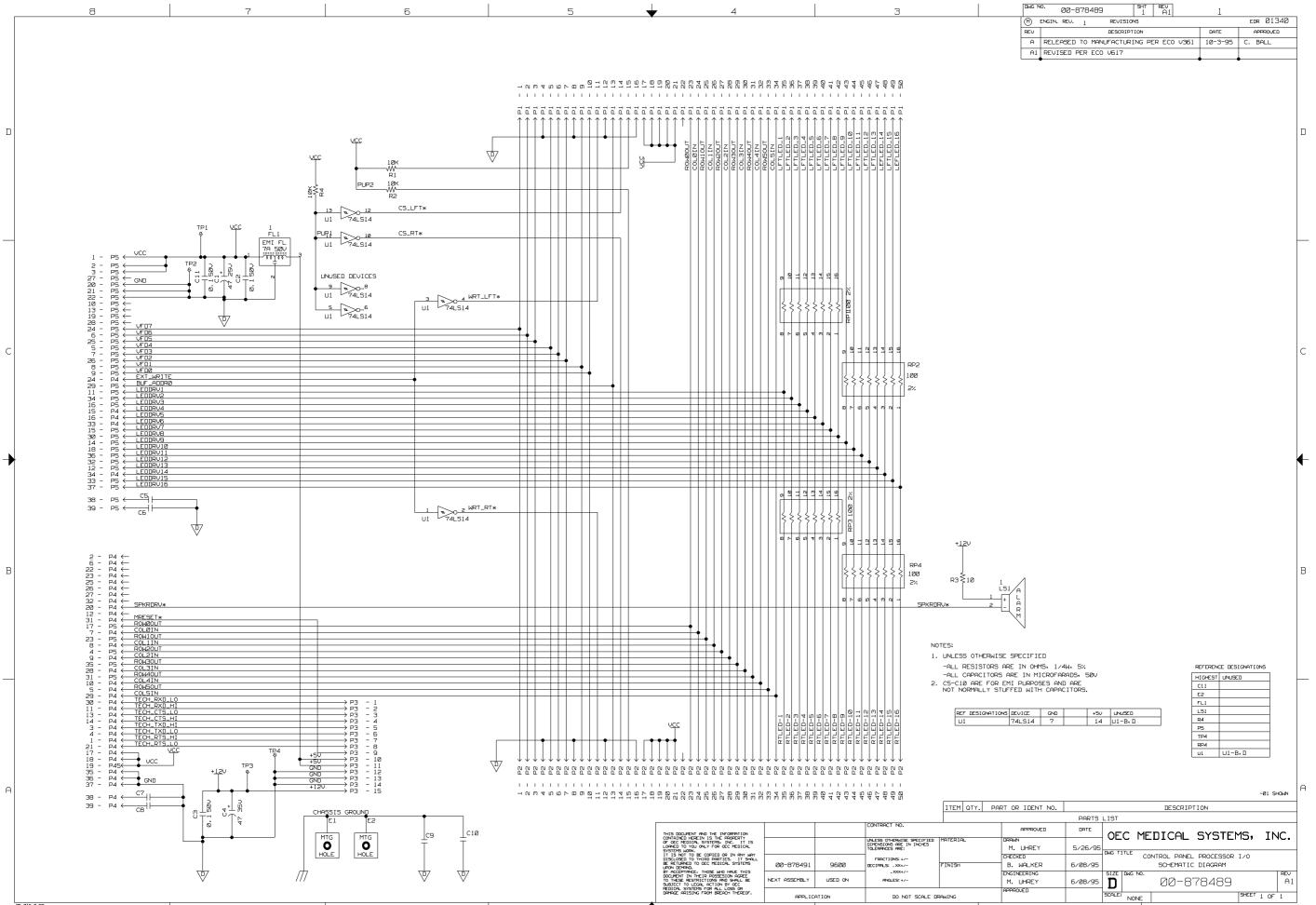












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								•						_
-														F
				CR1										
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=	4	- P4 ← - P4 ←		1N4005	KRAY_SWITCH	P10 - 2								Е
	6 7	- P4 ← - P4 ←		•	RÍ	P10 - 5	P1- 1 €	ROTATE	_115VAC_A					
	q	- P4 ← - P4 ← - P4 ←			150 +5V >	P10 - 1	PI- I 4 P1- 2 4	ROTATE	_115VAC_A		→ P3 - 1 → P3 - 2			
	11	- P4 (- P4 (- P4 (,	K_RAY LAMP	P10 - 3	P1- 3 €				→ P3 - 3			
1	14	- P4		[]E	EMEROF_HI	- P9 - 1 - P9 - 2	P1- 4 € P1- 5 €	ROTATE	_115VAC_B		→ P3 - 4 → P3 - 5			
	16 17	- P4 ←			L,	· P9 - 3			_115VAC_B		→ P3 - 6			
	19	- P4 - P4 - P4 - P4		E		P8 - 1 P8 - 2	P1- 7 € P1- 8 €	<u></u>			→ P3 - 7 → P3 - 8			
							P1- 9 € P1- 10 €	ROTATE	_AC_RTN _AC_RTN					
					TEC_RXD_LO									
					TECH_CTS_LO		P2- 1 € P2- 2 €	LIFT_D	P_SW N_SW		→ P11 - 3 → P5 - 3			
					TECH_TXD_LO (13 - 6	P2 - 3 6	ROT_Ch	_SW		→ P12 - 3			
▶	1 2 3	- P6 (TECH_RTS_LO	J3 - 7 J3 - 8	P2- 4 € P2- 5 €	ROT_CC	_SW _RTN_EN		→ P7 - 3 → P11 - 2			
	4		•		MRESET*	- J3 - 9 - J3 - 10	P2 - Б.€	SPARE1			→P5 - 2			
	6 7	- P6			+50	· J3 - 11 · J3 - 12	P2- 7 € P2- 8 €				→ P12 - 2 → P7 - 2			0
	9 10	- P6	•		GND ;	J3 - 13 J3 - 14	P2- 9 €	- SPARES			→ P11 - 1			
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		TI C	THIS DOCUMENT AND THE INFORMATION			ONTRACT NO.	MATERIAL	APPROU	ALS DATE		EDICA .	SYSTEN	15, INC.	
ا			INTS DOCUMENT AND THE INFORMATION CONTAINED HEREIN IS THE PROPERTY Y GC MEDICAL SYSTEMS INFO. IT IS YSTEMS WORK, ONLY FOR OCC MEDICAL ITS NOT OB ECOPELD OR IN ANY MAY ISSUEDSED TO THIRD PARTIES. IT SHAL BE RETURNED TO BE COPELD OR IN ANY MAY INFOLOTION TO BE OPPOSITION STREFT			LESS OTHERWISE SPECIFIED MENSIONS ARE IN INCHES DLERANCES ARE:	DELEKTEL	B. 1	ALKER 4-27-9				, TIAC.	A
		B	DISCLOSED TO THIRD PARTIES, IT SHALL BE RETURNED TO DEC MEDICAL SYSTEMS JPON DEMAND. 37 ACCEPTANCE, THOSE WHO HAVE THIS	00-878494	9600 1	FRACTIONS: +/- ECIMALS: . XX+/-	FINISH	CHECKED M. UHRE		5 CO	DLUMN I/O	PCB SC		
			37 ACCEPTINCE, THOSE WHO HAVE THIS DOCUMENT IN THEIR POSSESION AGREE TO THESE RESTRICTIONS AND SHALL BE SUBJECT TO LEGAL ACTION BY OEC TEDICAL SYSTEMS FOR ALL LOSS OR DAMAGE ARISING FROM BREACH THEREOF.	NEXT ASSY	USED ON	.XXX+/- ANGLES:+/-		ENGINEERIN B. WALK		5 C	00-8	78492	REV	
Ļ		D	AMAGE ARISING FROM BREACH THEREOF.	APPLICAT	ION	DO NOT SCALE	DRAWING	APPROVED		SCALE N/A			SHEET 1 OF 1	
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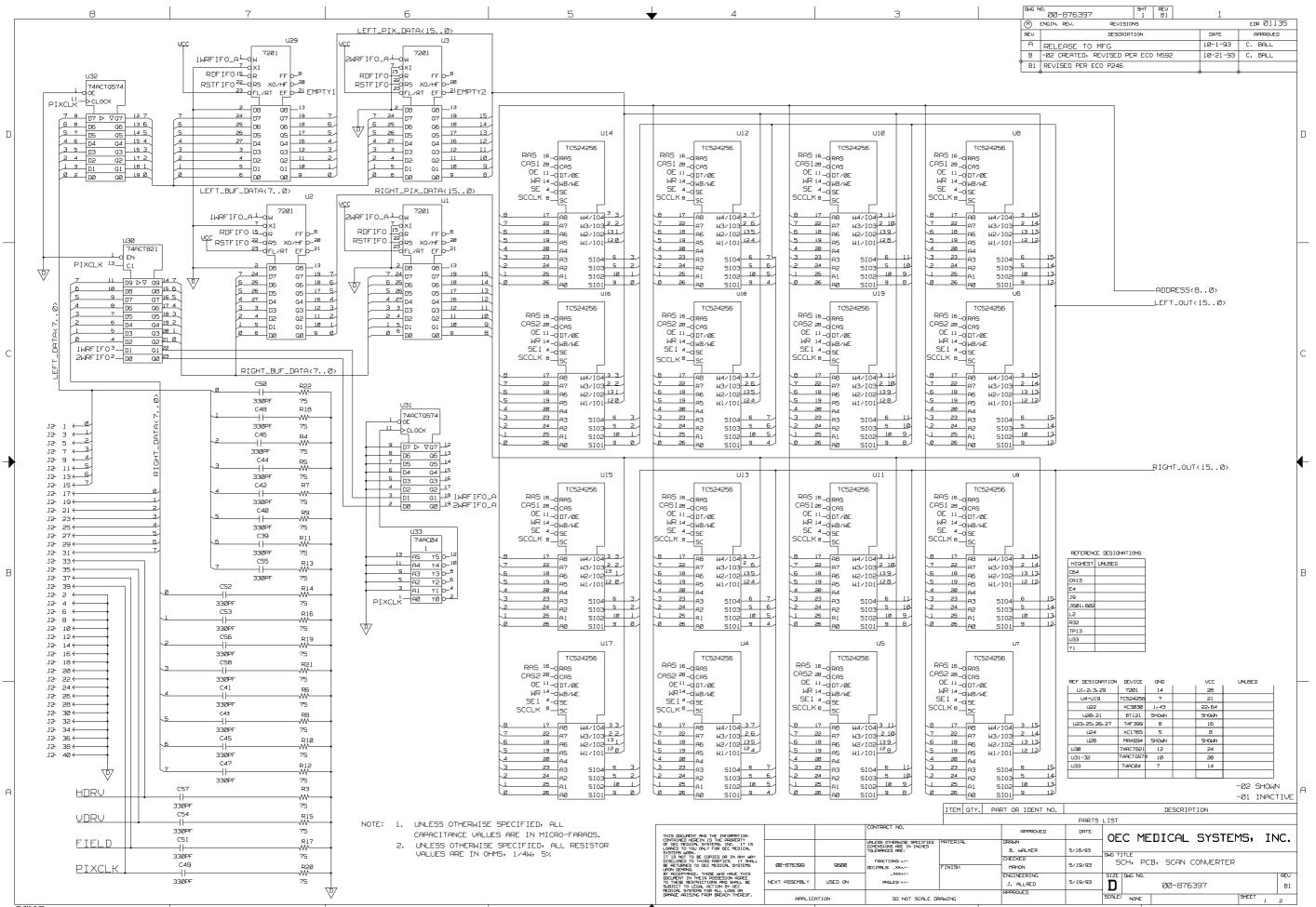
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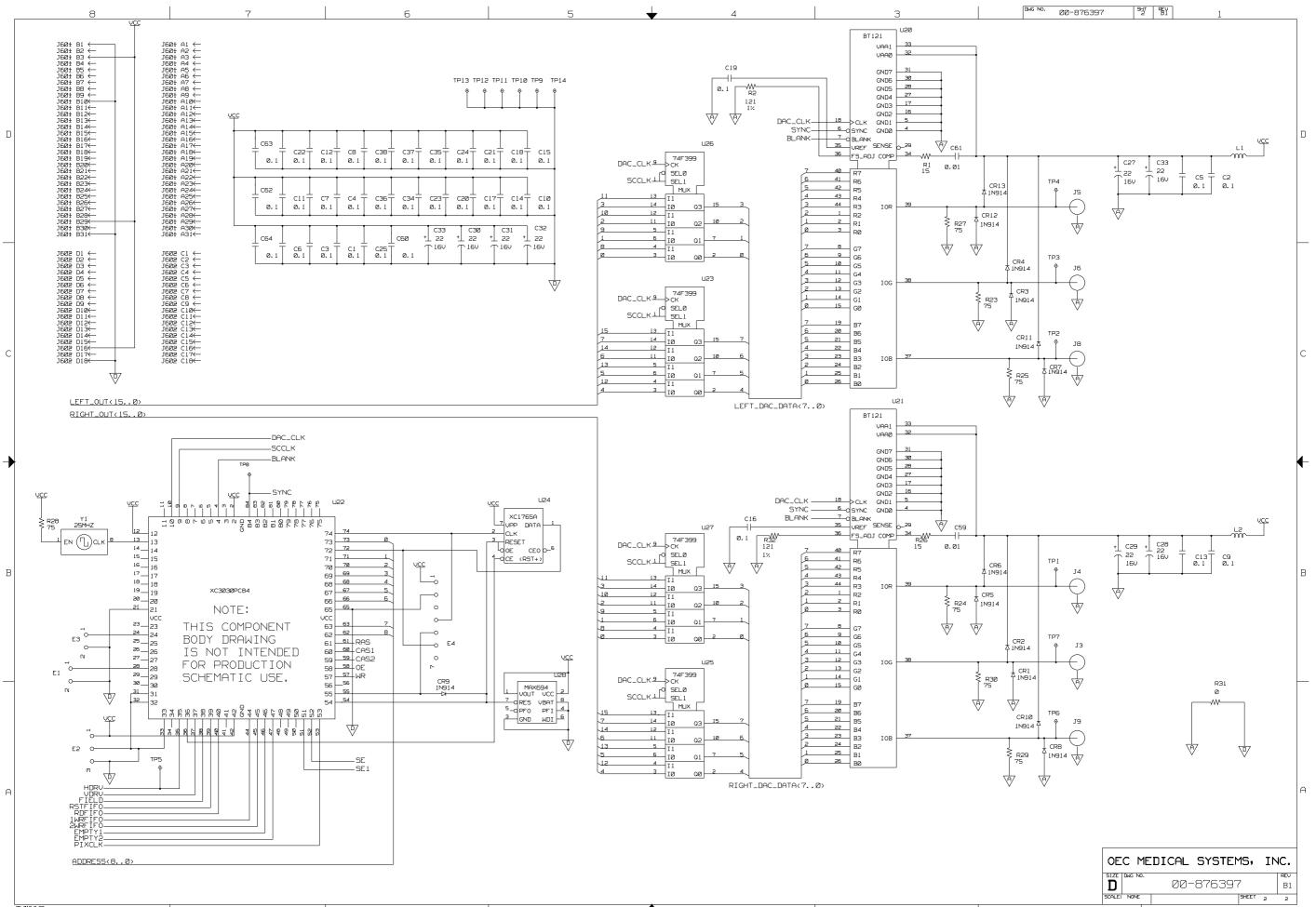
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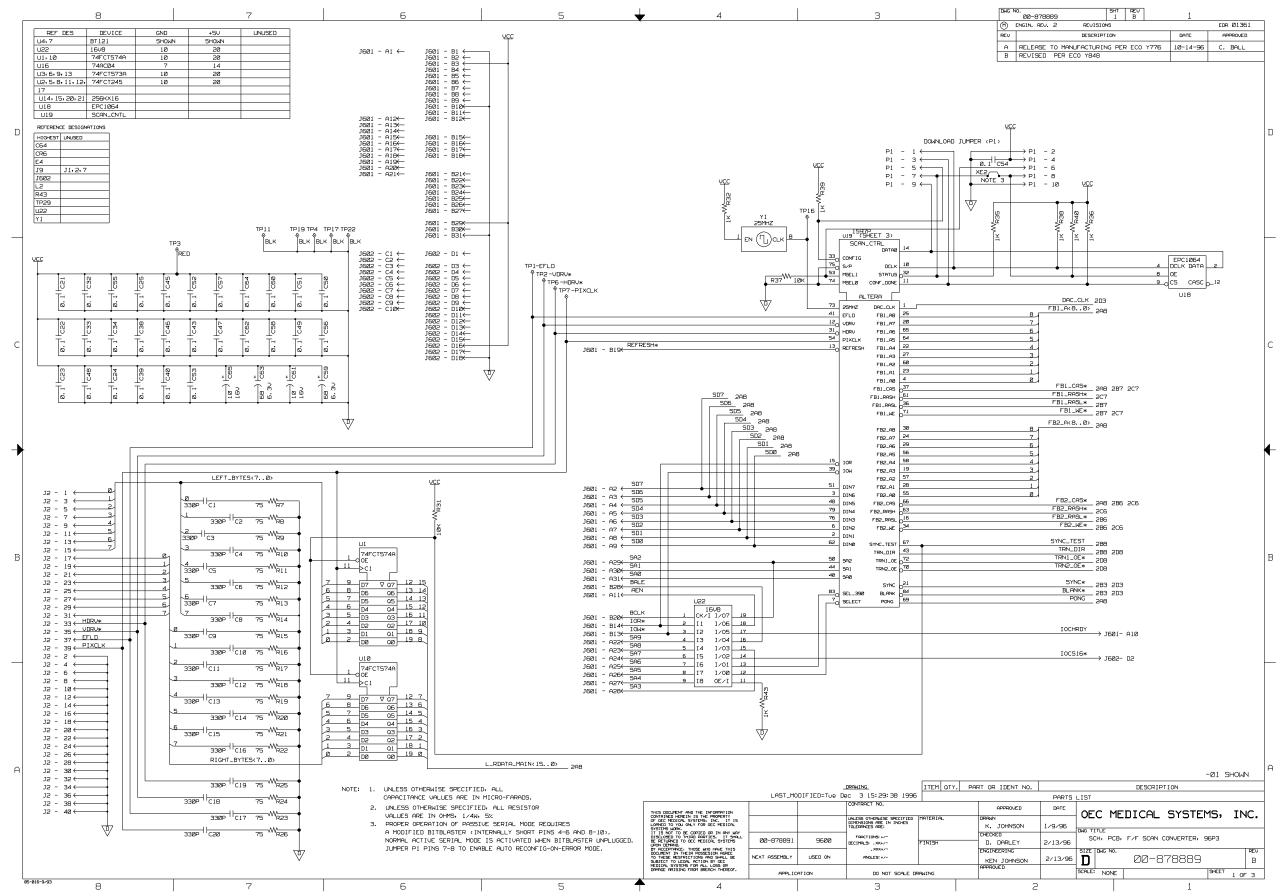
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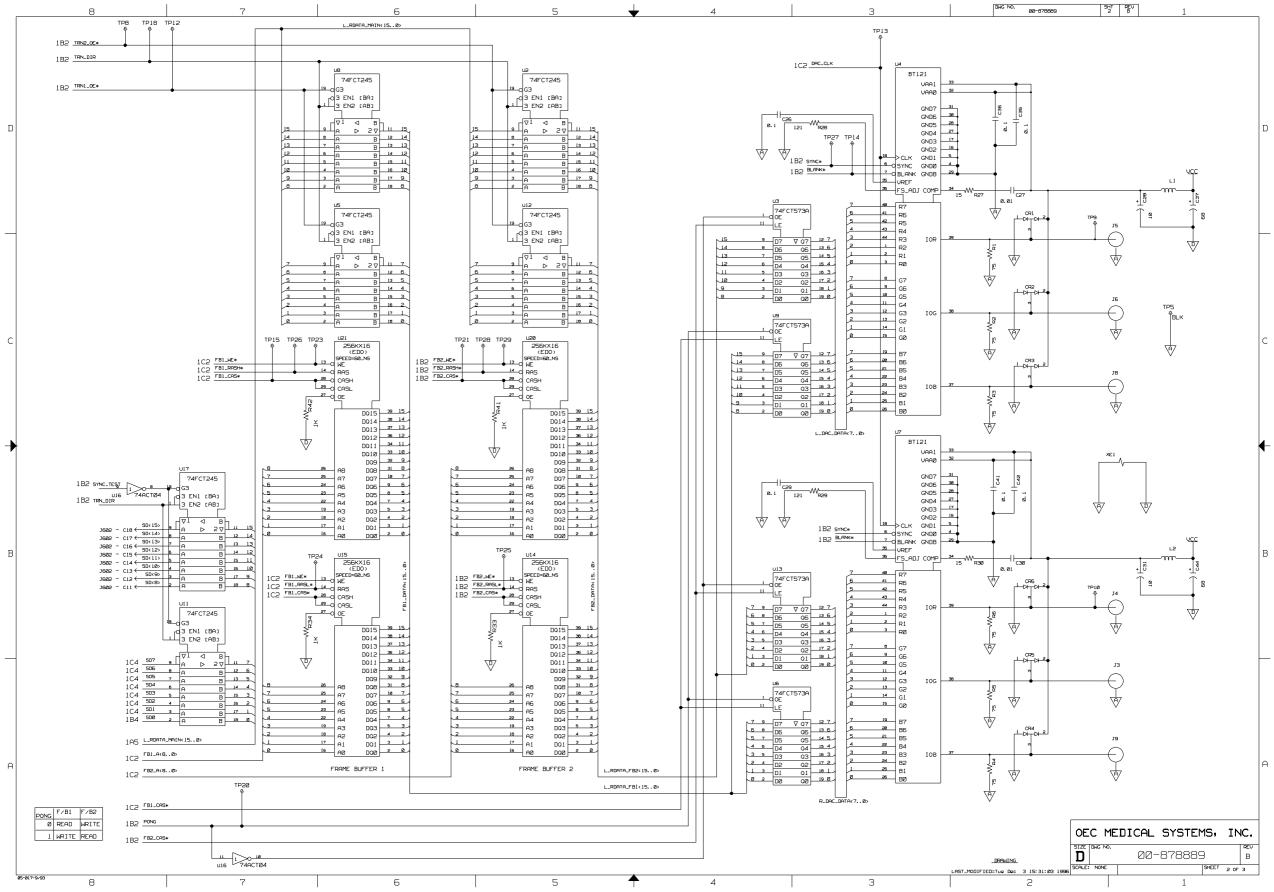
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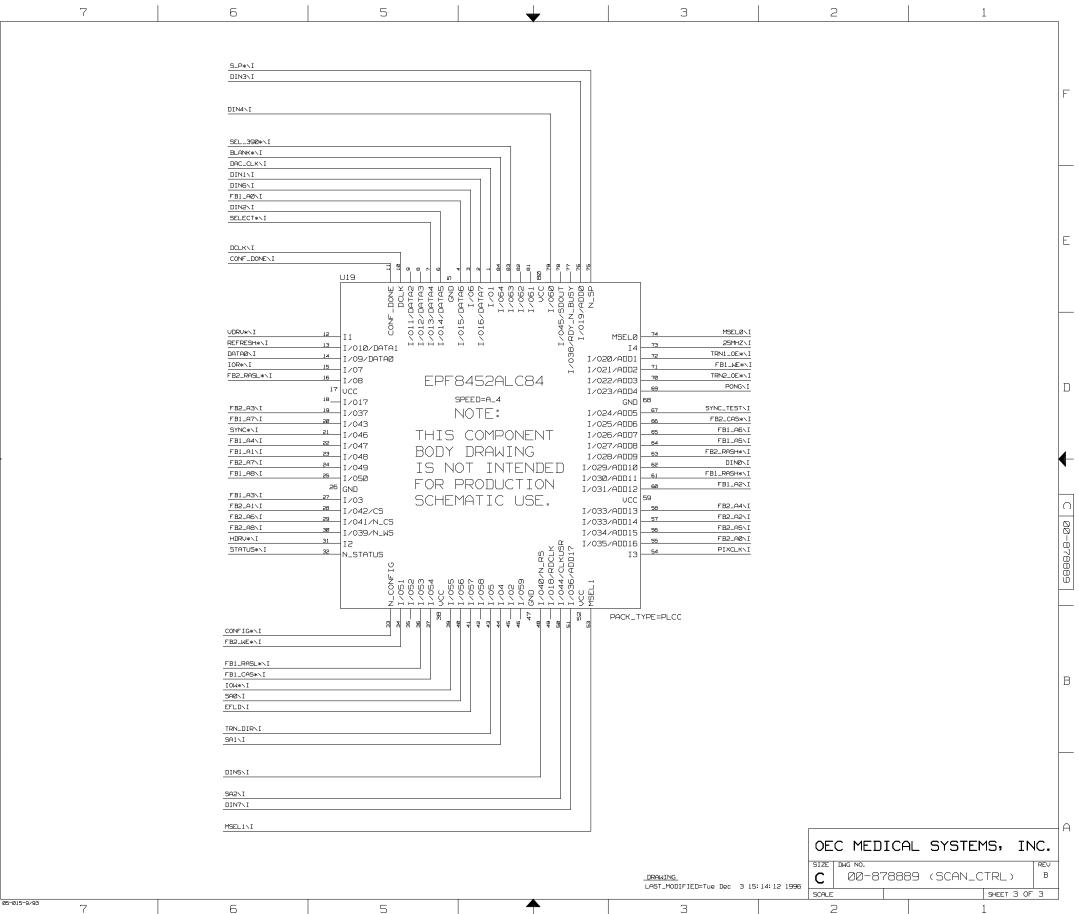
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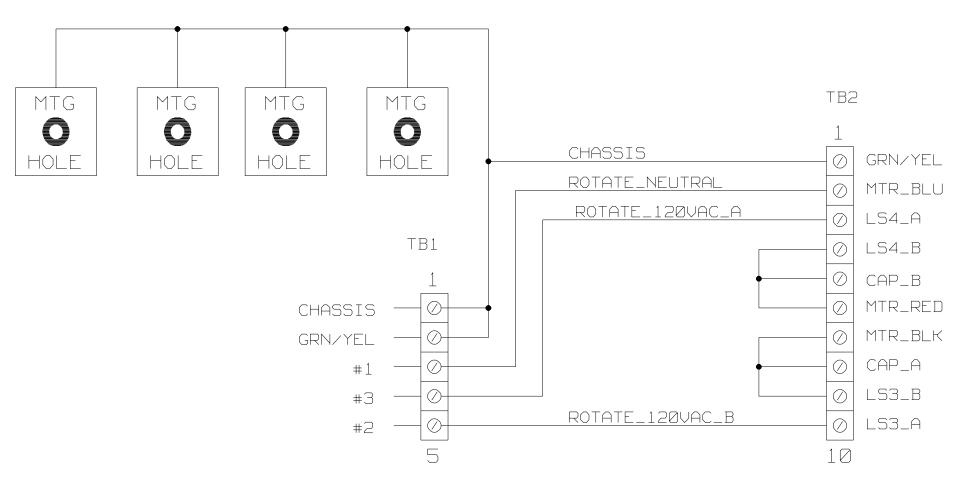
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P1 - 1 LINE F2 - 1 P1 - 2 LINE F2 - 1 P1 - 3 LINE F2 - 1 P1 - 3 LINE F2 - 2 P1 - 4 LINE F2 - 2 P1 - 5 LINE F2 - 2 P1 - 6 LINE F2 - 2 P1 - 7 LINE F2 - 2 P1 - 8 LINE LINE P1 - 8 LINE LINE P1 - 8 LINE LINE P1 - 9 LINE LINE							REV		DESCRIPTION			1
P1 - 1 LINE P1 - 2 LINE P1 - 2 LINE P1 - 3 LINE P1 - 4 LINE P1 - 5 LINE P1 - 6 LINE P1 - 7 LINE P1 - 8 LINE P1 - 9 LINE P1 - 1 LINE P1 - 2 LINE P1 - 3 LINE P1 - 9 LINE										2-11-94	C. BALL	1
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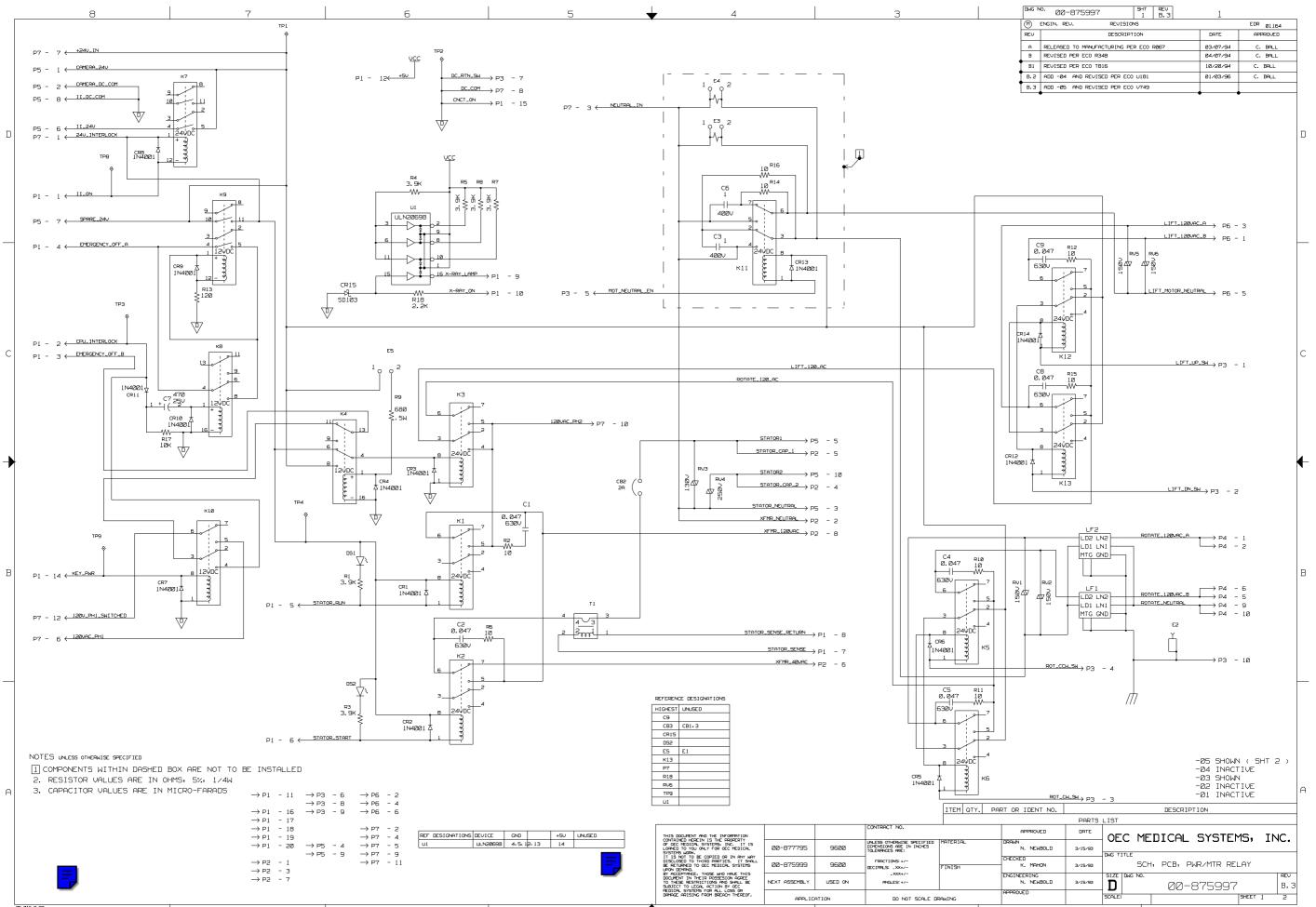
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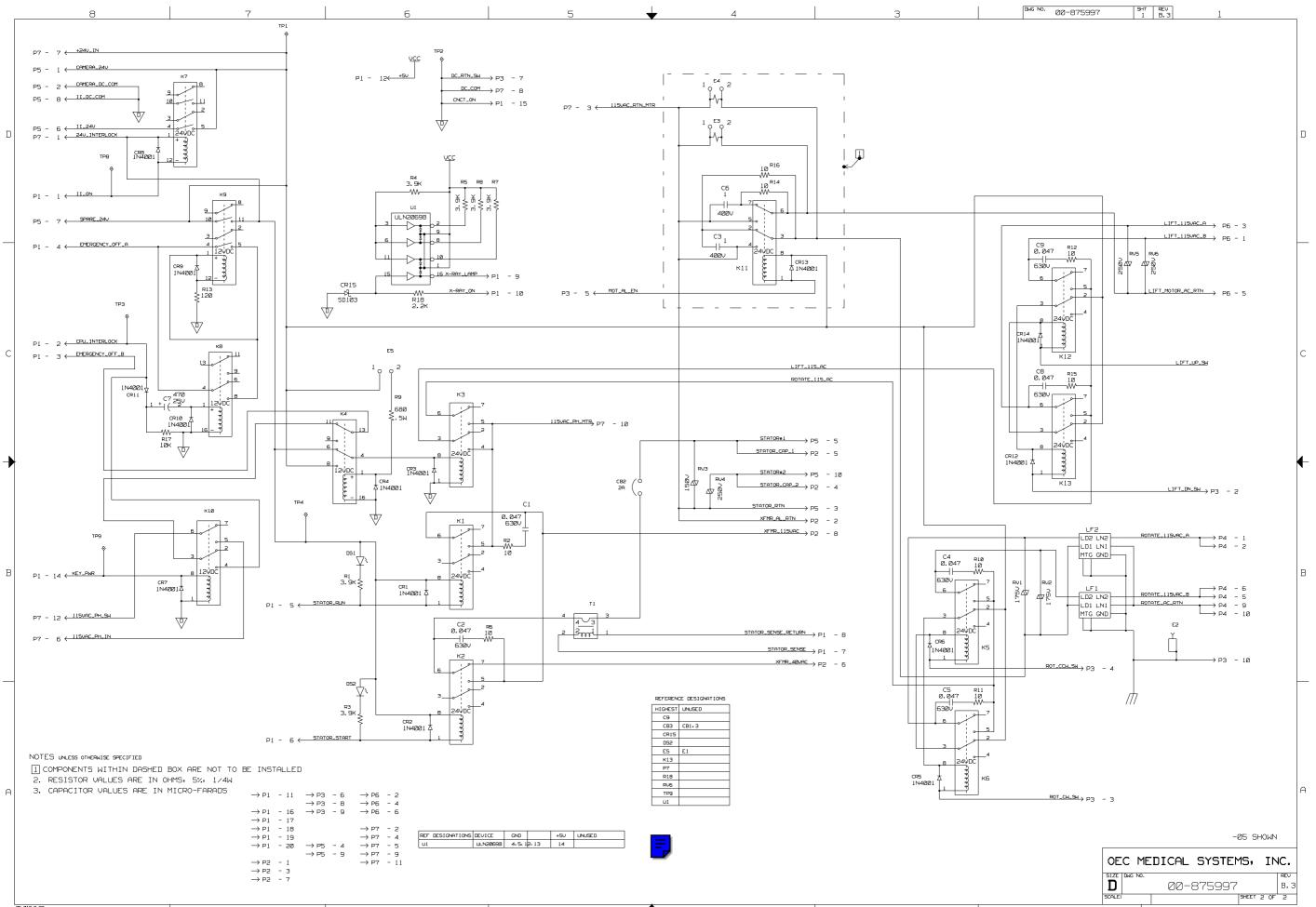
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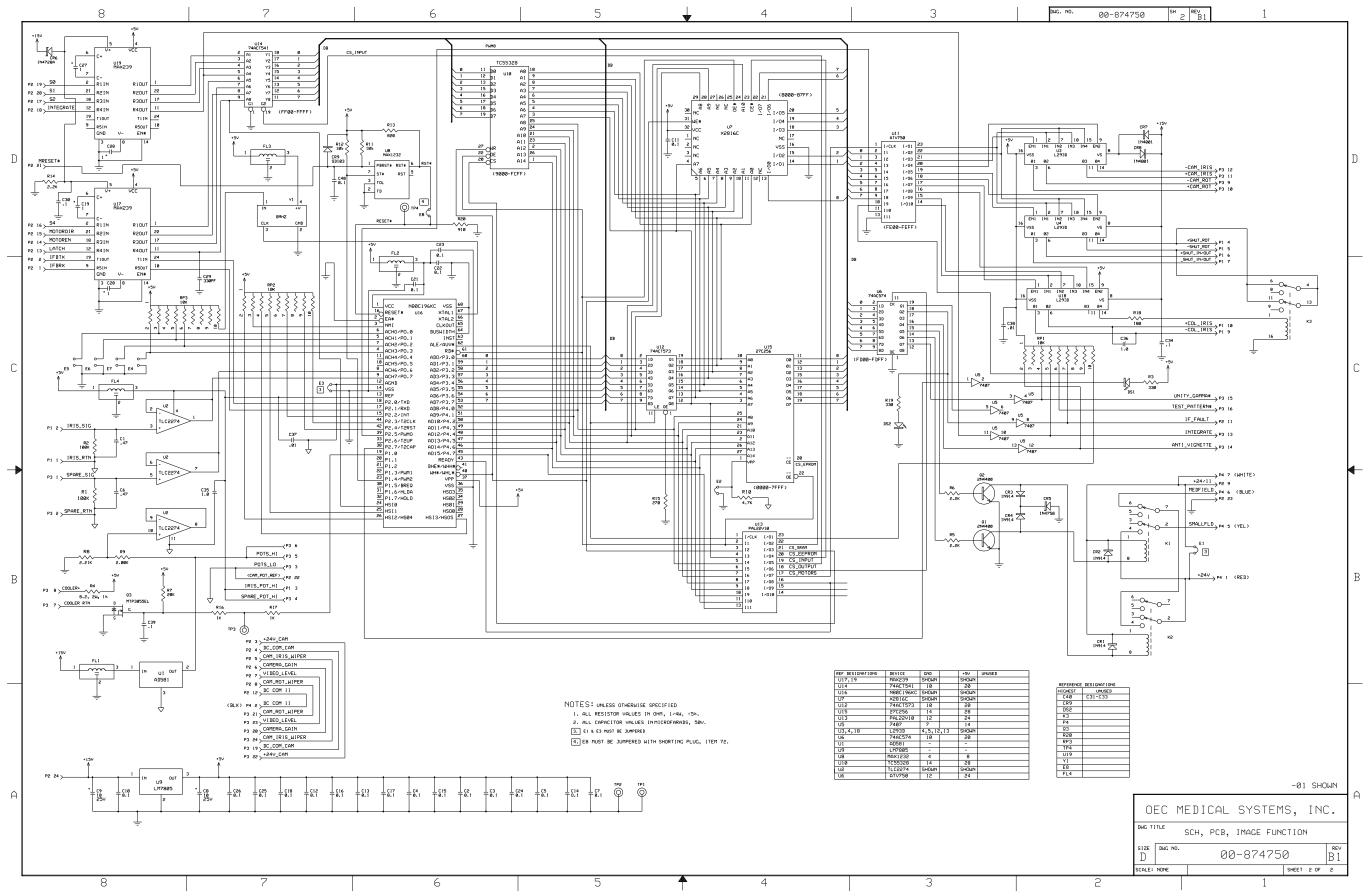
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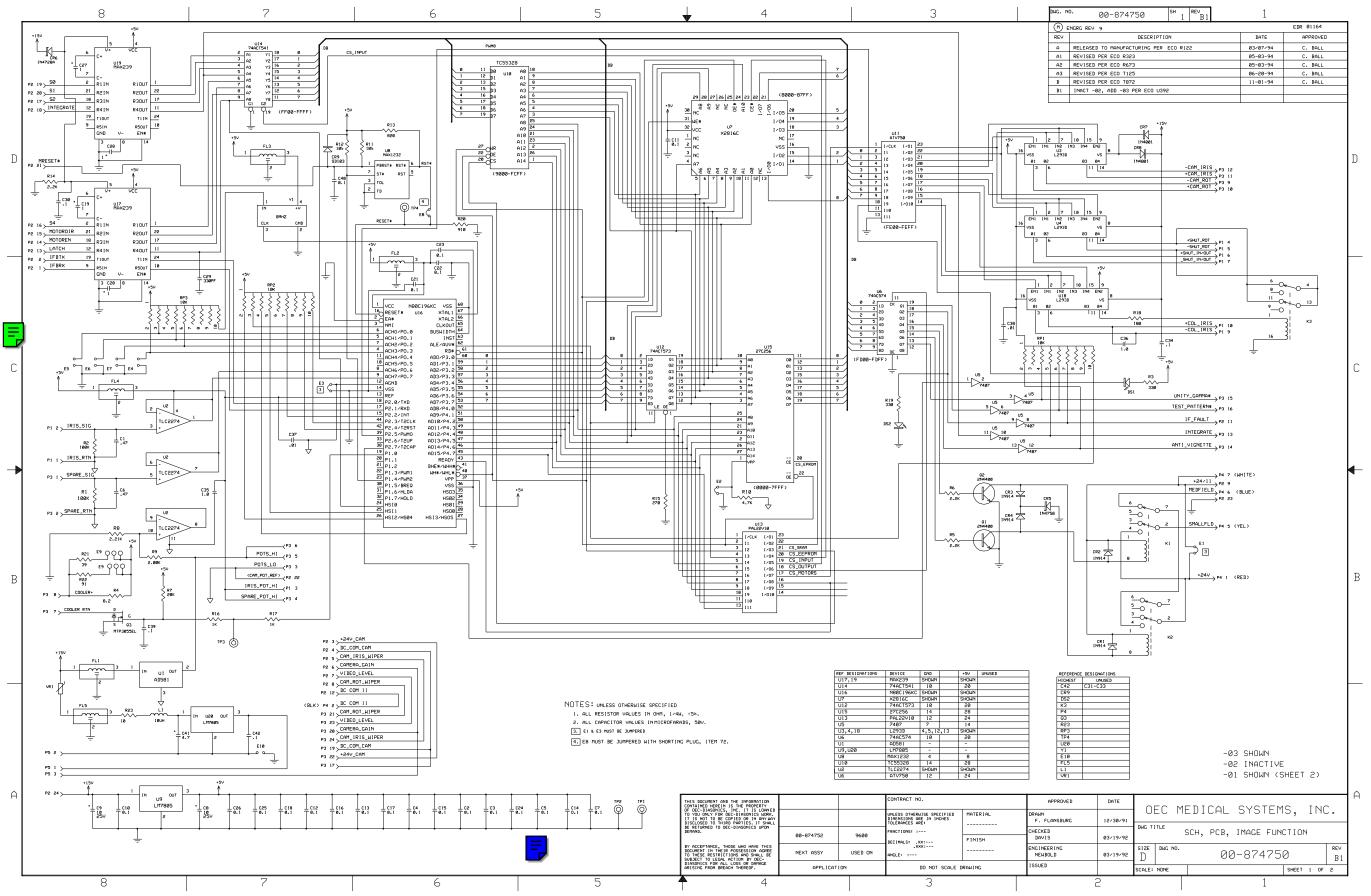
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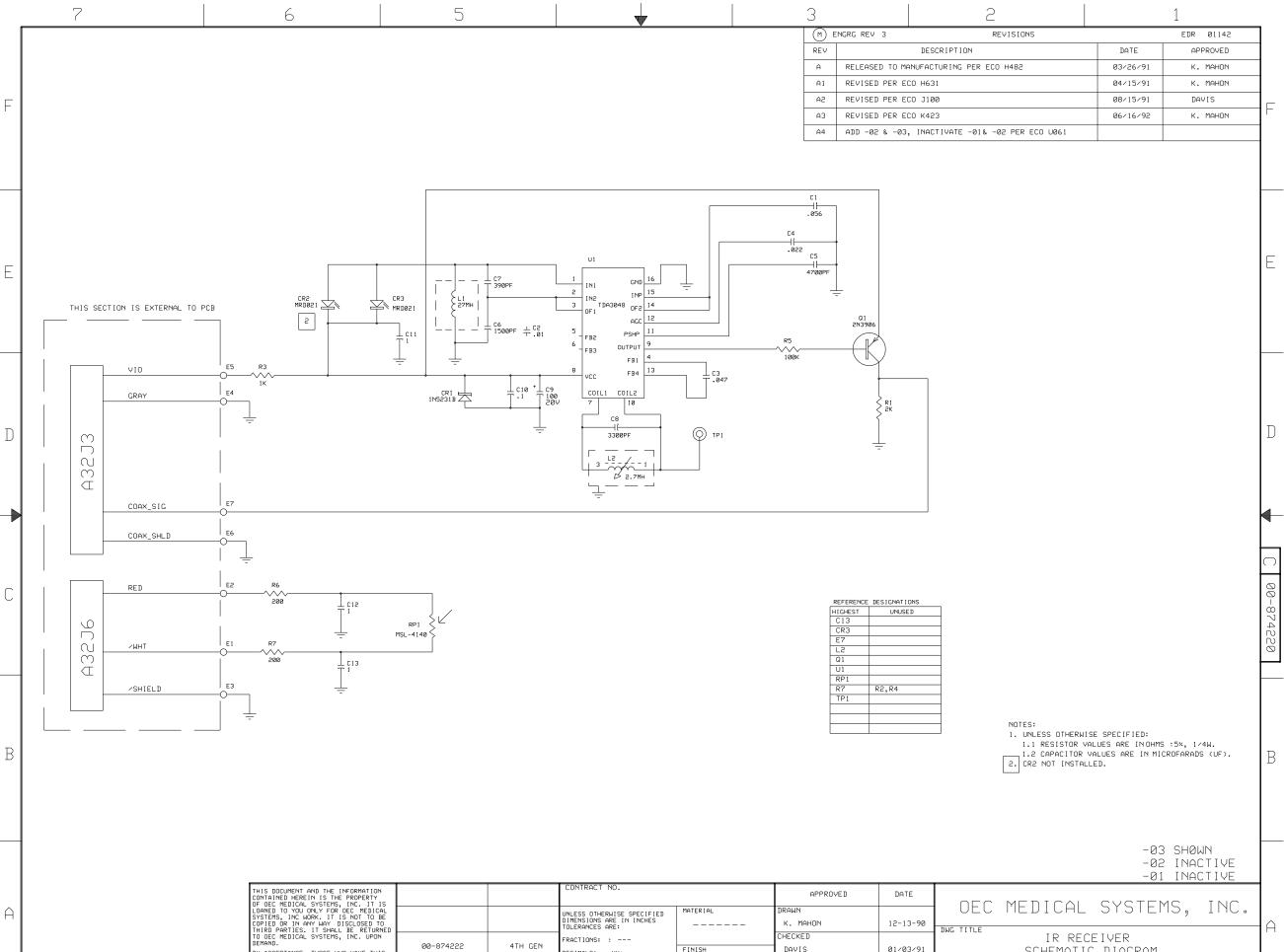




Electrical Schematic Diagram 00-875972, Image Processor, is not included in this package. The diagram is not currently available in electronic form.







FINISH

DAVIS

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SCHEMATIC DIAGRAM

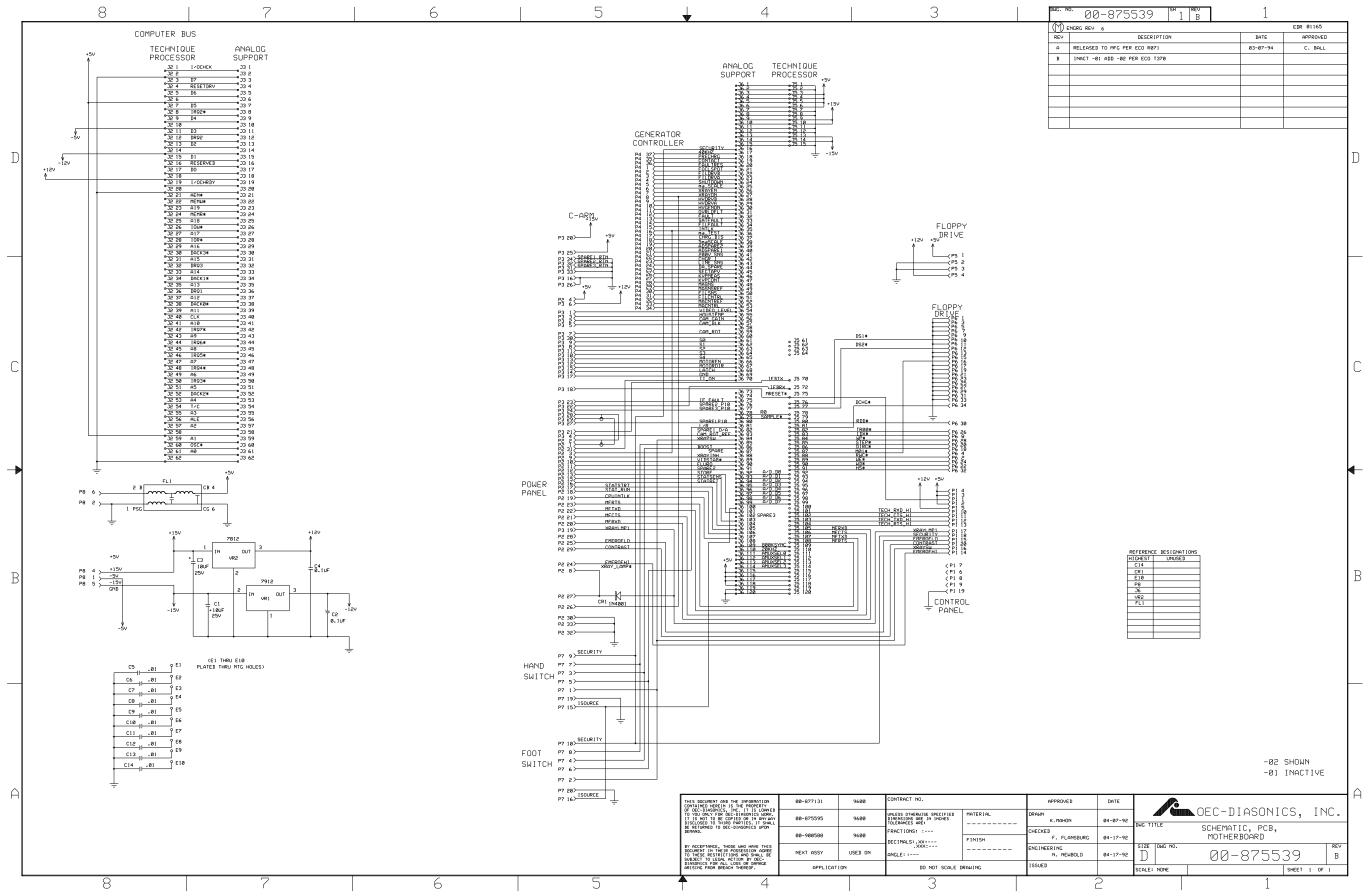
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l				В	REVISED PER ECO	J U580		
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P1 2 ROTATE_120VAC_A	<i>*</i>						¹	
ROTATE 120VAC B							1	
P1 5 ROTATE_120VAC_B P1 6 ROTATE_120VAC_B	→ P3 3							
PI 9 ROTATE_NEUTRAL PI 10 ROTATE_NEUTRAL	→ P3 6							
P2 1 LIFT_UP_SW	→ P4 3							
P1 9 ROTATE_NEUTRAL P1 10 ROTATE_NEUTRAL P2 LIFT_UP_SW P2 LIFT_DN_SW P2 ROT_CW_SW P2 ROT_CCW_SW P2 ROT_NEUTRAL_EN P2 SPARE1 P2 SPARE2 P2 SPARE3	→ P5 3 → P6 3							
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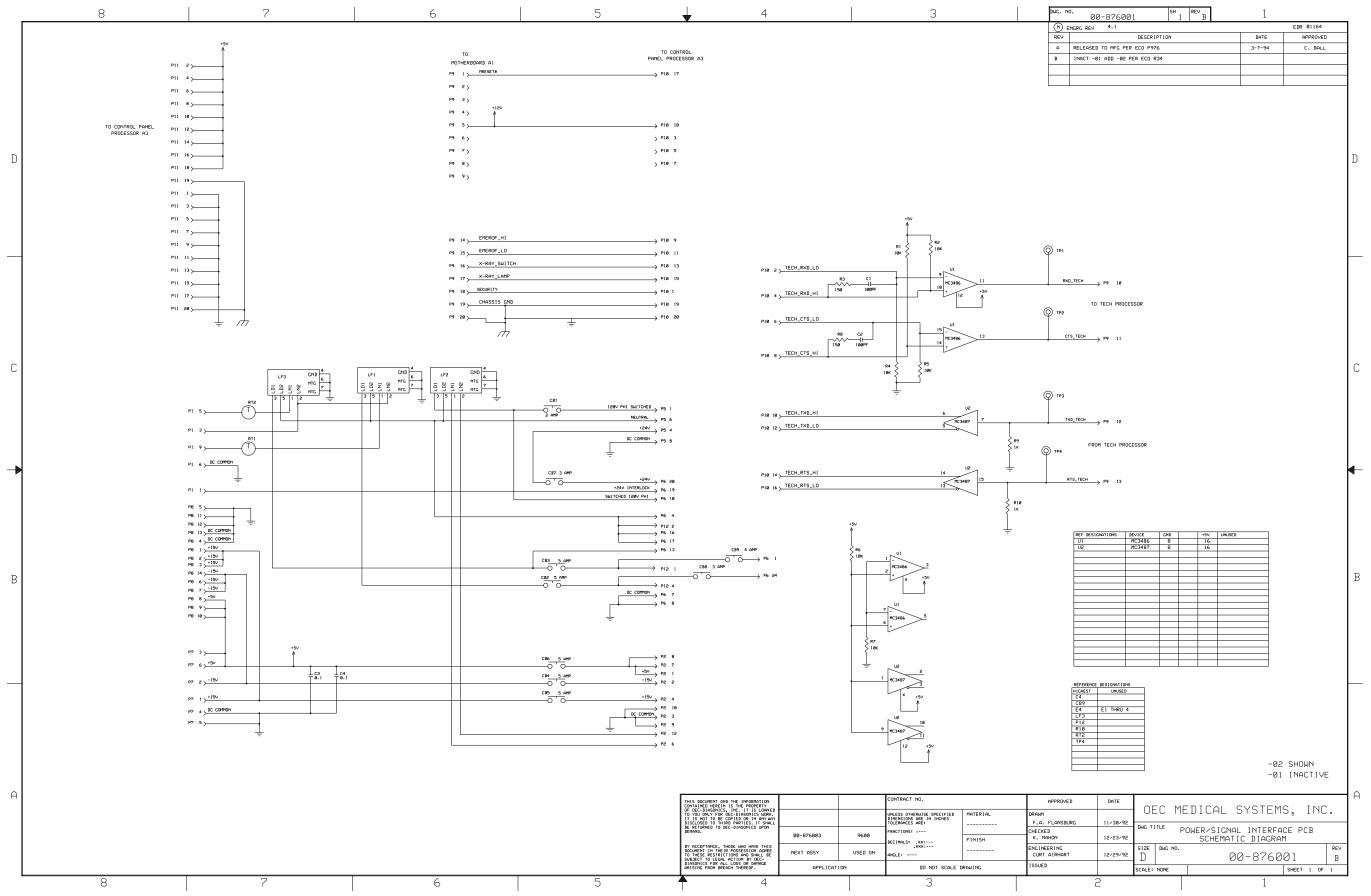
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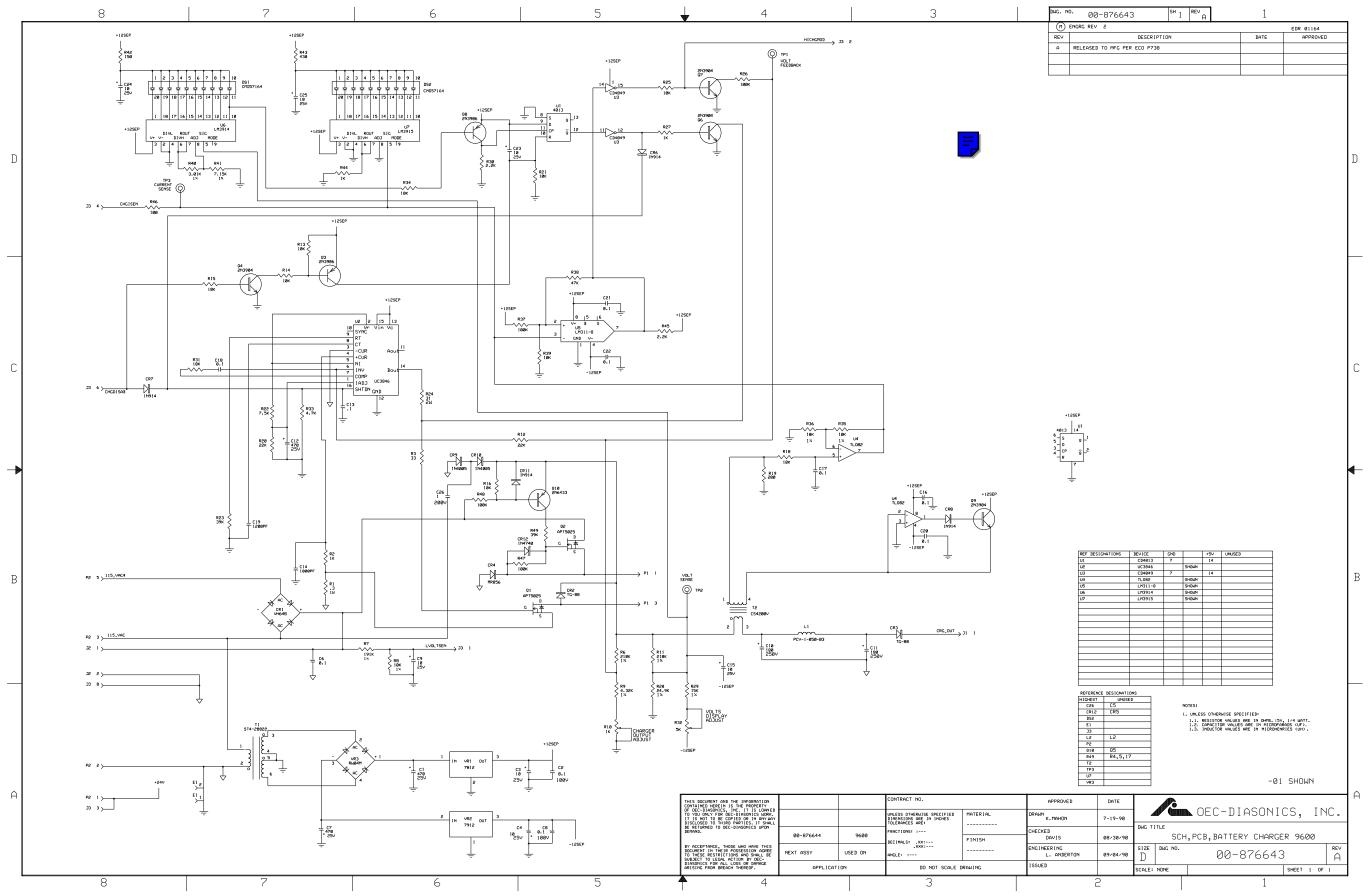
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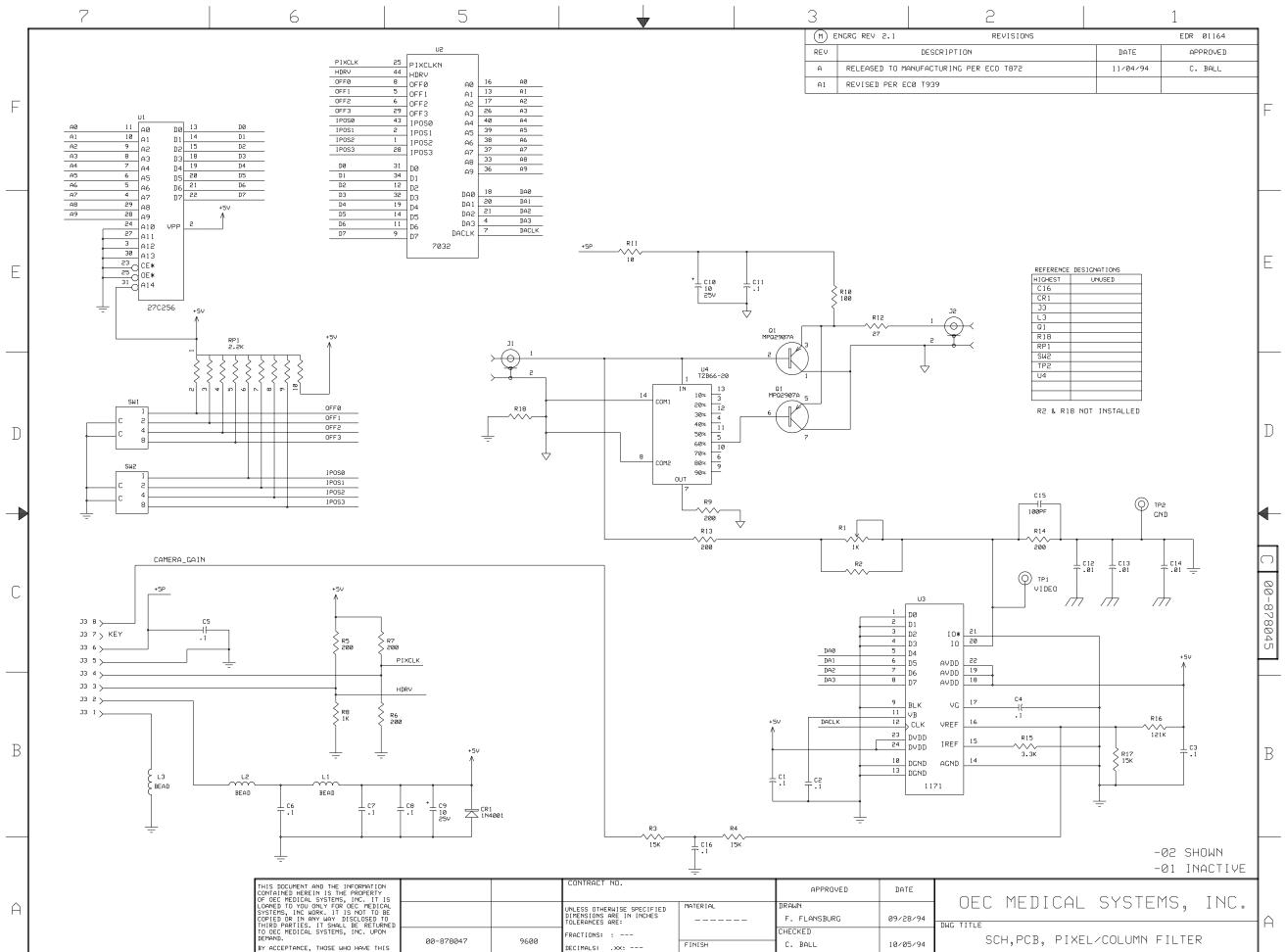
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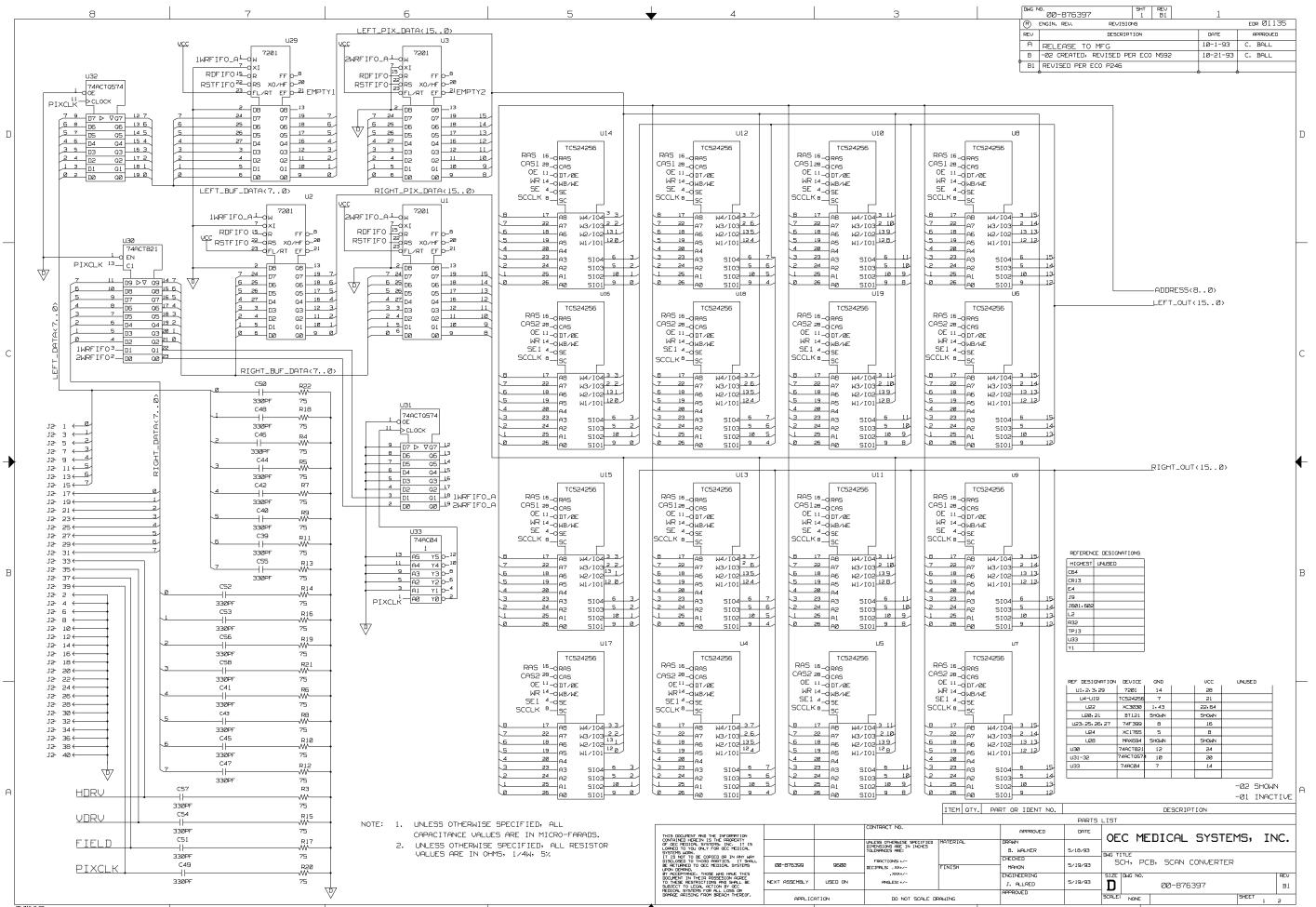
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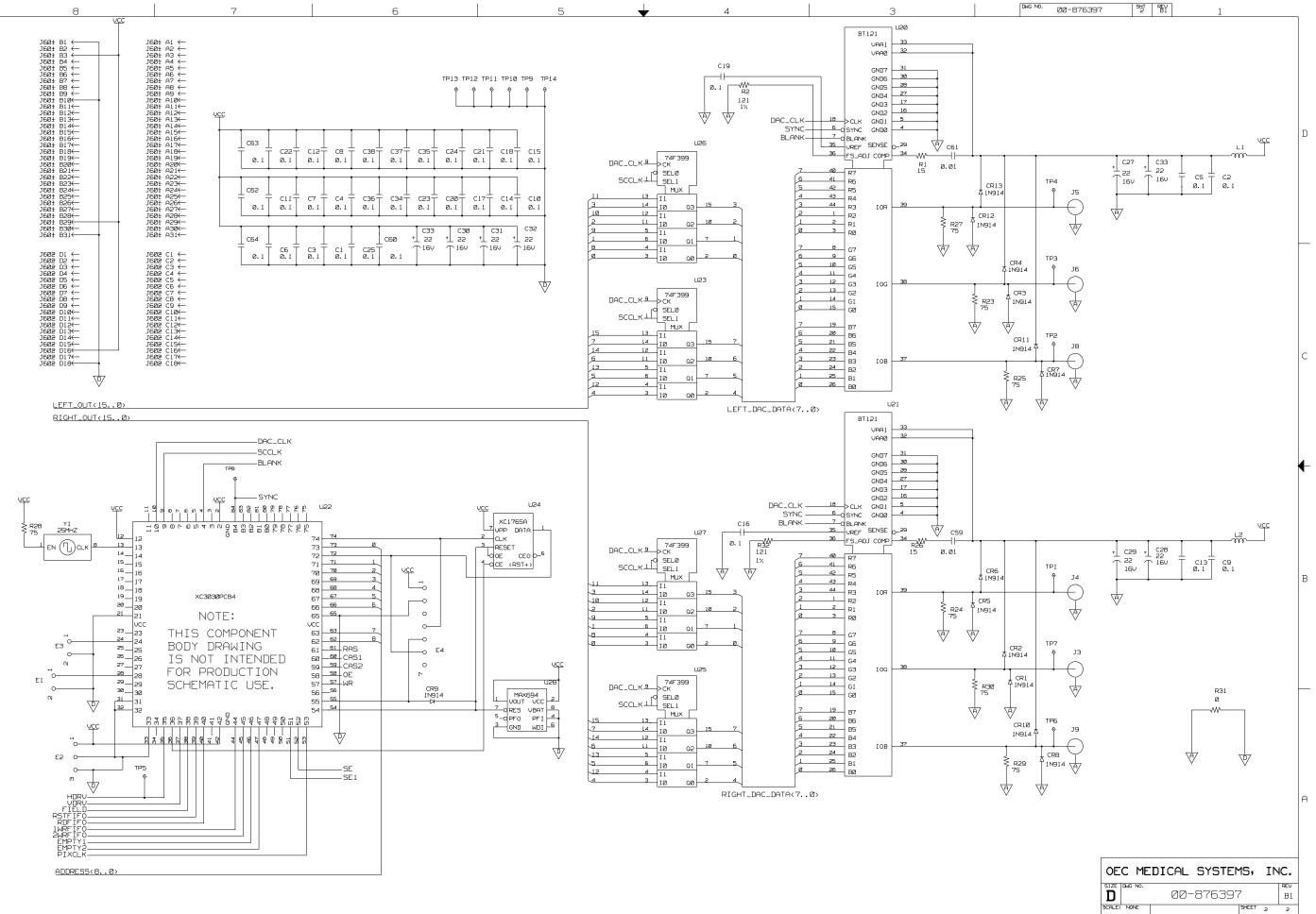
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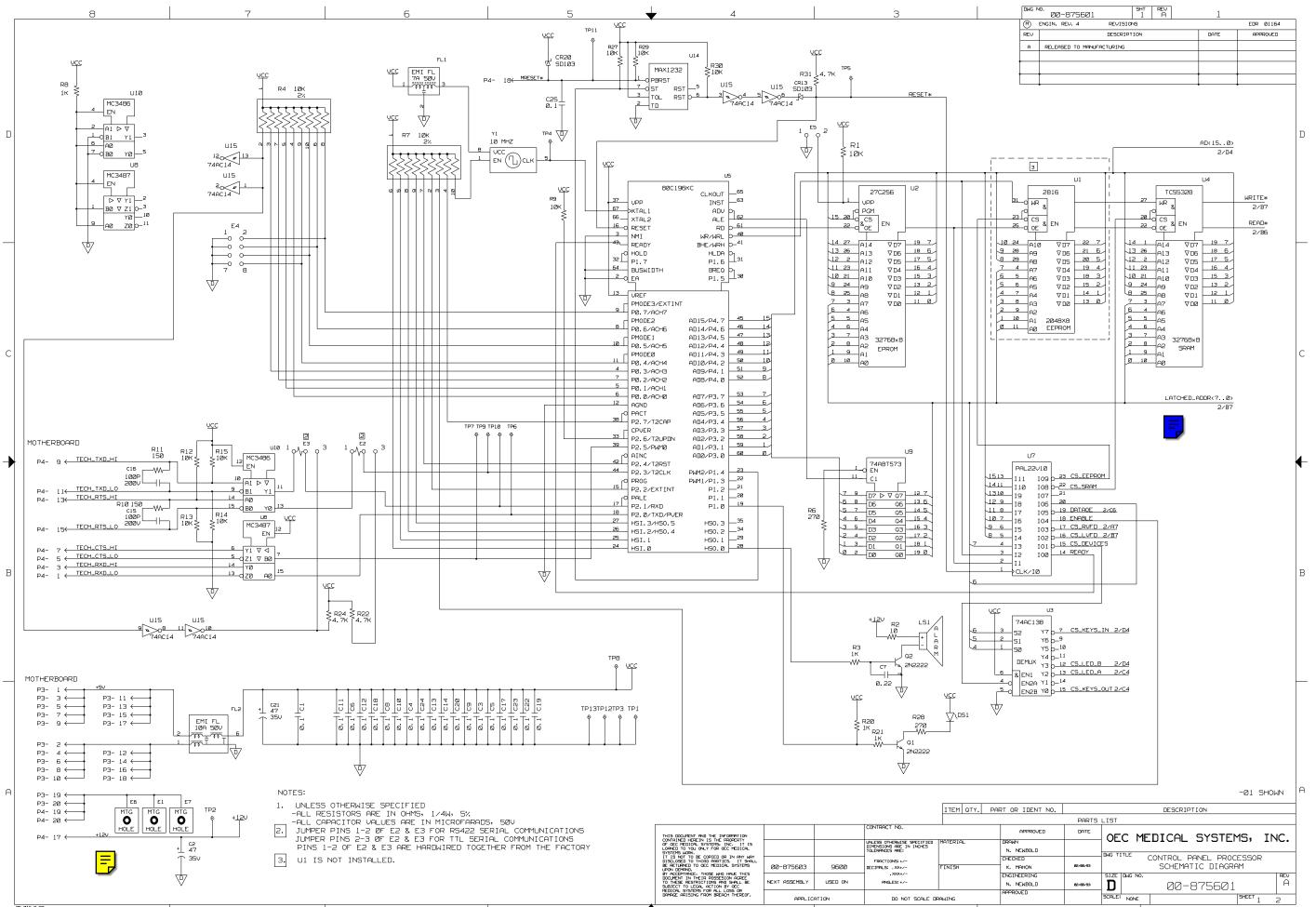


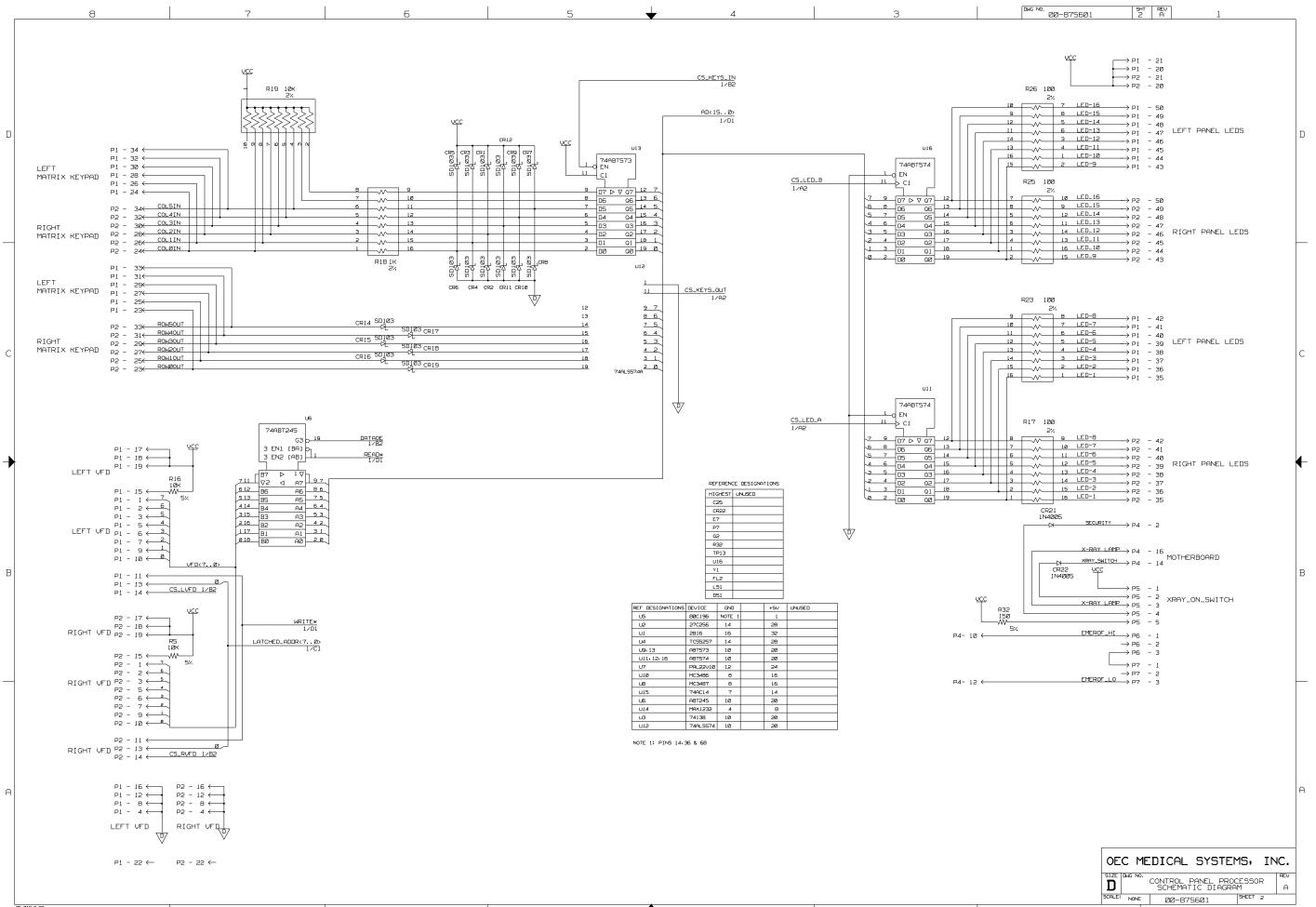
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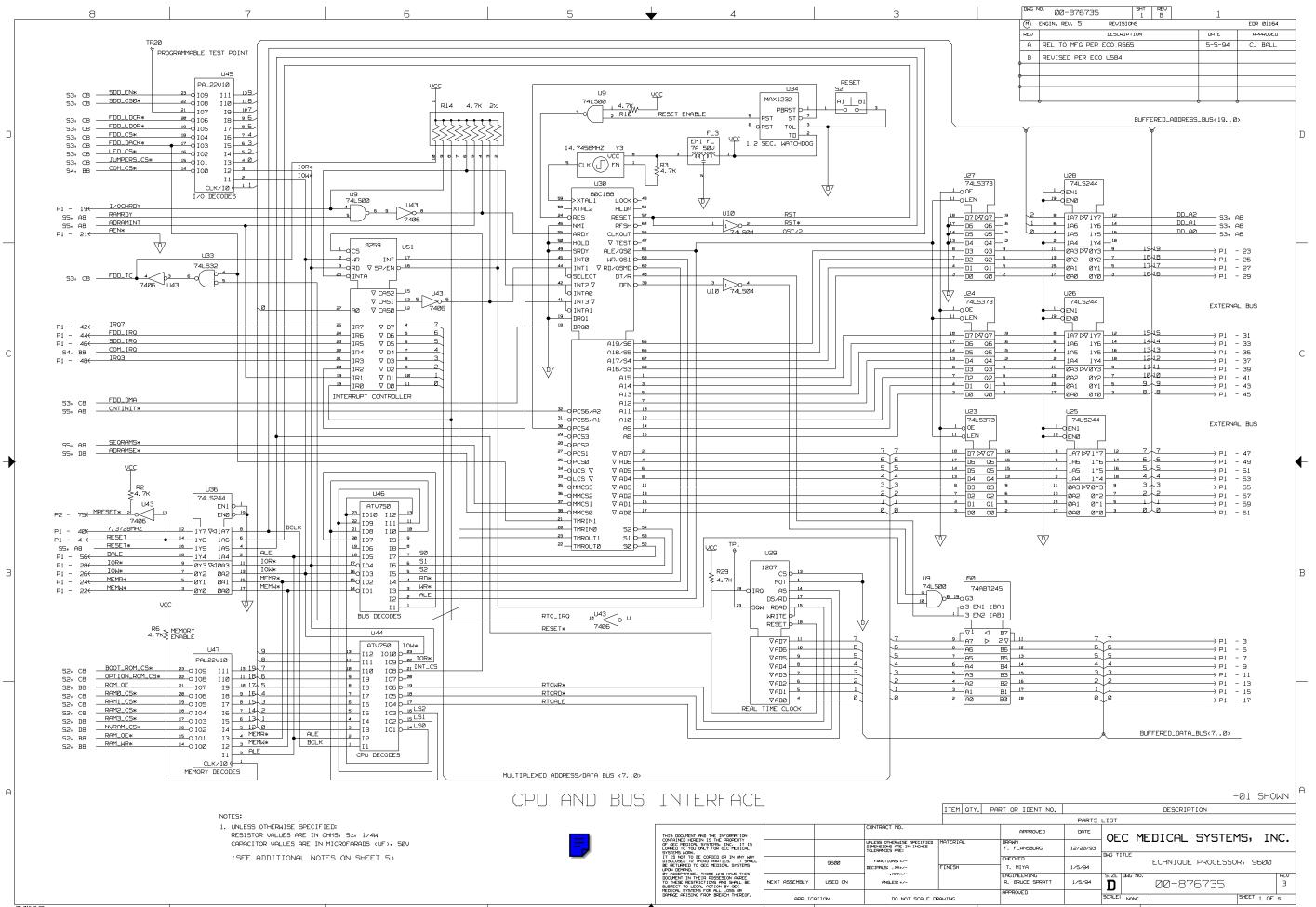
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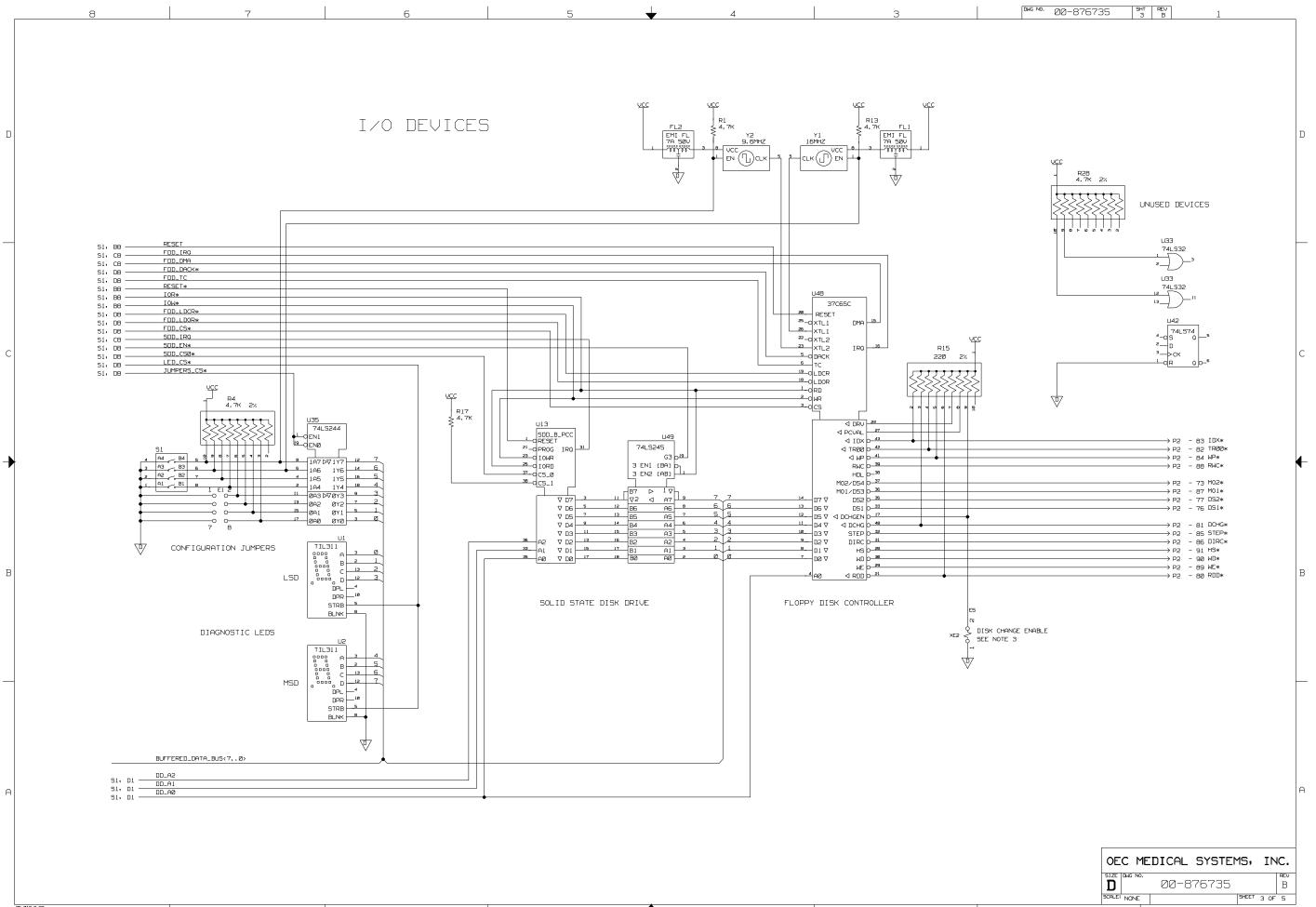
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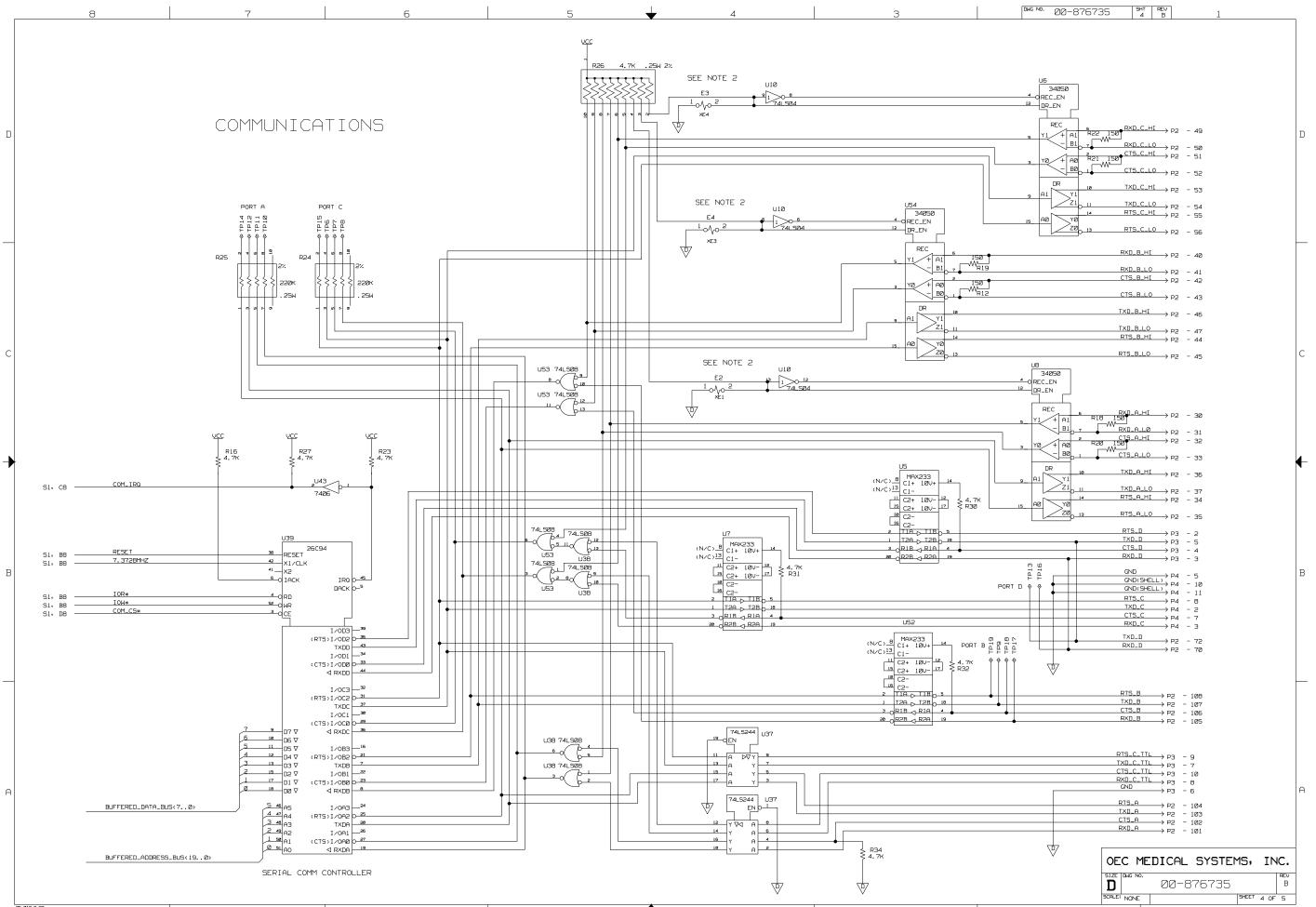
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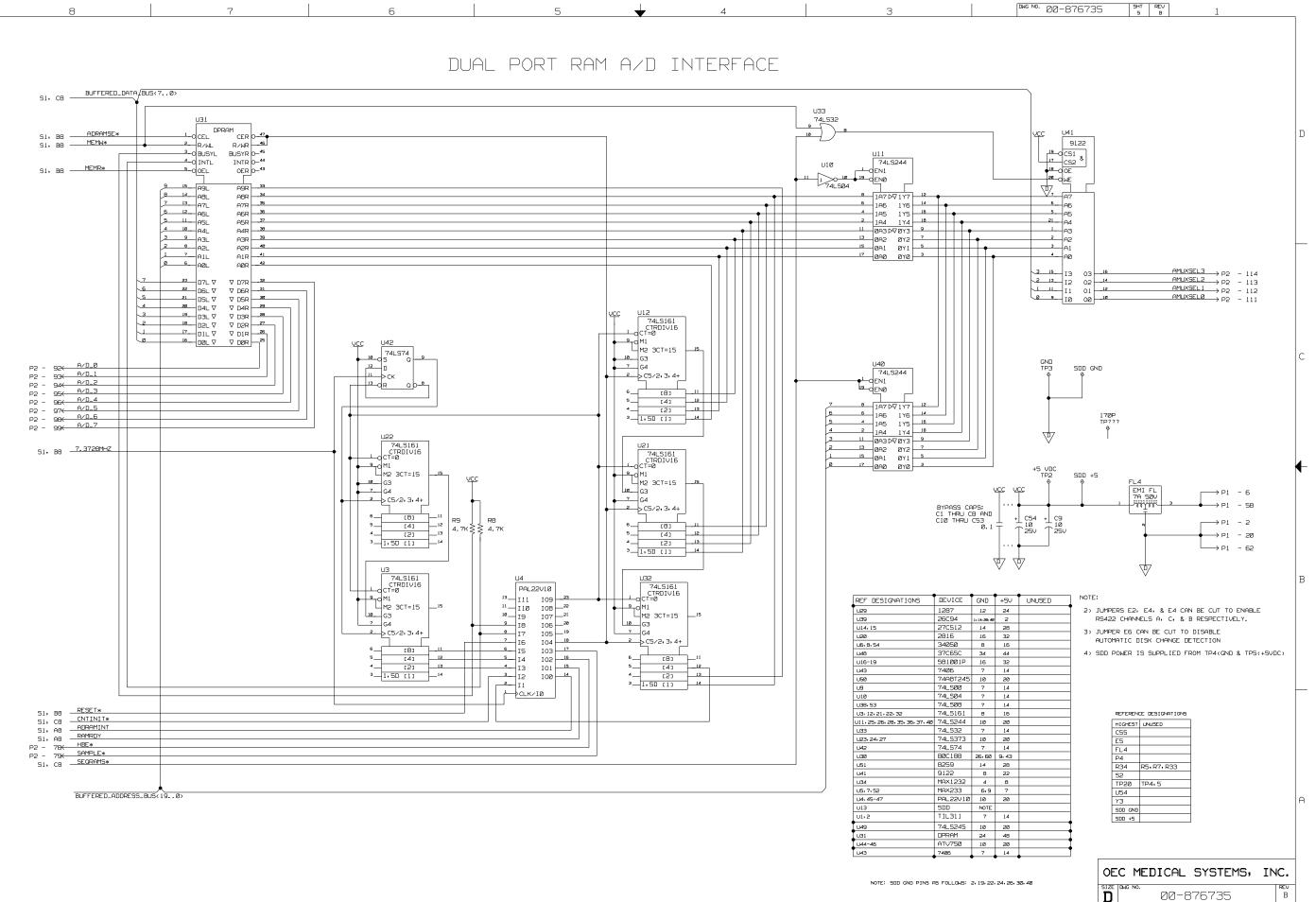
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